DSA3001

Silicon PNP epitaxial planar type

For general amplification Complementary to DSC3001 DSA9001 in SSSMini3 type package

■ Features

- High forward current transfer ratio h_{FE} with excellent linearity
- \bullet Low collector-emitter saturation voltage $V_{\text{CE}(\text{sat})}$
- Halogen-free / RoHS compliant
 (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

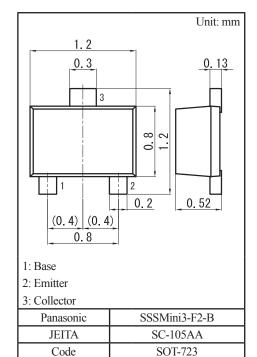
■ Marking Symbol: A1

Packaging

DSA300100L Embossed type (Thermo-compression sealing): 10 000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	-60	V	
Collector-emitter voltage (Base open)	V _{CEO}	-50	V	
Emitter-base voltage (Collector open)	V_{EBO}	-7	V	
Collector current	I_{C}	-100	mA	
Peak collector current	I_{CP}	-200	mA	
Collector power dissipation	P _C	100	mW	
Junction temperature	T_j	150	°C	
Operating ambient temperature	T _{opr}	-40 to +85	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

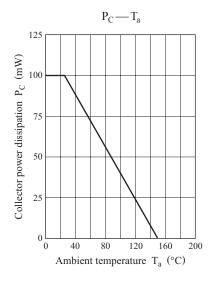


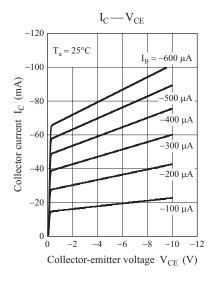
■ Electrical Characteristics $T_a = 25$ °C±3°C

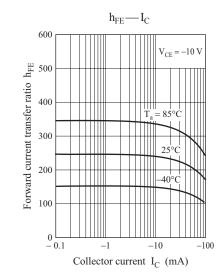
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = -10 \mu A, I_E = 0$	-60			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = -10 \mu\text{A}, I_C = 0$	-7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{\rm CB} = -20 \text{ V}, I_{\rm E} = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = -10 \text{ V}, I_{B} = 0$			-100	μΑ
Forward current transfer ratio	h_{FE}	$V_{CE} = -10 \text{ V}, I_{C} = -2 \text{ mA}$	210		460	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$		-0.2	-0.5	V
Transition frequency	f_T	$V_{CE} = -10 \text{ V}, I_{C} = -2 \text{ mA}$		150		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		2		pF

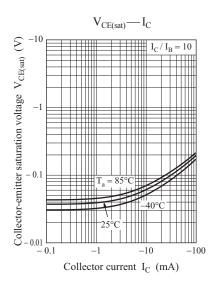
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

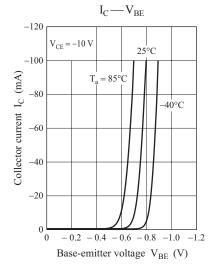
DSA3001 Panasonic

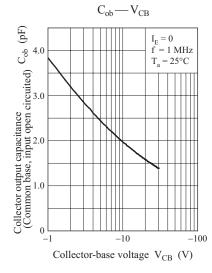


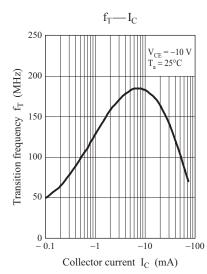








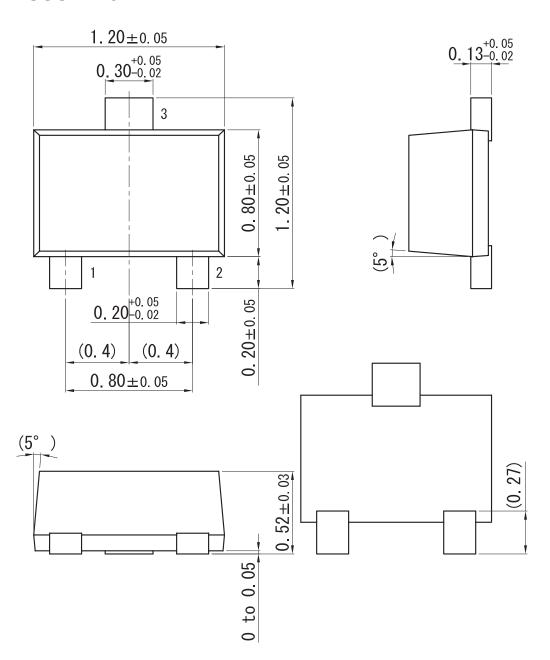




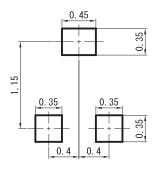
Ver. EED 2

SSSMini3-F2-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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