

Keil C166 uses leading compiler technology to generate the most efficient code for C16x, XC16x, and ST10 microcontrollers. C166 is fully integrated into the μ Vision3 IDE/Debugger that combines Project Management, Source Code Editing, Program Debugging, and Flash Programming in a single, powerful environment. This brochure informs you about:

- C166 C Compiler and ECI66 Embedded C++ Compiler.
- Advanced RTX166 Real-Time OS with TCP/IP & Flash File System.
- ULINK USB-JTAG Adapter for Debugging & Flash Programming.

Detailed information for the μ Vision3 IDE/Debugger is provided in the **μ Vision3 Quick Start** guide that explains features such as Editor Functions, Configuration Wizard, Function & Template Window, CPU & Peripheral Simulation, Logic Analyzer, Target Debugging and Flash Programming.

The screenshot displays the Keil μ Vision3 IDE interface. The main window shows a C program named 'Measure.c' with the following code:

```

242 t_getesc = os_tsk_create (GET_ESC); /* ESC check in display loop */
243 escape = 0; /* clear escape flag */
244 os_evt_clr (0x0003, t_command); /* clear pending signals */
245
246
247 while (!escape
248         os_evt_set (
249         os_evt_wait_
250         measure_disp
251         )
252
253         os_tsk_delete
254         printf ("\n\n")
255         break;
256
257 case 'S':
258     printf ("\nSta
259     os_evt_set (0x
260     bre
261
262 case '+**** REM
263 sta | This pro
264 on the i
265 voltage
266 command
267 Read
268 Display
269 Time
270 Interva
271 Clear
272 Quit
273 Start
    
```

Below the code, a Logic Analyzer window shows a waveform with a red signal and a green signal. The x-axis represents time in seconds, with markers at 0.0814692, 0.0816693, and 0.08248. A 'Serial #1' window shows the output of the program:

```

+**** REM
sta | This pro
on the i
voltage
command
Read
Display
Time
Interva
Clear
Quit
Start
    
```

At the bottom, the 'Analog/Digital Converter' window is open, showing settings for Mode (Single Ch. Conversion), ADCON (0x0003), ADCTC (Tcpu * 4), ADSTC (Tbc * 8), and ADDAT2 (0x0000). The 'Analog Input Channels' table is also visible:

Channel	Value
0	4.7000
1	3.0000
2	1.3000
3	4.2000
4	0.0000
5	0.0000
6	0.0000
7	0.0000
8	0.0000
9	0.0000
10	0.0000
11	0.0000
12	0.0000
13	0.0000
14	0.0000
15	0.0000

The bottom status bar shows the command prompt with the text: 'Display current Measurements: (ESC to abort) Time: 0:00:07.390 P2:3412 AN0:4.59V AN1:2.99V AN2:1.29V AN3:4.19V'. The bottom right corner shows a memory dump window with the following data:

```

Address: \measure\save_record
00010014: FF 00 00 00 00 00 FF 00 00
0001001E: 00 00 00 00 00 00 FF 00 00
00010028: 00 00 00 00 00 00 00 00 00
00010032: 00 00 FF 00 00 00 00 00 FF
0001003C: 00 00 00 00 00 00 00 00 FF
    
```

μ Vision3 integrates the development tools in a single, intelligent environment and supports all C16x, XC16x, and ST10 device.

The ANSI standard Keil C166 compiler is specifically designed for the XC16x, C16x, and ST10 microcontroller families. Extensions incorporated into the compiler support all resources of your embedded hardware and allow efficient access to interrupts, register banks, SFRs, and memory spaces.

Configuration Wizard

The μ Vision3 configuration wizard simplifies editing the startup code (and many other configuration files). A graphical interface helps you quickly select the options that match your target system and easily spot potential startup problems.

Embedded C++

Keil C166 Version 5 includes an efficient Embedded C++ compiler. EC++ is a new, evolving C++ standard for embedded systems that avoids the overhead involved with C++ programming. The Keil EC++ implementation includes numerous I66-specific language extensions.

In-Line Assembly

A new, updated in-line assembler enables you to insert assembly instructions directly into your program and offers:

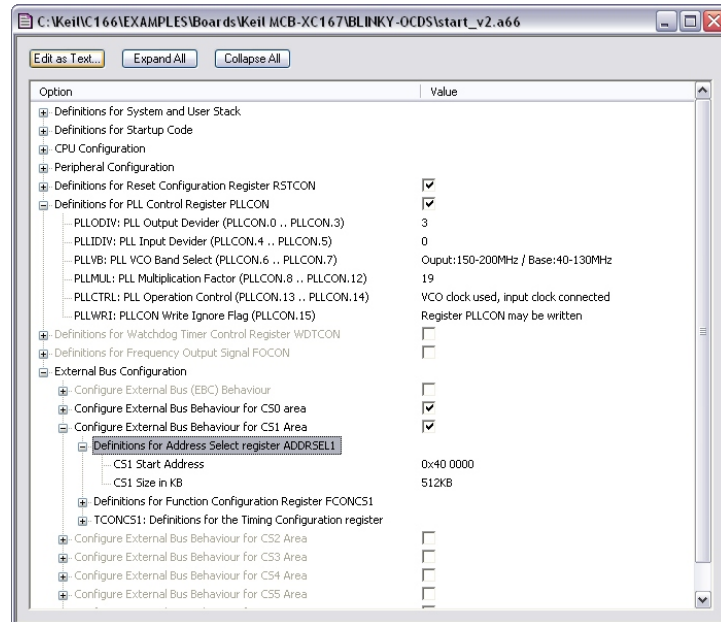
- Access to C program variables by name,
- C-level debugging information (source-level and symbolic),
- Optimized MAC instruction utilization.

Function In-lining

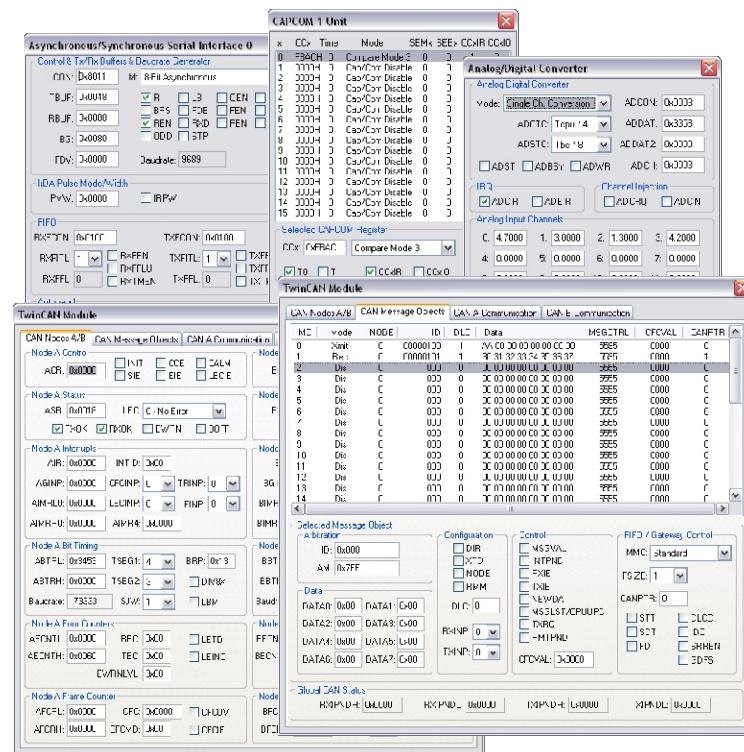
Functions defined with the `__inline` attribute are directly embedded when called (CALL and RET instructions are no longer required). Parameters are accessed directly (they are not passed in special registers or stored on the stack) by the generated code.

Accurate Device Simulation

The μ Vision3 Debugger accurately simulates all on-chip peripherals and has debug dialogs that help you rapidly test I66 applications.



The Configuration Wizard provides a graphical editor for startup files.



The μ Vision3 Debugger provides debug dialogs and accurate simulation of on-chip peripherals. A/D converters, CAN, TwinCAN, CAPCOM, ASC, SSC, PWM, PEC, Timers, Watchdog, and I/O Ports are all fully simulated.

Keil provides two Real-Time Operating Systems for the I66 family. Both support all device variants, are royalty-free, and are fully integrated into the C166 toolchain. Each RTOS uses standard I66 stacks (so no overhead is added to your C code). Neither requires a special run-time library.

RTX166 Tiny is an easy-to-use kernel that is part of the C166 package. It is designed for simple real-time programs that do not require much inter-task communication.

Advanced RTX166 (ARTX166) is a full-featured kernel with task priorities, round-robin, preemptive context switching, and support for multiple instances of the same task function. It also includes a Flash file system and TCP/IP network support.

Flash File System

The Flash File System allows you to create, save, read, and modify files stored in Flash memory. It provides file access functions (fopen, fread, fwrite, fgets, fprintf, ...) and may be configured for various Flash devices.

TCP/IP Network Support

The TCP/IP stack in ARTX166 is a ground-up implementation specifically designed for embedded applications. It includes the following protocols:

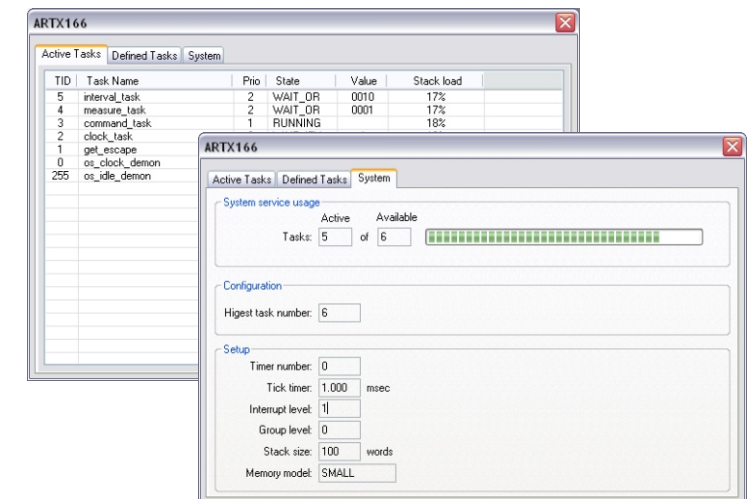
- TCP, UDP, PPP, SLIP, IP, ARP.
- HTTP Web Server and FTP Server.
- DHCP Client.

ARTX166 helps you create embedded TCP/IP solutions that connect to standard internet browsers. It is pre-configured for the Keil MCB-XC167-NET and MCB167-NET evaluation boards and includes examples you may use to quickly get started with your own internet-enabled projects.

Applications with numerous web pages, FTP files, or dynamic content may use the Flash File System to store HTML and data files. Flash System Files are added to the μ Vision3 project and automatically converted and stored in the final object file.

Advanced RTX166 Function Overview

- **Task Management** functions allow you to define and remove tasks. ARTX166 allows multiple instances of the same task.
- **Event Flag Management** for task synchronisation based on up to 16 event flags.
- **Semaphore Management** for efficient resource sharing.
- **Mailbox Management** for inter-task communication.
- **Time Management** use for task delays (interval or timeout).
- **User Timer Management** allows definition of callback functions which are called after a time delay.
- **Flash File System** functions for file-based data access in Flash ROM and RAM devices.
- **BSD Socket** functions for network communication.



ARTX166 dialogs in the μ Vision3 Debugger support kernel-aware debugging and provide detailed task and system status.

Technical Data	Advanced RTX166	RTX166 Tiny
Number of tasks	256 maximum	32 maximum
Number of mailboxes	Unlimited	Not supported
Number of semaphores	Unlimited	Not supported
Number of signals	16 Event Flags per task	1 per task
Number of user timers	Unlimited	Not supported
RAM Requirements	Minimum 2-3 KB	8 + 4 * tasks Bytes
Code requirements	Less than 4 KB	Less than 1.5KB
Hardware Requirements	One timer	One timer
User task priorities	256	Not supported
Context switch time	Less than 25 μ sec.	40-100 μ sec.
Interrupt lockout time	0.2 μ sec.	Less than 4 μ sec.

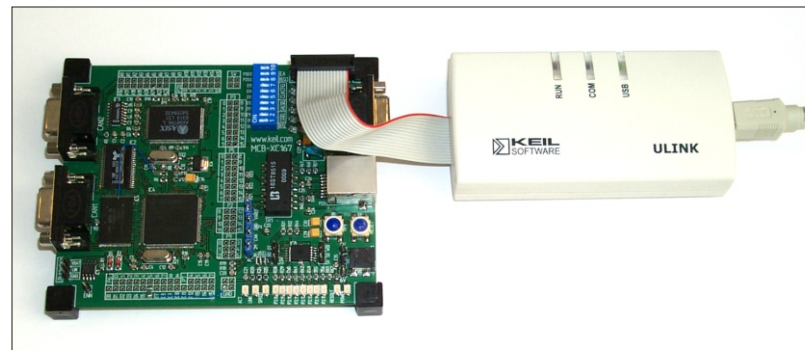
Timings are based on C167CR running at 20MHz with no waitstates.

Feature	Benefit
The C166 Compiler is highly optimized. In-line functions give you the fastest execution speed. In-line assembly allows optimized MAC instruction sequences.	There is almost no size or speed penalty when using Keil C166. DSP algorithms using the MAC unit may be directly written as C functions.
Embedded C++ Compiler includes specific compiler extensions for the I66 architecture.	EC++ allows you to write object-oriented programs while avoiding the overhead of the C++ language.
The μ Vision3 Simulator is the only debugger that accurately simulates all on-chip peripherals of the Infineon C16x/XC16x and ST10 devices.	You may write and test application code before production hardware is available. Investigate different hardware configurations to optimize the hardware design.
The μ Vision3 Device Database automatically configures the development tools for the target microcontroller.	Mistakes in tool settings are practically eliminated and tool configuration time is minimized.
The μ Vision3 IDE integrates third-party tools like SVCS, CASE, and FLASH/Device Programming.	Quickly access development tools and third-party tools. All configuration details are saved in the μ Vision3 project.
Identical Editor, Debugger, and Simulator Interfaces.	No need to learn different tool interfaces.
Advanced RTX166 is a full-featured, high-performance Real-Time Operating System that incorporates TCP/IP networking and a Flash File System.	Allows you to implement complex, time-critical applications that can be easily connected to the Internet and accessed using a standard web browser.

ULINK USB-JTAG Adapter

The ULINK USB-JTAG Adapter connects μ Vision3 to the On-Chip Debugging System (OCDS) of the XC16x, C161U, C165UTAH, or C165H device. ULINK allows you to:

- Download target programs.
- Examine memory and registers.
- Single-step through programs.
- Insert multiple breakpoints.
- Run programs in real-time.
- Program on-chip FLASH.



ULINK connects the PC USB Port to the OCDS Interface of the C16x or XC16x device (in this case the Keil MCB-XC167 NET board).

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Europe:

Keil Elektronik GmbH
Bretonischer Ring 15
85630 Grasbrunn
Germany

Phone ++49 89 / 45 60 40 - 0
Support ++49 89 / 45 60 40 - 24
FAX ++49 89 / 46 81 62
Email sales.intl@keil.com
support.intl@keil.com

United States:

Keil Software, Inc.
1501 10th Street, Suite 110
Plano, Texas 75074
USA

Phone 800-348-8051
972-312-1107
FAX 972-312-1159
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