

SK8603170L

Silicon N-channel MOS FET

For Load-switching / For DC-DC Converter

■ Features

- Low Drain-source On-state Resistance : $R_{DS(on)typ} = 3.9 \text{ m}\Omega$ ($V_{GS} = 4.5 \text{ V}$)
- Halogen-free / RoHS compliant
 (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)

■ Marking Symbol : 17

■ Packaging

Embossed type (Thermo-compression sealing) : 3 000 pcs / reel (standard)

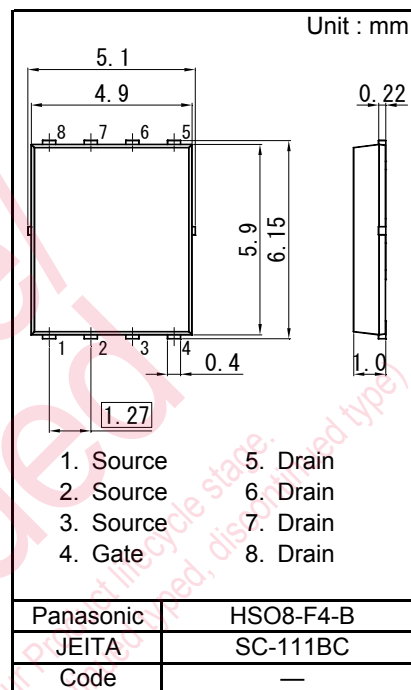
■ Absolute Maximum Ratings $T_a = 25 \text{ }^\circ\text{C}$

Parameter	Symbol	Rating	Unit	
Drain to Source Voltage	VDS	30	V	
Gate to Source Voltage	VGS	± 20		
Drain Current	ID	$T_a = 25 \text{ }^\circ\text{C}, t = 10 \text{ s}^{-1}$	28	A
		$T_a = 25 \text{ }^\circ\text{C}, DC$ *1	20	
		$T_c = 25 \text{ }^\circ\text{C}$	59	
		Pulsed, $T_{ch} < 150 \text{ }^\circ\text{C}$ *2	84	
Total Power Dissipation	PD	$T_a = 25 \text{ }^\circ\text{C}, DC$ *1	2.8	W
		$T_c = 25 \text{ }^\circ\text{C}$	24	
Thermal Resistance	Channel to Ambient	Rth(ch-a)	44	$^\circ\text{C} / \text{W}$
	Channel to Case	Rth(ch-c)	5.1	
Channel Temperature	Tch	150	$^\circ\text{C}$	
Operating ambient temperature	Topr	-40 to +85		
Storage Temperature Range	Tstg	-55 to +150		
Avalanche Current (Single pulse) *3	IAR	14	A	
Avalanche Energy (Single pulse) *3	EAR	24	mJ	

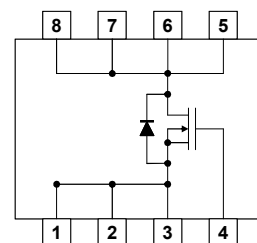
Note *1 Device mounted on a glass-epoxy board in Figure 1

*2 Pulse test: Ensure that the channel temperature does not exceed $150 \text{ }^\circ\text{C}$

*3 $V_{DD} = 24 \text{ V}, V_{GS} = 10 \text{ to } 0 \text{ V}, L = 0.1 \text{ mH}, T_{ch} = 25 \text{ }^\circ\text{C}$ (initial)



Internal Connection



Pin Name

- | | |
|-----------|----------|
| 1. Source | 5. Drain |
| 2. Source | 6. Drain |
| 3. Source | 7. Drain |
| 4. Gate | 8. Drain |

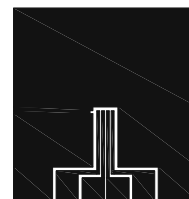


Figure 1 FR4 Glass-Epoxy Board
 25.4 mm × 25.4 mm × 0.8 mm

■ Electrical Characteristics Ta = 25 °C ± 3 °C

Static Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	30			V
Zero Gate Voltage Drain Current	IDSS	VDS = 30 V, VGS = 0 V			10	μA
Gate-source Leakage Current	IGSS	VGS = ±16 V, VDS = 0 V			±10	μA
Gate-source Threshold Voltage	Vth	ID = 2.56 mA, VDS = 10 V	1.3		3	V
Drain-source On-state Resistance	RDS(on)1	ID = 14 A, VGS = 10 V		2.9	4.1	mΩ
	RDS(on)2	ID = 14 A, VGS = 4.5 V		3.9	5.8	

Dynamic Characteristics

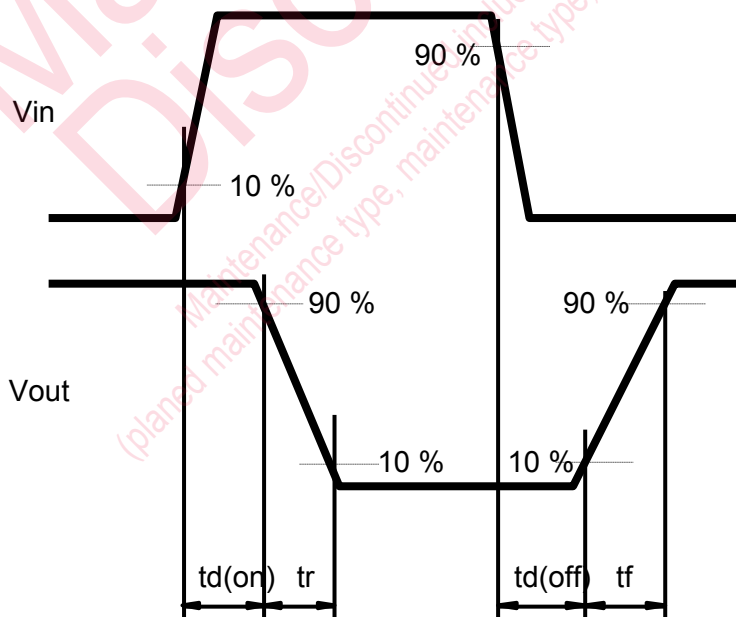
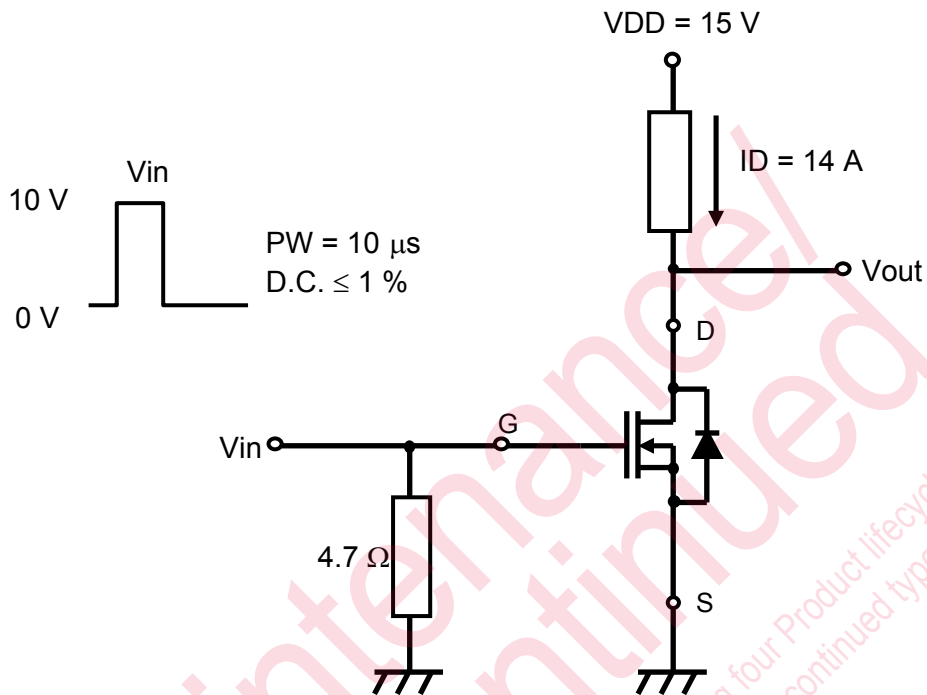
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input Capacitance	Ciss	VDS = 10 V, VGS = 0 V f = 1 MHz		2 100	2 940	pF
Output Capacitance	Coss			250	350	
Reverse Transfer Capacitance	Crss			180	290	
Turn-on Delay Time*1	td(on)	VDD = 15 V, VGS = 0 to 10 V		11		ns
Rise Time*1	tr	ID = 14 A		10		
Turn-off Delay Time*1	td(off)	VDD = 15 V, VGS = 10 to 0 V		48		ns
Fall Time*1	tf	ID = 14 A		7		
Total Gate Charge	Qg	VDD = 15 V, VGS = 0 to 4.5 V ID = 14 A		17		nC
Gate to Source Charge	Qgs			6		
Gate to Drain Charge	Qgd			7		
Gate resistance	rg	f = 5 MHz		1.2	3	Ω

Body Diode Characteristic

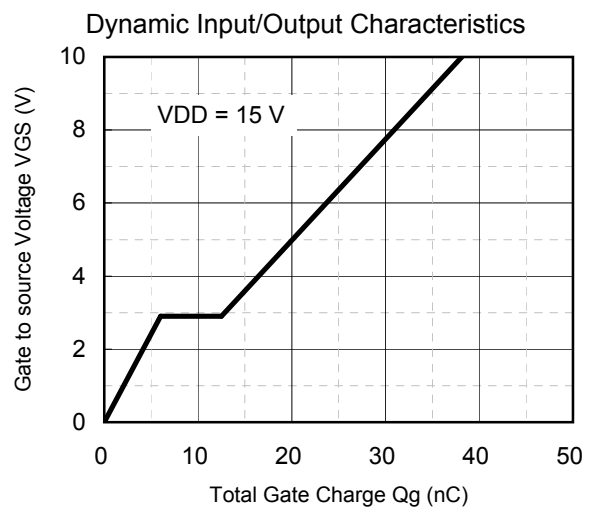
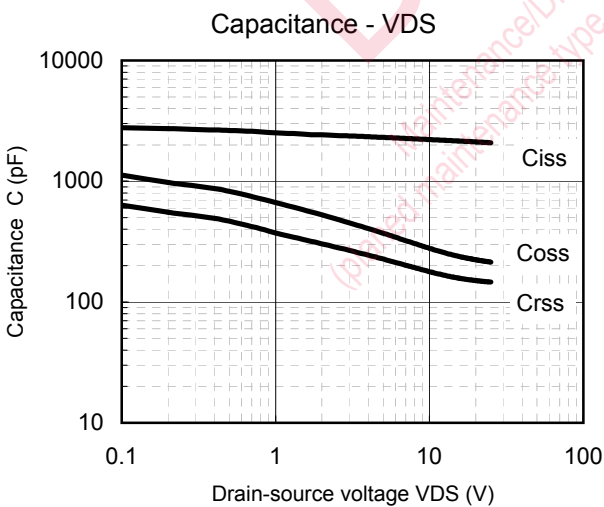
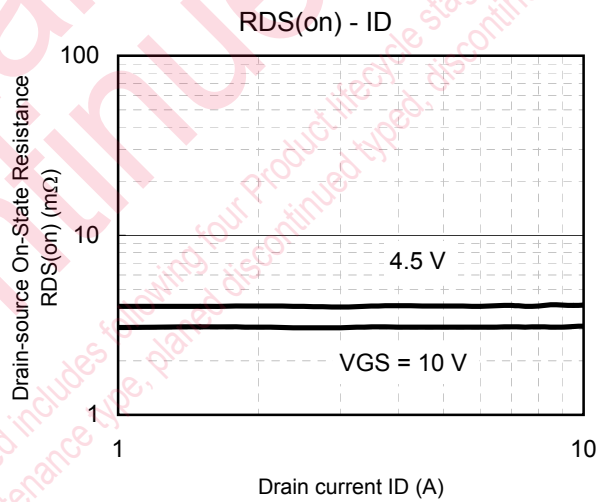
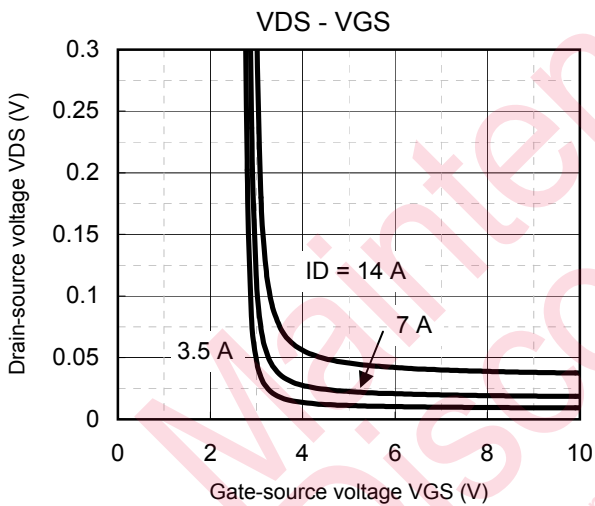
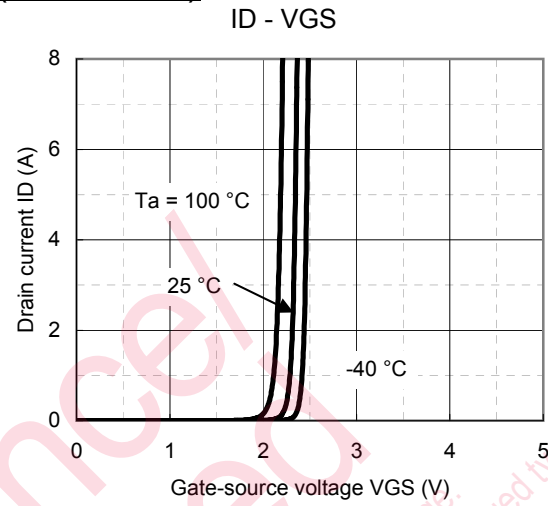
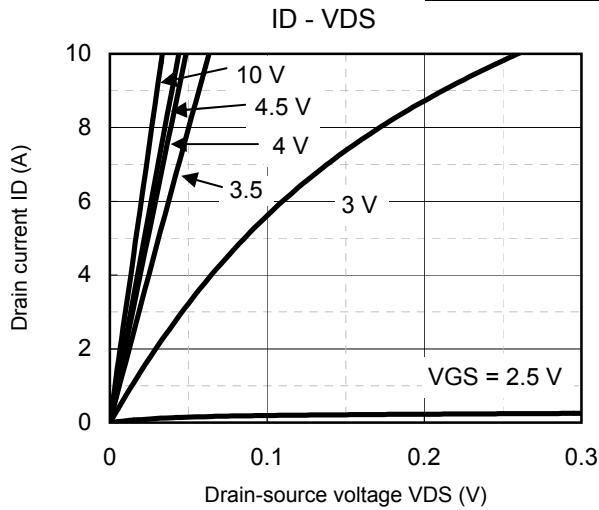
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage	VSD	IS = 14 A, VGS = 0 V		0.8	1.2	V

Note : 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.
2. *1 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time

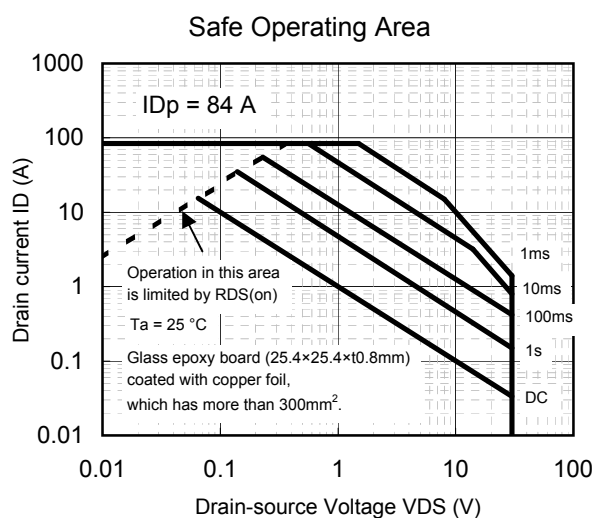
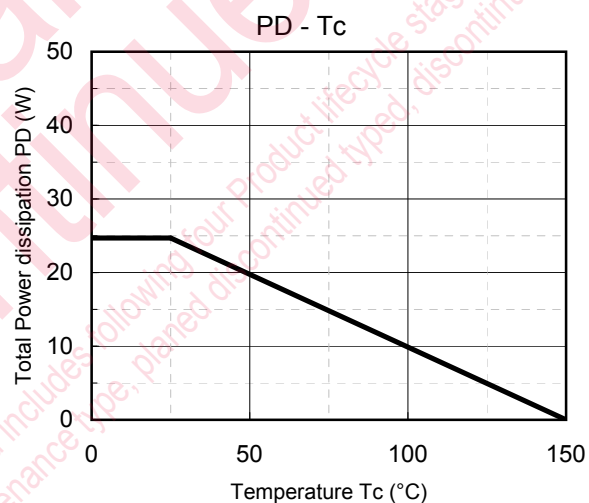
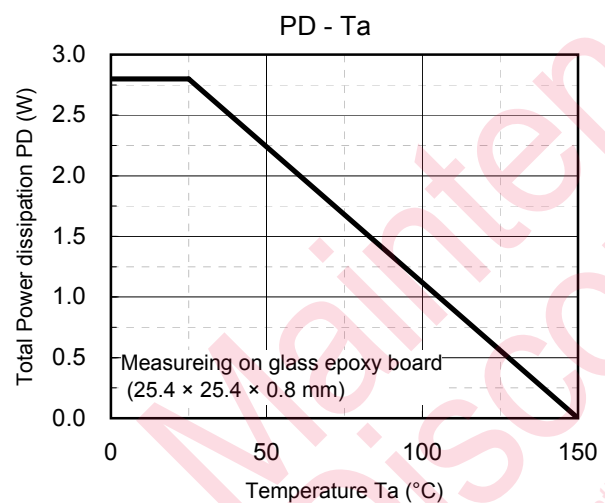
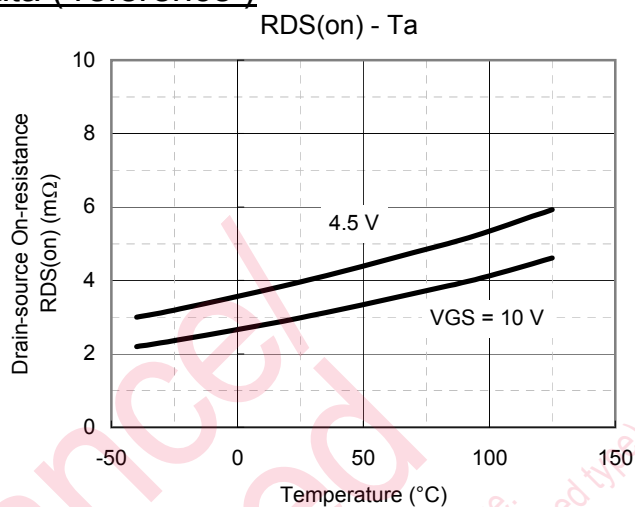
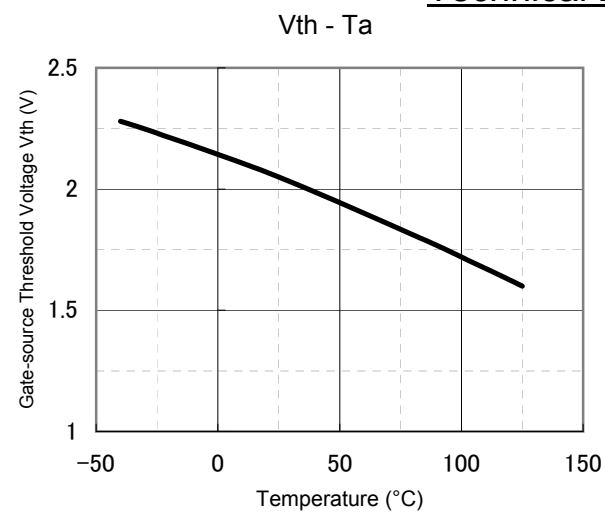
*1 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time



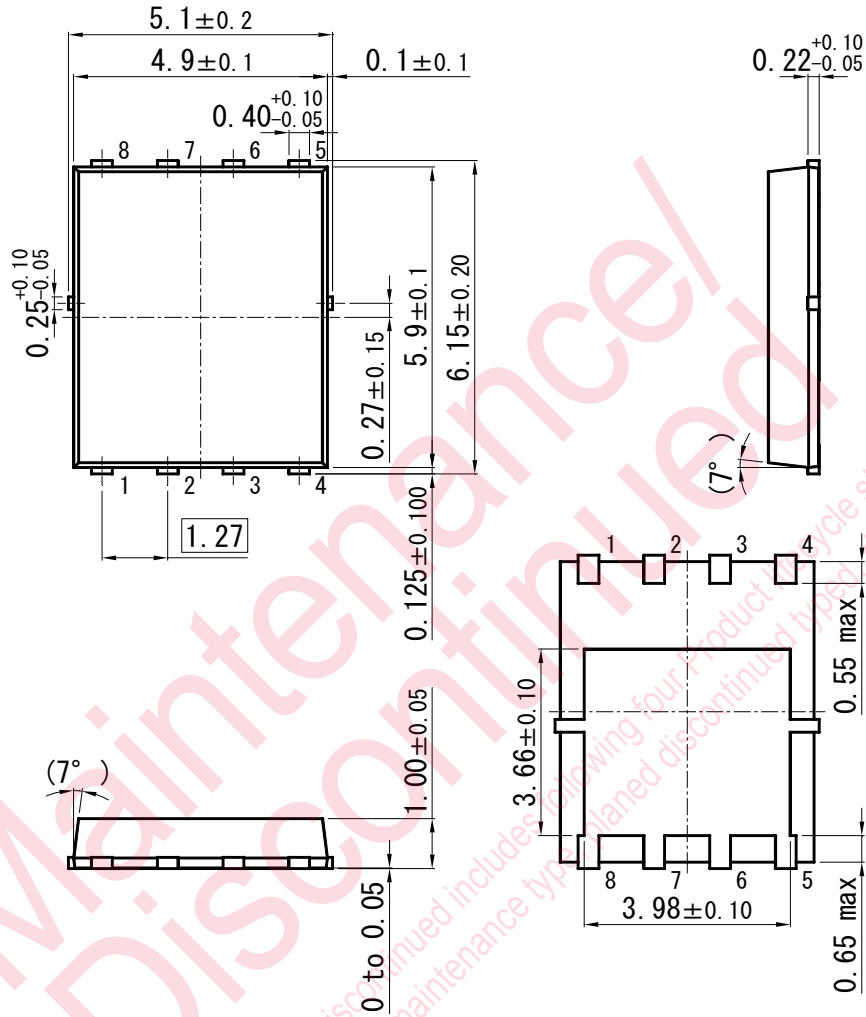
Technical Data (reference)



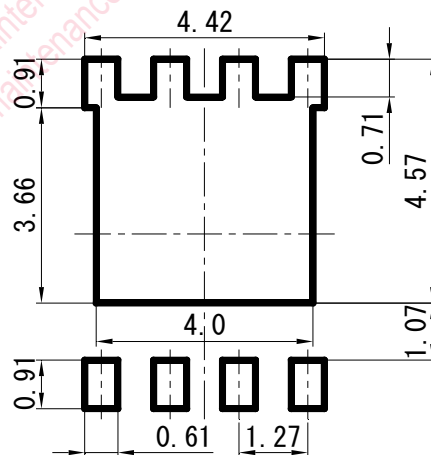
Technical Data (reference)



HSO8-F4-B



■ Land Pattern (Reference) (Unit : mm)



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