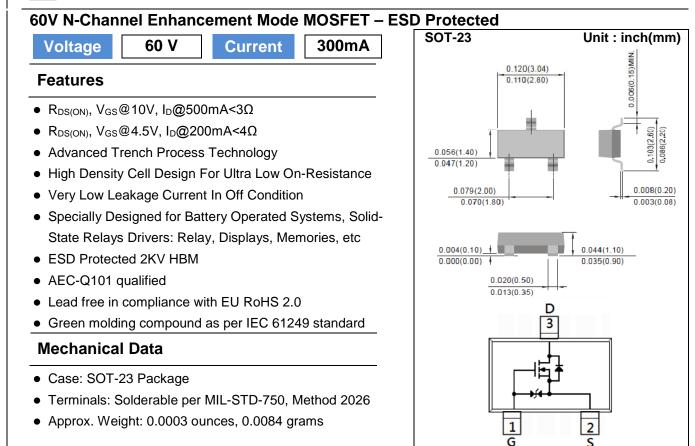
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Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	60	
Gate-Source Voltage	V _{GS}	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	lь	300	mA	
Pulsed Drain Current (Note 1)	ldм	2000		
	T _A =25°C	PD	500	mW
Power Dissipation	Derate above 25°C		4	mW/°C
Operating Junction and Storage Te	TJ,TSTG	-55~150	٥C	
Typical Thermal Resistance	R _{0JA}	250	°C/W	
- Junction to Ambient (Note 3,4)				

ς



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static	1	1			1	1
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =10uA	60	-	-	v
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1	-	2.5	v
Drain Course On State Desistance	D	V _{GS} =10V,I _D =500mA	-	-	3	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =200mA	-	-	4	Ω
Zero Gate Voltage Drain Current	IDSS	V _{DS} =60V,V _{GS} =0V	-	-	1	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 10	uA
Forward Transconductance	g fs	V _{DS} =15V, I _D =250mA	100	-	-	mS
Dynamic ^(Note 5)						
Total Gate Charge	Qg		-	0.8	-	
Gate-Source Charge	Q _{gs}	$V_{DS}=15V, I_{D}=250mA,$	-	0.35	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =5V ^(Note 1,2)	-	0.2	-	
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V,	-	35	-	
Output Capacitance	Coss		-	13	-	pF
Reverse Transfer Capacitance	Crss	f=1MHZ	-	8	-	
Turn-On Delay Time	td _(on)	$V_{DD}=30V, I_{D}=200mA,$ $V_{GS}=10V,$ $R_{G}=10\Omega$ (Note 1,2)	-	2.7	-	
Turn-On Rise Time	tr		-	19	-	
Turn-Off Delay Time	td _(off)		-	15	-	ns
Turn-Off Fall Time	tf	$\prod_{i=1}^{n} \prod_{j=1}^{n} \prod_{i=1}^{n} \prod_{j=1}^{n} \prod_{j=1}^{n} \prod_{j=1}^{n} \prod_{i=1}^{n} \prod_{j=1}^{n} \prod_{j$	-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	300	mA
Diode Forward Voltage	V _{SD}	Is=200mA, V _{GS} =0V	-	0.82	1.3	V

NOTES:

1. Pulse width<300us, Duty cycle<2%.

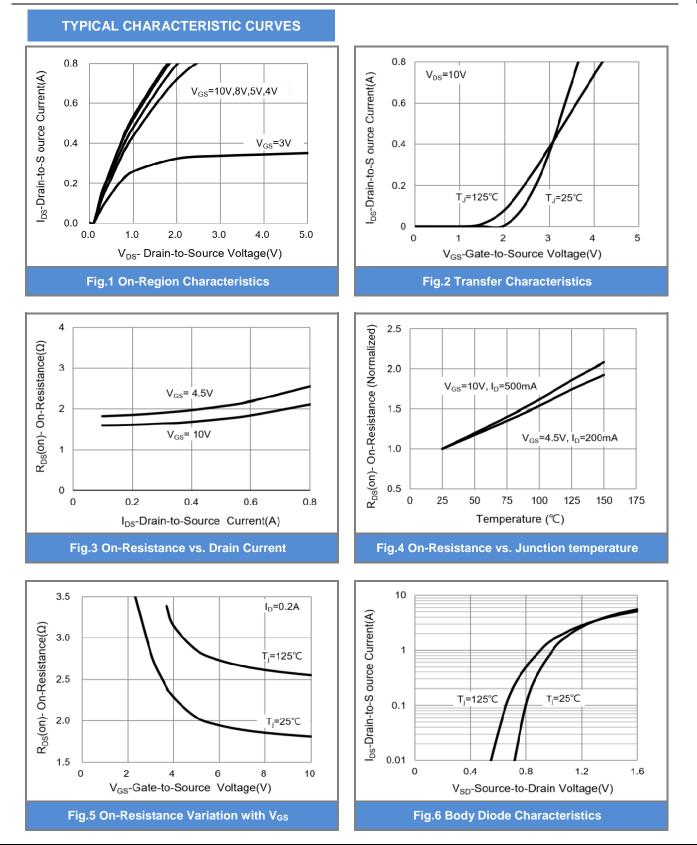
2. Essentially independent of operating temperature typical characteristics.

3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.

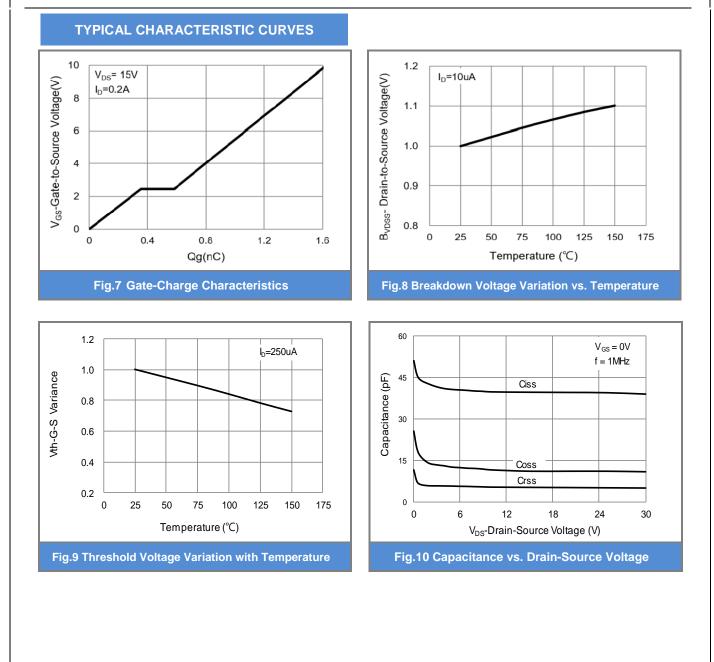
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

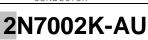
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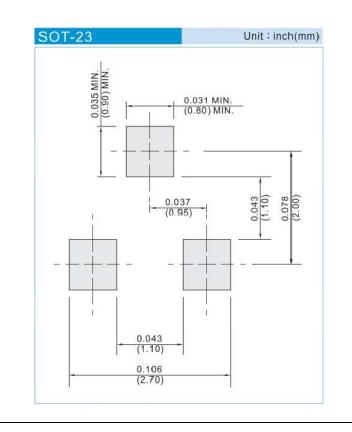




Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
2N7002K-AU_R1_000A2	SOT-23	3K pcs / 7" reel	K72	Halogen free

Mounting Pad Layout





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