



### 60V N-Channel Enhancement Mode MOSFET - ESD Protected

Voltage

60 V

Current

250mA

#### **Features**

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@500mA<3\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_{D}@200mA<4\Omega$
- Advanced Trench Process Technology
- High Density Cell Design For Ultra Low On-Resistance
- Very Low Leakage Current In Off Condition
- Specially Designed for Battery Operated Systems, Solid-State Relays Drivers: Relay, Displays, Memories, etc
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

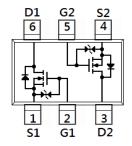
• Case: SOT-363 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.006 grams

#### SOT-363





## **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>G</sub> s	<u>+</u> 20		
Continuous Drain Current		I <sub>D</sub>	250	mA	
Pulsed Drain Current		I <sub>DM</sub>	1000		
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	350	mW	
	Derate above 25°C		4	mW/°C	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		Reja	357	°C/W	





# **Electrical Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static			_				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =10uA	60	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	-	2.5		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =500mA	-	-	3	Ω	
		V <sub>GS</sub> =4.5V,I <sub>D</sub> =200mA	-	-	4		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	uA	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 10		
Forward Transconductance	<b>G</b> fs	V <sub>DS</sub> =15V, I <sub>D</sub> =250mA	100	-	-	mS	
Dynamic <sup>(Note 5)</sup>							
Total Gate Charge	$Q_g$	\/ 45\/   050m A	-	0.8	-	nC	
Gate-Source Charge	$Q_gs$	V <sub>DS</sub> =15V, I <sub>D</sub> =250mA, V <sub>GS</sub> =5V <sup>(Note 1,2)</sup>	-	0.35	-		
Gate-Drain Charge	$Q_gd$	VGS=3V(Note 1,2)	-	0.2	-		
Input Capacitance	Ciss	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	35	-	pF	
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHZ	-	13	-		
Reverse Transfer Capacitance	Crss	I=IIVIMZ	-	8	-		
Turn-On Delay Time	td <sub>(on)</sub>	\/ 00\/   000··· A	-	2.7	-	ns	
Turn-On Rise Time	tr	V <sub>DD</sub> =30V, I <sub>D</sub> =200mA,	-	19	-		
Turn-Off Delay Time	td <sub>(off)</sub>	V <sub>GS</sub> =10V,	-	15	-		
Turn-Off Fall Time	tf	$R_G=10\Omega^{(Note\ 1,2)}$	-	23	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	l.		-	-	250	mA	
Diode Forward Current	I <sub>S</sub>						
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> =200mA, V <sub>GS</sub> =0V	-	0.82	1.3	V	

#### NOTES:

- 1. Pulse width<a></a>300us, Duty cycle<a></a>2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





#### TYPICAL CHARACTERISTIC CURVES

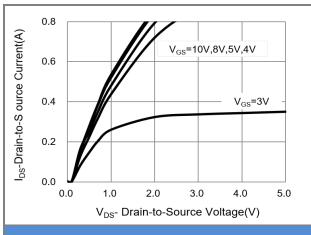
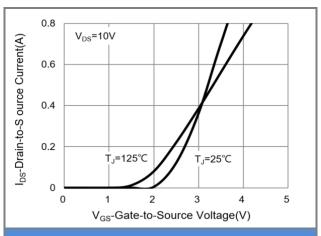


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

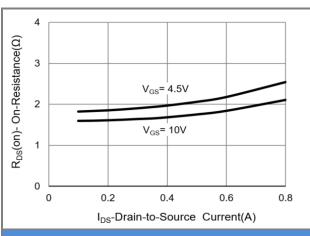


Fig.3 On-Resistance vs. Drain Current

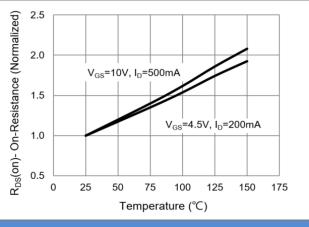


Fig.4 On-Resistance vs. Junction temperature

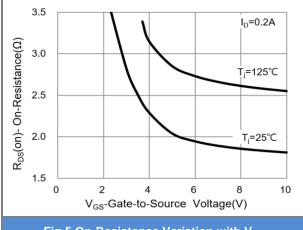
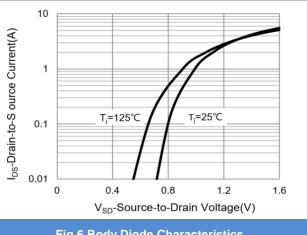


Fig.5 On-Resistance Variation with V<sub>GS</sub>



**Fig.6 Body Diode Characteristics** 





#### **TYPICAL CHARACTERISTIC CURVES**

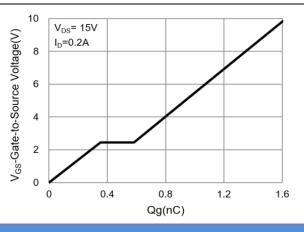


Fig.7 Gate-Charge Characteristics

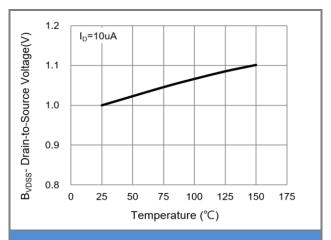


Fig.8 Breakdown Voltage Variation vs. Temperature

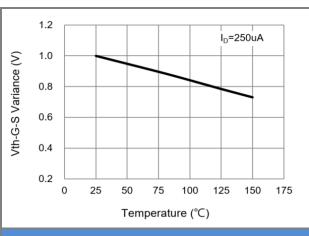


Fig.9 Threshold Voltage Variation with Temperature

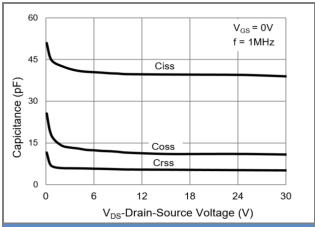


Fig.10 Capacitance vs. Drain-Source Voltage

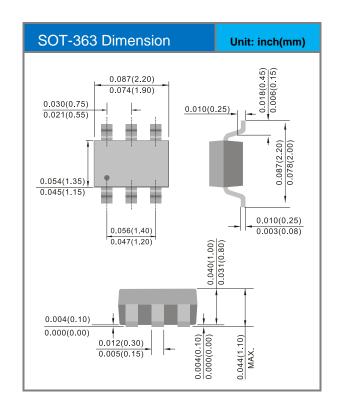


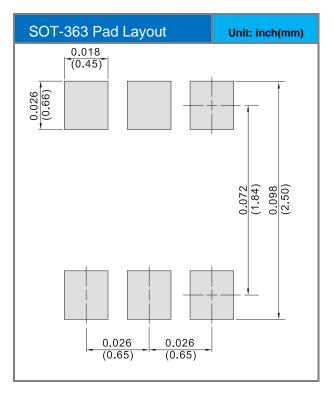


## Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
2N7002KDW_R1_00501	SOT-363	3K pcs / 7" reel	K27	Halogen free RoHS compliant

### **Packaging Information & Mounting Pad Layout**









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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
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