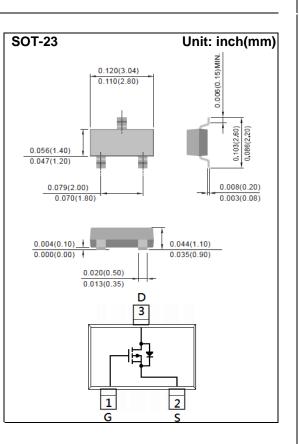


- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams
- Marking : A41



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-40	V
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	-3.1	А
Pulsed Drain Current (Note 4)		I _{DM}	-12.4	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		$R_{ extsf{ heta}JA}$	100	°C/W





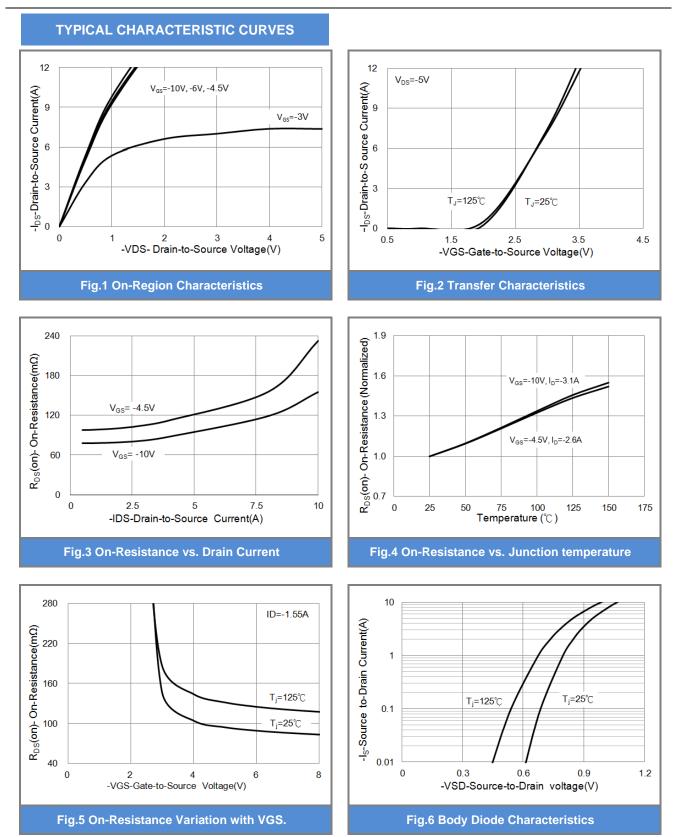
Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

	0/450			7.0		
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			Ι		[
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V, I_{D}=-250uA$	-40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}, I_{D}=-250uA$	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =-10V, I _D =-3.1A	-	74	88	mΩ
		V _{GS} =-4.5V, I _D =-2.6A	-	88	108	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_{g}	V_{DS} =-20V, I _D =-3.1A, V_{GS} =-4.5V ^(Note 1,2)	-	6	-	nC
Gate-Source Charge	Q_gs		-	1.6	-	
Gate-Drain Charge	Q_gd		-	2.3	-	
Input Capacitance	Ciss	V _{DS} =-20V, V _{GS} =0V, f=1.0MHZ	-	505	-	pF
Output Capacitance	Coss		-	48	-	
Reverse Transfer Capacitance	Crss		-	33	-	
Turn-On Delay Time	td _(on)		-	6	-	ns
Turn-On Rise Time	tr	V_{DD} =-20V, I_{D} =-2.5A, V_{GS} =-10V, R_{G} =1 Ω ^(Note 1.2)	-	35	-	
Turn-Off Delay Time	td _(off)		-	18	-	
Turn-Off Fall Time	tf	$R_{G}=1\Omega$	-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					4.0	•
Diode Forward Current	I _S		-	-	-1.0	A
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.82	-1.2	V
Reverse Recovery Time	trr	V _{GS} =0V, I _S =-2.5A dI _F / dt=100A/us	-	13	-	ns
Reverse Recovery Charge	Qrr		-	8.7	-	nC

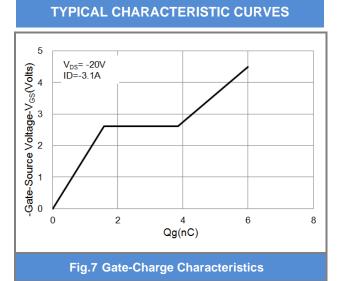
NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.









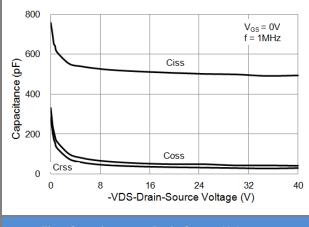
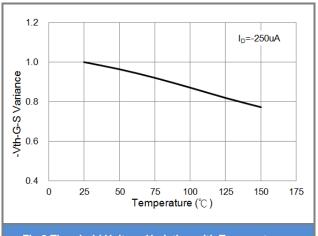


Fig.9 Capacitance vs. Drain-Source Voltage.





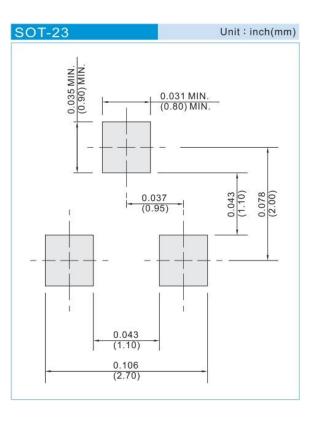




PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJA3441_R1_00001	SOT-23	3K pcs / 7" reel	A41	Halogen free
PJA3441_R2_00001	SOT-23	12K pcs / 13" reel	A41	Halogen free

MOUNTING PAD LAYOUT







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