



100V P-Channel Enhancement Mode MOSFET

Voltage -100 V Current -0.9 A

Features

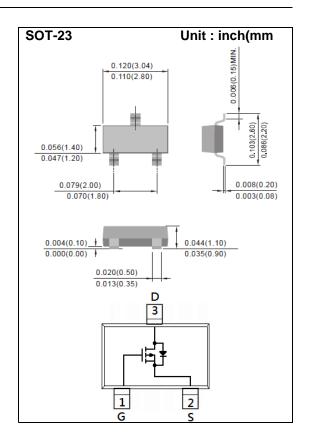
- R_{DS(ON)}, VGS@-10V, I_D@-0.9A<650mΩ
- R_{DS(ON)}, VGS@-4.5V, I_D@-0.45A<700mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	-100	V	
Gate-Source Voltage		V _G s	<u>+</u> 20		
Continuous Drain Current (Note 4)	T _A =25°C		-0.9		
	T _A =70°C	I _D	-0.75	Α	
Pulsed Drain Current (Note 1)		I _{DM}	-3.6]	
Power Dissipation	T _A =25°C		1.25	W	
	T _A =70°C	P _D	0.8		
Single Pulse Avalanche Energy (Note 6)		Eas	0.2	mJ	
Operating Junction and Storage Temperature Range		T_{J},T_{STG}	-55~150	°C	
Typical Thermal resistance - Junction to Ambient (Note 4,5)		Reja	100	°C/W	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1	-2	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-0.9A	-	500	650	mΩ
		V _{GS} =-4.5V, I _D =-0.45A	-	560	700	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-80V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	V _{DS} =-50V, I _D =-1A, V _{GS} =-10V (Note 2,3)	-	8	-	nC
Gate-Source Charge	Q_gs		-	1.8	-	
Gate-Drain Charge	Q_gd		-	1.4	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1MHZ	-	448	-	pF
Output Capacitance	Coss		-	28	-	
Reverse Transfer Capacitance	Crss		-	21	-	
Turn-On Delay Time	td _(on)	V_{DS} =-50V, I_{D} =1A, V_{GS} =-10V, R_{G} =6.2 Ω (Note 2,3)	-	3.7	-	ns
Turn-On Rise Time	tr		-	25	-	
Turn-Off Delay Time	td _(off)		-	21	-	
Turn-Off Fall Time	tf		-	22	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-1.5	А
Diode Forward Current	Is					
Diode Forward Voltage	V_{SD}	I _S =-1A, V _G S=0V	-	-0.82	-1.2	V

NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. The test condition is L=0.1mH, I_{AS}=-2A, V_{DD}=-25V, V_{GS}=-10V
- 6. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 7. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

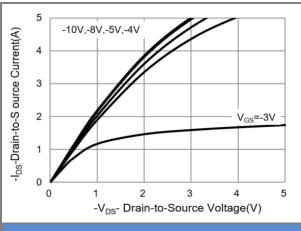


Fig.1 On-Region Characteristics

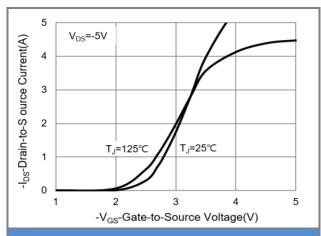


Fig.2 Transfer Characteristics

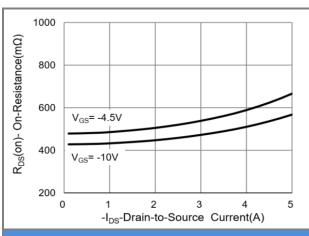


Fig.3 On-Resistance vs. Drain Current

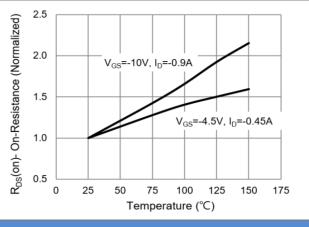


Fig.4 On-Resistance vs. Junction temperature

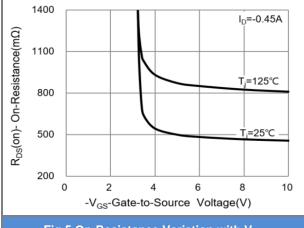


Fig.5 On-Resistance Variation with V_{GS}

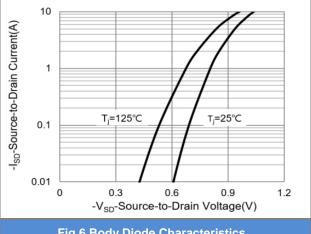


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

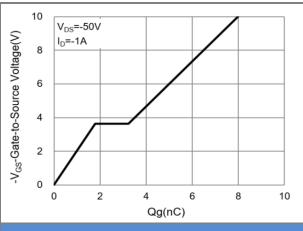


Fig.7 Gate-Charge Characteristics

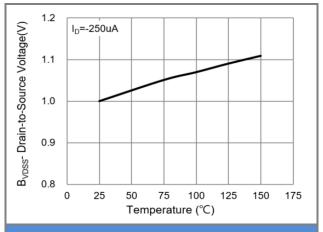


Fig.8 Breakdown Voltage Variation vs. Temperature

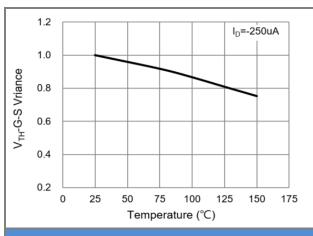


Fig.9 Threshold Voltage Variation with Temperature

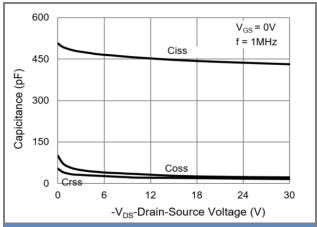


Fig.10 Capacitance vs. Drain-Source Voltage

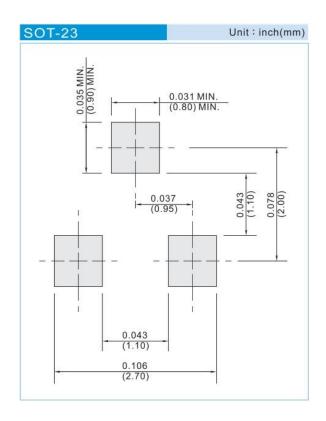




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3471_R1_00001	SOT-23	3K pcs / 7" reel	A71	Halogen free

Mounting Pad Layout







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