## PJE5V0U8TB-AU

## Ultra Low Capacitance ESD Protection

## $\mathrm{V}_{\text {RwM }} \quad 5 \mathrm{~V}$

## Features

- IEC61000-4-2(ESD): $\pm 18 \mathrm{kV}$ Air, $\pm 12 \mathrm{kV}$ Contact Compliance
- IEC61000-4-4(EFT): 40A(5/50nS)
- IEC61000-4-5(Lightning): 4A(8/20 $\mu$ S)
- Low leakage current, maximum $1 \mu \mathrm{~A}$ at rated voltage
- Acquire quality system certificate : TS16949
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std..(Halogen Free)


## Mechanical Data

- Case: SOT-523, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00007 ounces, 0.002 grams


## Applications

- USB3.0 Data Line Protection
- High Definition Multi-Media Interface Protection
- Monitors and Flat Panel Displays Notebook computers
- Video Line Protection \& Base Stations
- 10/100/1000 Ethernet


Fig.25(Top View)

- HDSL, IDSL Secondary IC Side Protection
- Control Signal Lines Protection

Maximum Ratings ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| PARAMETER | SYMBOL | LIMIT | UNITS |
| :--- | :---: | :---: | :---: |
| ESD IEC61000-4-2(Air) | $\mathrm{V}_{\text {ESD }}$ | $\pm 18$ |  |
| ESD IEC61000-4-2(Contact) | $\mathrm{T}_{\mathrm{J}}$ | kV |  |
| Operating Junction Temperature Range | $\mathrm{T}_{\text {STG }}$ | -55 to +150 | -55 to +150 |
| Storage Temperature Range | ${ }^{\circ} \mathrm{C}$ |  |  |

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Electrical Characteristics ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Stand-Off Voltage | $\mathrm{V}_{\text {RWM }}$ | - | - | - | 5 | V |
| Reverse Breakdown Voltage | $V_{\text {BR }}$ | $\mathrm{I}_{\mathrm{BR}}=1 \mathrm{~mA},$ <br> Between any I/O pins to GND | 5.8 | - | 10.2 | V |
| Reverse leakage current | $I_{R}$ | $V_{R}=5 \mathrm{~V},$ <br> any I/O pin to GND | - | - | 1 | $\mu \mathrm{A}$ |
| Clamping Voltage | $\mathrm{V}_{\mathrm{CL}}$ | $\mathrm{I}_{\mathrm{Pp}}=1 \mathrm{~A}, \mathrm{t}_{\mathrm{P}}=8 / 20 \mu \mathrm{~s}$, any I/O pin to GND | - | 9 | 12 | V |
|  |  | $\mathrm{I}_{\mathrm{Pp}}=4 \mathrm{~A}, \mathrm{t}_{\mathrm{P}}=8 / 20 \mu \mathrm{~s}$, any I/O pin to GND | - | - | 15 |  |
| Clamping Voltage TLP ${ }^{\text {(Note 1) }}$ | $\mathrm{V}_{\mathrm{CL}}$ | $\mathrm{I}_{\mathrm{pp}}=4 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=100 \mathrm{~ns}$, any I/O pin to GND | - | 9.6 | - | V |
|  |  | $\mathrm{I}_{\mathrm{pp}}=8 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=100 \mathrm{~ns}$, any I/O pin to GND | - | 10.6 | - |  |
| Dynamic Resistance ${ }^{\text {(Note 1) }}$ | $\mathrm{R}_{\text {DYN }}$ | $\mathrm{t}_{\mathrm{p}}=100 \mathrm{~ns}$ | - | 0.25 | - | $\Omega$ |
| Off State Junction Capacitance | C J | OVdc Bias $\mathrm{f}=1 \mathrm{MHz}$, <br> Between any I/O pins to GND | - | 0.6 | 0.8 | pF |

NOTES :

1. Testing using Transmission Line Pulse (TLP) conditions: $Z_{0}=50 \Omega$, $t_{P}=100 \mathrm{~ns}$.

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## TYPICAL CHARACTERISTIC CURVES



Fig. 1 Typical Junction Capacitance


Fig. 3 Typical Peak Clamping Voltage $(8 / 20 \mu \mathrm{~s})$


Fig. 2 Transmission Line Pulsing (TLP) Measurement


Fig. 4 8/20 $\mu$ s Pulse Waveform

CONDUCTOR

## PJE5V0U8TB-AU

PART NO PACKING CODE VERSION

| Part No Packing Code | Package Type | Packing Type | Marking | Version |
| :---: | :---: | :---: | :---: | :---: |
| PJE5V0U8TB-AU_R1_000A1 | SOT-523 | $4 \mathrm{~K} \mathrm{pcs} \mathrm{/} \mathrm{7"} \mathrm{reel}$ | $8 T$ | Halogen free |

## For example :

RB500V-40_R2_00001


## Version code means HF

- Packing size code means 13"
- Packing type means T/R

| Packing Code XX |  |  |  | Version Code |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Packing type | $\begin{gathered} 1^{\text {st }} \\ \text { Code } \end{gathered}$ | Packing size code | $\begin{gathered} 2^{\text {nd }} \\ \text { Code } \end{gathered}$ | HF or RoHS | $\begin{gathered} 1^{\text {st }} \\ \text { Code } \end{gathered}$ | $2^{\text {nd }} \sim 5^{\text {th }}$ <br> Code |
| Tape and Ammunition Box (T/B) | A | N/A | 0 | HF | 0 | serial number |
| Tape and Reel (T/R) | R | 7" | 1 | RoHS | 1 | serial number |
| Bulk Packing (B/P) | B | 13" | 2 |  |  |  |
| Tube Packing (T/P) | T | 26mm | X |  |  |  |
| Tape and Reel (Right Oriented) (TRR) | S | 52mm | Y |  |  |  |
| Tape and Reel (Left Oriented) (TRL) | L | PANASERT T/B CATHODE UP (PBCU) | U |  |  |  |
| FORMING | F | PANASERT T/B CATHODE DOWN (PBCD) | D |  |  |  |

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## MOUNTING PAD LAYOUT



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