## PJEC24MTA-AU

LOW CAPACITANCE DOUBLE BIDIRECTIONAL ESD PROTECTION DIODES

| VOLTAGE | 24 Volt | IPP | 4 Ampere |
| :--- | :--- | :--- | :--- |
| FEATURES |  |  |  |

- ESD protection of two lines
- Acquire quality system certificate : TS16949
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. . (Halogen Free)


## MECHANICAL DATA

- Case : SOT-23, Plastic
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams
- Marking : 12A


MAXIMUMRATINGSANDELECTRICALCHATACTERISTICS

| Rating | Symbol | Value | Units |
| :--- | :---: | :---: | :---: |
| Peak Pulse Current on 8/20 $\mu$ s waveform (Notes 1,2,3) | $\mathrm{I}_{\text {PPM }}$ | 4 | Amps |
| ESD Voltage Air Mode | $\mathrm{V}_{\text {ESD }}$ | 20 | KV |
| ESD Voltage Contact Mode | $\mathrm{V}_{\text {ESD }}$ | 20 | KV |
| Operating Temperature And Storage Temperature | $\mathrm{T}_{J, ~} \mathrm{~T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

## ELECTRICALCHATACTERISTICS

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Units |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Stand-Off Voltage (Notes 4) | $\mathrm{V}_{\mathrm{RWM}}$ | - | - | - | 24 | V |
| Reverse Breakdown Voltage | $\mathrm{V}_{\mathrm{BR}}$ | $\mathrm{I}_{\mathrm{BR}}=5 \mathrm{~mA}$ | 25.4 | - | 30.3 | V |
| Reverse Leakage Current | $\mathrm{I}_{\mathrm{R}}$ | $\mathrm{V}_{\mathrm{R}}=24 \mathrm{~V}$ | - | - | 50 | nA |
| Clamping Voltage (8/20 $\mu \mathrm{s})$ | $\mathrm{V}_{\mathrm{C}}$ | $\mathrm{I}_{\mathrm{PP}}=4 \mathrm{~A}$ | - | - | 50 | V |
| Off State Junction Capacitance | $\mathrm{C}_{\mathrm{J}}$ | $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | - | 11 | - | pF |

## NOTES:

1. Non-repetitive current pulse.
2. Mounted on copper pads to each terminal.
3. Peak pulse power waveform is $8 / 20 \mu \mathrm{~s}$.
4. A transient suppressor is selected according to the working peak reverse voltage ( $\mathrm{V}_{\mathrm{R} W m}$ ), which should be equal to or greater than the DC or continuous peak operating voltage level.

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Fig. 1 Typical Junction Capacitance


Fig. 3 Typical Peak Clamping Voltage


Fig. 2 Typical Reverse Characteristics


Fig. $48 / 20 \mu$ S Peak Pulse Current Waveform

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## MOUNTING PAD LAYOUT



## ORDER INFORMATION

- Packing information

T/R - 12 K per 13 " plastic Reel
T/R - 3K per 7" plastic Reel

## PJEC24MTA-AU

## Part No_packing code_Version

PJEC24MTA-AU_R1_000A1
PJEC24MTA-AU_R2_000A1

For example :
RB500V-40_R2_00001


| Packing Code XX |  |  |  | Version Code |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Packing type | $1{ }^{\text {st }}$ Code | Packing size code | $2^{\text {nd }}$ Code | HF or RoHS | $1^{\text {st }}$ Code | $2^{\text {nd }} \sim 5^{\text {th }}$ Code |
| Tape and Ammunition Box (T/B) | A | N/A | 0 | HF | 0 | serial number |
| Tape and Reel (T/R) | R | 7" | 1 | RoHS | 1 | serial number |
| Bulk Packing (B/P) | B | 13" | 2 |  |  |  |
| Tube Packing (T/P) | T | 26 mm | X |  |  |  |
| Tape and Reel (Right Oriented) (TRR) | S | 52 mm | Y |  |  |  |
| Tape and Reel (Left Oriented) (TRL) | L | PANASERT T/B CATHODE UP (PBCU) | U |  |  |  |
| FORMING | F | PANASERT T/B CATHODE DOWN (PBCD) | D |  |  |  |

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