



20V N-Channel Enhancement Mode MOSFET

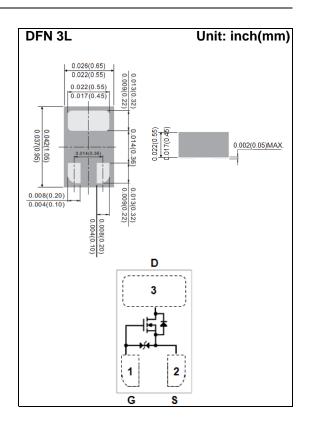
Voltage 20 V Current 1.2 A

Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN 3L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00004 ounces, 0.0011 grams
- Marking: 0



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 10	V	
Continuous Drain Current	T _A =25°C	_D	1.2		
	T _{sp} =25°C (Note 3)		2.0	A	
Pulsed Drain Current, tp≤10us		I _{DM}	4.0	Α	
Power Dissipation	T _A =25°C	P _D	900	mW	
	Derate above 25°C		7.2	mW/°C	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C	
Typical Thermal resistance					
- Junction to Ambient, t<10s (Note 4)		$R_{\theta JA}$	139	°C/W	





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	0.3	0.65	0.9	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =600mA	-	300	400	mΩ	
		V_{GS} =2.5V, I_{D} =200mA	-	350	650		
		V _{GS} =1.8V, I _D =100mA	-	400	800		
		V _{GS} =1.5V, I _D =50mA	-	500	1200		
		V _{GS} =1.2V, I _D =20mA	-	1000	3000		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	I _{GSS}	$V_{GS}=\underline{+}8V, V_{DS}=0V$	-	<u>+</u> 0.5	<u>+</u> 10	uA	
Dynamic (Note 6)							
Total Gate Charge	Q_g	V _{DS} =10V, I _D =300mA, V _{GS} =4.5V ^(Note 2)	-	1.4	-	nC	
Gate-Source Charge	Q_gs		-	0.22	-		
Gate-Drain Charge	Q_gd		-	0.21	-		
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	67	-	pF	
Output Capacitance	Coss		-	19	-		
Reverse Transfer Capacitance	Crss	I=1.0IVIDZ	-	6	-		
Turn-On Delay Time	td _(on)	\/ 40\/ I 450m A	-	2.8	-	ns	
Turn-On Rise Time	tr	$V_{DD}=10V, I_{D}=150mA,$ $V_{GS}=4.0V,$ $R_{G}=10\Omega$ (Note 1,2)	-	20	-		
Turn-Off Delay Time	td _(off)		-	23	-		
Turn-Off Fall Time	tf	K _G =1022	-	23	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	300	mA	
Diode Forward Voltage	V_{SD}	I _S =300mA, V _{GS} =0V	-	0.87	1.3	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Tsp is the temperature at the soldering point of the source lead.
- 4. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1inch FR-4 with 2oz. square pad of copper.
- 5. The maximum current rating is package limited
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

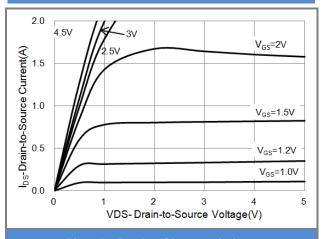


Fig.1 On-Region Characteristics

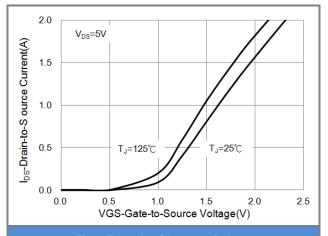


Fig.2 Transfer Characteristics

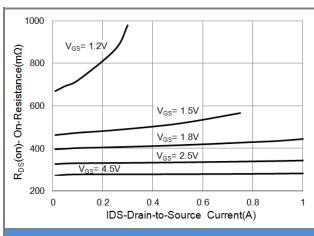


Fig.3 On-Resistance vs. Drain Current

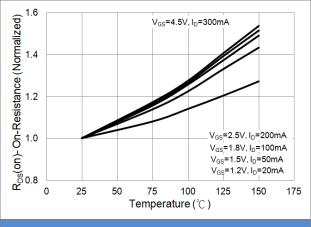
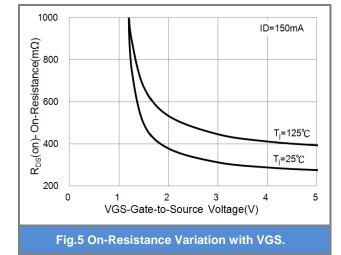
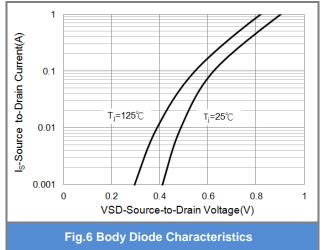


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

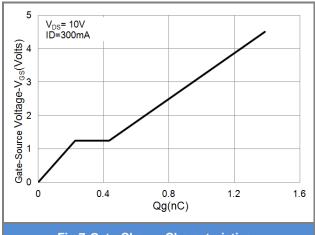


Fig.7 Gate-Charge Characteristics

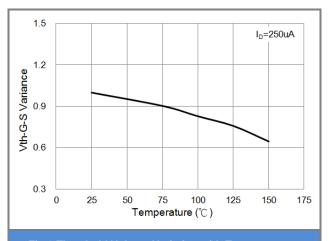


Fig.8 Threshold Voltage Variation with Temperature.

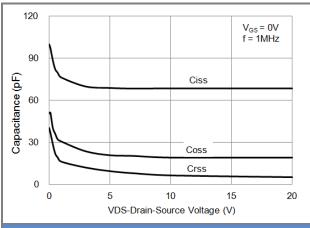


Fig.9 Capacitance vs. Drain-Source Voltage.

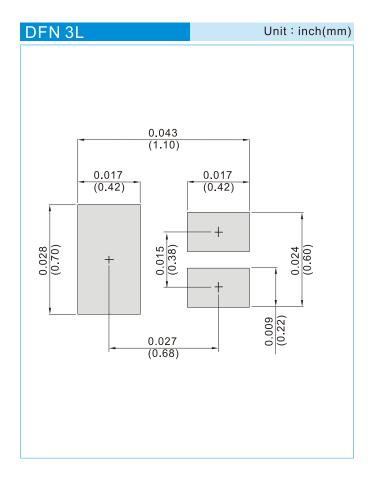




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ1900_R1_00001	DFN 3L	8K pcs / 7" reel	0	Halogen free

MOUNTING PAD LAYOUT







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