ΡΛΝ	JIT
	SEMI
	CONDUCTOR

## 40V N-Channel Enhancement Mode MOSFET

Current

40 A

Voltage

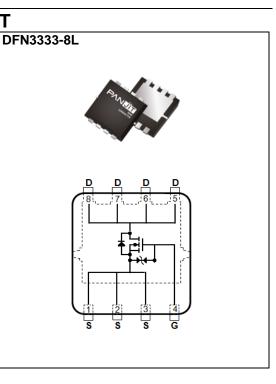
40 V

### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@10A<10.4m\Omega$
- Rds(ON), Vgs@7V, Id@6A<12.7mΩ
- Excellent FOM
- Standard Level Drive
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.03 grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETE	R	SYMBOL	LIMIT	UNITS
Prain-Source Voltage		V <sub>DS</sub>	40	
Gate-Source Voltage		V <sub>GS</sub>	±20	V
O (Note 2)	Tc=25°C		40	
Continuous Drain Current <sup>(Note 3)</sup>	Tc=100°C	I <sub>D</sub>	28	А
Pulsed Drain Current(Note 1)	T <sub>C</sub> =25°C	I <sub>DM</sub>	160	
Power Dissipation	T <sub>C</sub> =25°C	2	30	
	Tc=100°C	PD	15	W
Question of Duration Querran (Note 4)	T <sub>A</sub> =25°C		11.6	
Continuous Drain Current <sup>(Note 4)</sup> T <sub>A</sub> =70 <sup>o</sup> C	T <sub>A</sub> =70°C	I <sub>D</sub>	9.7	— A
De la Dischartier	T <sub>A</sub> =25°C	5	2.5	
Power Dissipation	T <sub>A</sub> =70°C	PD	1.8	W
Single Pulse Avalanche Energy <sup>(Note</sup>	e 5)	Eas	36	mJ
Operating Junction and Storage Te	emperature Range	TJ,TSTG	-55~175	°C
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	R <sub>θJC</sub>	5	°C/W
	Junction to Ambient	R <sub>θJA</sub>	60	C/W



### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static		·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =50uA	2	2.8	3.5		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	8.3	10.4	mΩ	
		V <sub>GS</sub> =7V, I <sub>D</sub> =6A	-	9.8	12.7		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	uA	
Onto Course London of Current		V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±10	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±1		
Dynamic <sup>(Note 6)</sup>	-	-	-		-		
Total Gate Charge	Qg		-	9.5	-	nC	
Gate-Source Charge	Qgs	V <sub>DS</sub> =32V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V	-	4.2	-		
Gate-Drain Charge	$Q_gd$	V <sub>GS</sub> =10V	-	2.6	-		
Input Capacitance	Ciss		-	673	-		
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	176	-	pF	
Reverse Transfer Capacitance	Crss	f=1MHz	-	29	-		
Gate resistance	Rg	f=1MHz	-	1.4	-	Ω	
Turn-On Delay Time	td <sub>(on)</sub>		-	10	-		
Turn-On Rise Time	tr	V <sub>DS</sub> =32V, I <sub>D</sub> =10A,	-	3	-		
Turn-Off Delay Time	td <sub>(off)</sub>	V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω (Note 2)	-	18	-	ns	
Turn-Off Fall Time	tf		-	3	-	1	
Drain-Source Diode							
Diode Forward Current	I <sub>S</sub>	T <sub>c</sub> =25°C	-	-	40		
Pulsed Diode Forward Current	I <sub>SM</sub>	1 <sub>C</sub> =25 C	-	-	160	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =20A, V <sub>GS</sub> =0V	-	0.9	1.3	V	
Reverse Recovery Time	Trr	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-	17	-	ns	
Reverse Recovery Charge	Qrr	dls/dt=100A/us	-	9	-	nC	

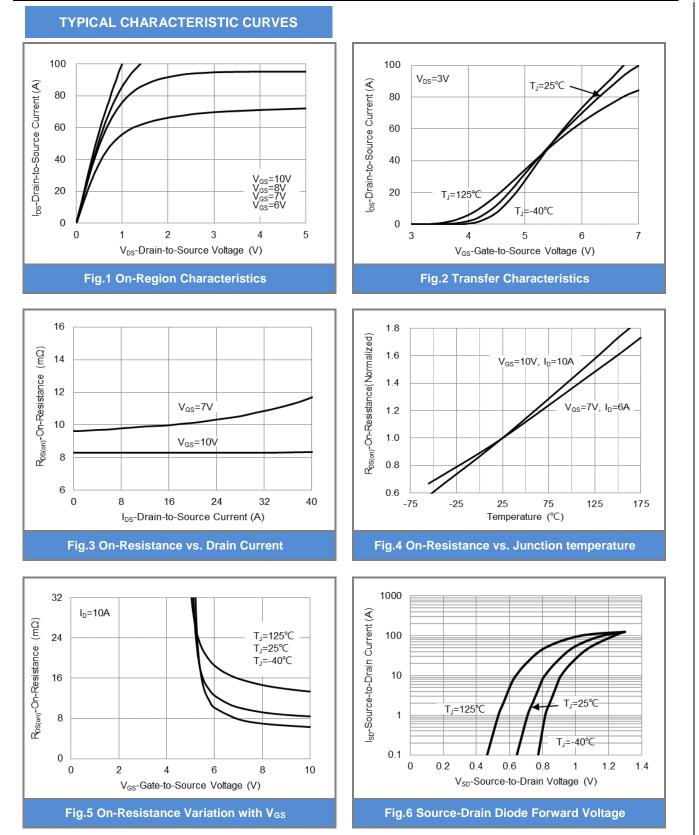
NOTES :

- 1. Pulse width100us, Duty cycle<2%.</td>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Chip capability with an  $R_{\theta JC}=5^{\circ}C/W$ .
- 4.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 5. The test condition is L=0.5mH,  $I_{AS}$ =12A,  $V_{DD}$ =30V,  $V_{GS}$ =10V, Starting T\_J=25°C.
- 6. Guaranteed by design, not subject to production testing.

February 18,2023

#### PJQ4548VP-AU-REV.01





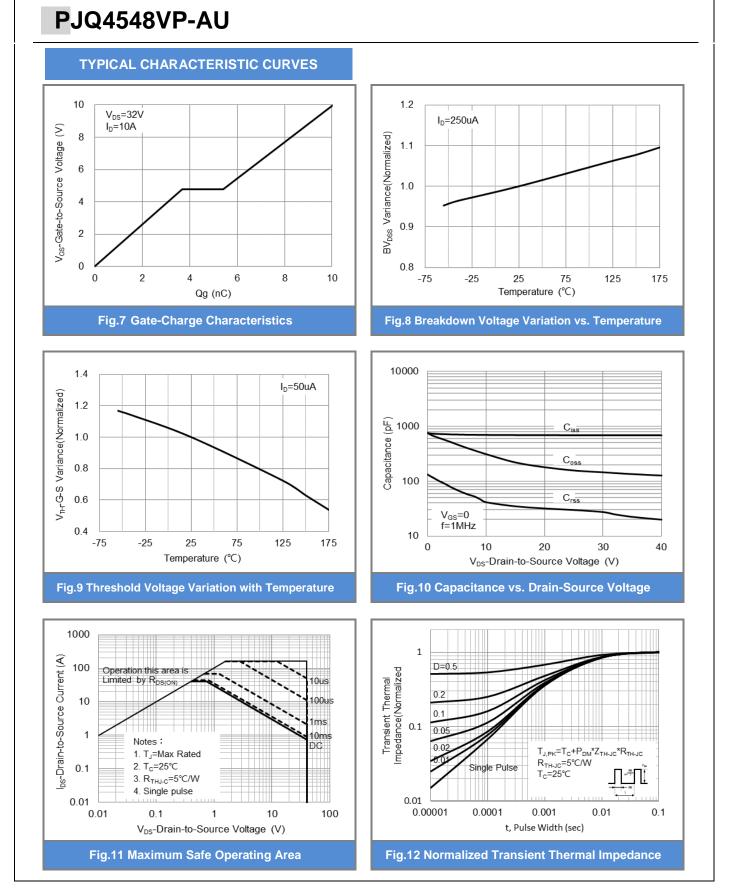
PJQ4548VP-AU



February 18,2023

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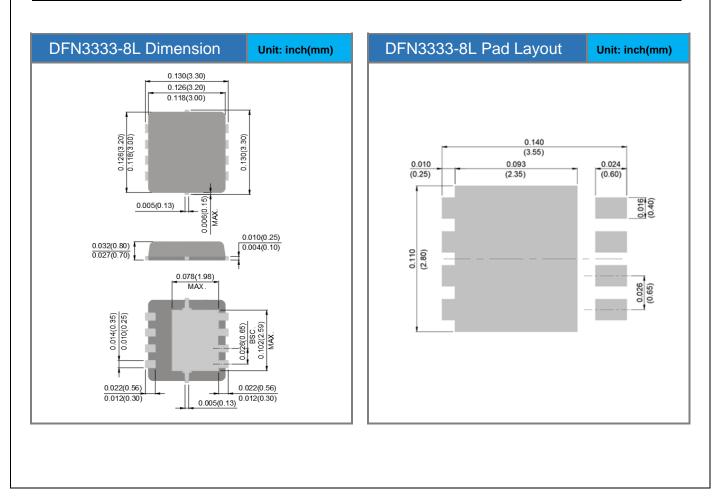




### **Product and Packing Information**

Part No.	Package Type	ckage Type Packing Type	
PJQ4548VP-AU	DFN3333-8L	5K pcs / 13" reel	548V

## Packaging Information & Mounting Pad Layout





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