٩.

PJW4N06A

60V N-Channel Enhancement Mode MOSFET

Current

Voltage

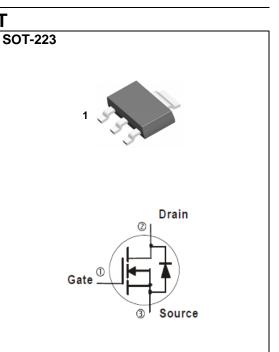
- Features
- $R_{DS(ON)}, V_{GS}@10V, I_D@3.0A<100m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@2.0A < 110m\Omega$
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance

60 V

- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123 grams
- Marking: W4N06A



Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

4.0 A

PARAMETER Drain-Source Voltage Gate-Source Voltage		SYMBOL	LIMIT	UNITS	
		V _{DS}	60	V V	
		V _{GS}	<u>+</u> 20		
Continuous Drain Current	T _A =25°C	I _D	4		
	T _A =70°C		3.2	A	
Pulsed Drain Current (Note 1)		I _{DM}	8	А	
Power Dissipation	T _A =25°C	P _D	3.1		
	T _A =70°C		2	W	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal resistance					
- Junction to Ambient (Note 5)		$R_{ extsf{ heta}JA}$	40.3	°C/W	

• Limited only By Maximum Junction Temperature

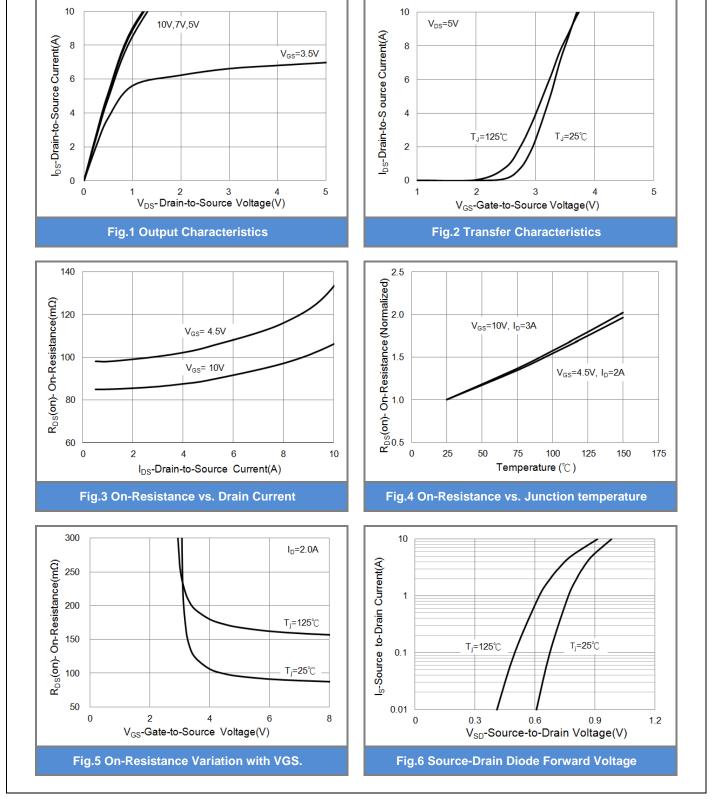


Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V,I _D =250uA	60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$,I _D =250uA	1.0	1.86	2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V,I _D =3.0A	-	85	100	mΩ
		V _{GS} =4.5V,I _D =2.0A	-	95	110	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V,V _{GS} =0V	-	-	1.0	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V _{DS} =48V, I _D =3A, V _{GS} =4.5V ^(Note 2,3)	-	5.1	-	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	
Gate-Drain Charge	Q _{gd}		-	1.9	-	
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	509	-	pF
Output Capacitance	Coss		-	39	-	
Reverse Transfer Capacitance	Crss		-	26	-	
Turn-On Delay Time	td _(on)		-	1.6	-	ns
Turn-On Rise Time	tr	$V_{DD}=30V, I_{D}=3A,$ $V_{GS}=10V,$	-	7.3	-	
Turn-Off Delay Time	td _(off)		-	25	-	
Turn-Off Fall Time	t _f	$R_G=3.3\Omega^{(Note 2,3)}$	-	14	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	1			-	4	A
Diode Forward Current	I _S		-			
Diode Forward Voltage	V _{SD}	I _S =1A,V _{GS} =0V	-	0.8	1.2	V

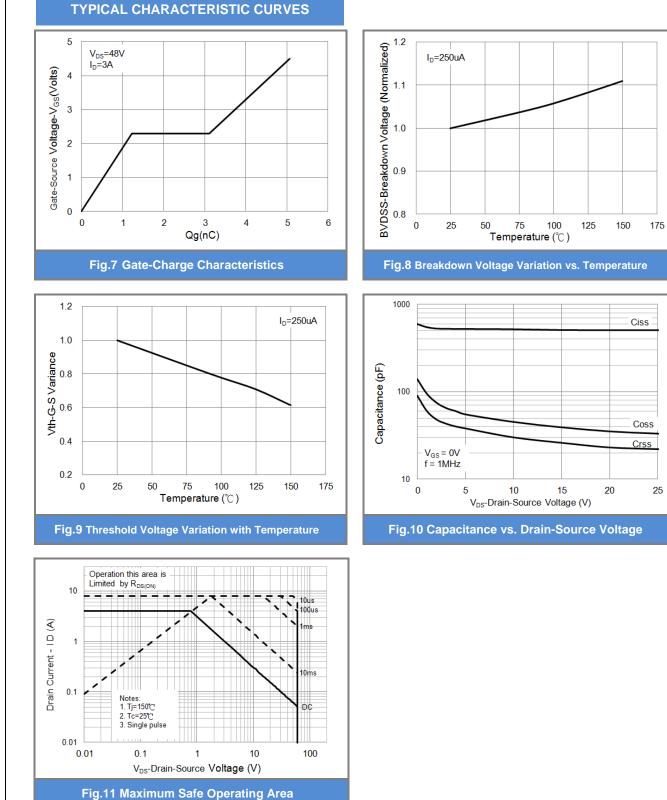
NOTES :

- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited
- 5. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing



TYPICAL CHARACTERISTIC CURVES







PJW4N06A **TYPICAL CHARACTERISTIC CURVES** Z_{TH-JC} Normalized Transient Thermal Impedance 1 D=0.5 0.2 0.1 0.1 0.05

0.001

0.01

t , Pulse Width (Sec)

Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width



0.02 0.01

Single Pulse

0.0001

0.01

0.001 0.00001



 $\begin{array}{l} T_{\mathsf{J},\mathsf{PK}} = \mathsf{Tc} + \mathsf{P}_\mathsf{DM} ^* Z_{\mathsf{TH}\text{-}\mathsf{JC}} ^* \mathsf{R}_{\mathsf{TH}\text{-}\mathsf{JC}} \\ \mathsf{R}_{\mathsf{TH}\text{-}\mathsf{JC}} = 40^\circ \!\!\! \mathbb{C} \, / \mathsf{W} \\ \mathsf{TC} = 25^\circ \!\! \mathbb{C} \end{array}$

1

 $D = \frac{PW}{T}$

10

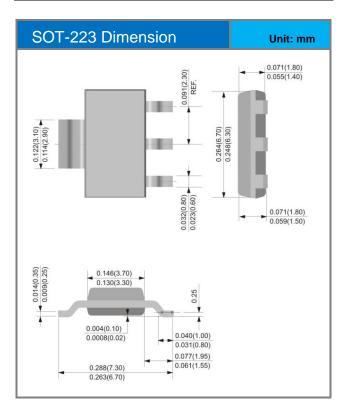
P₽

0.1



4

Packaging Information



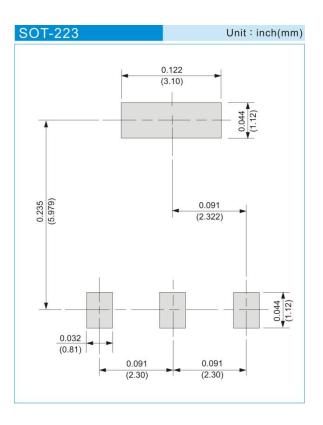




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version	
PJW4N06A_R2_00001	SOT-223	2,500pcs / 13" reel	W4N06A	Halogen free	

MOUNTING PAD LAYOUT





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