

PS150P-D32

Low Capacitance ESD Protection

Voltage

28 V

Features

- IEC61000-4-2(ESD) : ± 21 kV Air, ± 21 kV Contact
- IEC61000-4-5(Lightning) : 10A (8/20 μ S)
- Ultra-Low Capacitance : 0.6pF
- Low leakage current, maximum of 0.5 μ A at rated voltage
- Low clamping voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

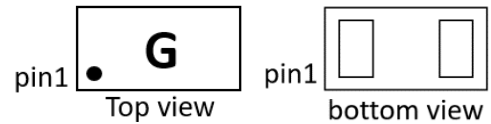
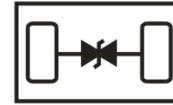
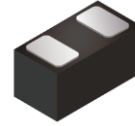
Mechanical Data

- Case : DFN0603-2L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0004 grams

Applications

- Thunderbolt
- USB 3.2
- Type-C connector CC/SBU
- Consumer electronics

DFN0603-2L



Part Marking	Parameter
G	G = Marking Code

Maximum Ratings and Thermal Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
ESD IEC61000-4-2(Air)	V _{ESD}	± 21	kV
ESD IEC61000-4-2(Contact)		± 21	
Typical Thermal Resistance ^(Note 1)	R _{θJA}	500	°C/W
Operating Junction Temperature Range	T _J	-55~125	°C
Storage Temperature Range	T _{STG}	-55~150	°C

PS150P-D32

Electrical Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage ^(Note 2)	V _{RWM}	Pin1 to Pin2	-	-	28	V
Reverse Breakdown Voltage	V _{BR}	I _{BR} = 10uA, Pin1 to Pin2	29.5	-	-	V
Reverse Leakage Current	I _R	V _{RWM} = ±28V, Pin1 to Pin2	-	0.4	0.5	uA
Surge Clamping Voltage (8/20us)	V _{CL}	I _{PP} = 10A, Pin1 to Pin2	-	2.7	3.7	V
Clamping Voltage TLP (t _{period} =100ns,t _r =1ns) ^(Note 3)	V _{CL}	I _{TLP} = 16A, Pin1 to Pin2	-	3	-	V
Off State Junction Capacitance ^(Note 4)	C _J	V _R =0V, f=1MHz, Pin1 to Pin2	-	0.6	0.65	pF

NOTES :

1. Mounted on a FR4 PCB, Single-sided copper, mini pad.
2. A transient suppressor is selected according to the working peak reverse voltage(V_{RWM}), which should be equal to or greater than the DC or continuous peak operation voltage level.
3. Testing using Transmission Line Pulse (TLP) conditions: Z₀ = 50 Ω, t_p = 100 ns.
4. This parameter is guaranteed by design.
5. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid the ESD protection device maintain in snap-back state after exceeding breakdown voltage.

PS150P-D32

TYPICAL CHARACTERISTIC CURVES

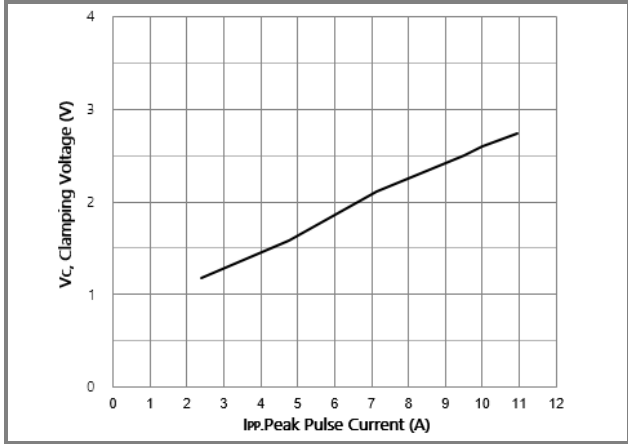


Fig.1 Typical Peak Clamping Voltage

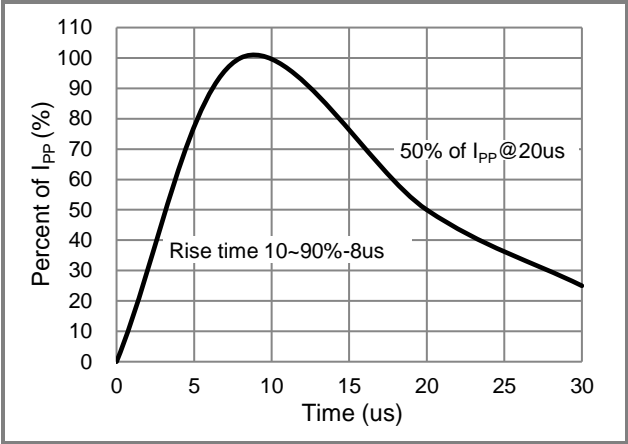


Fig.2 Pulse Waveform

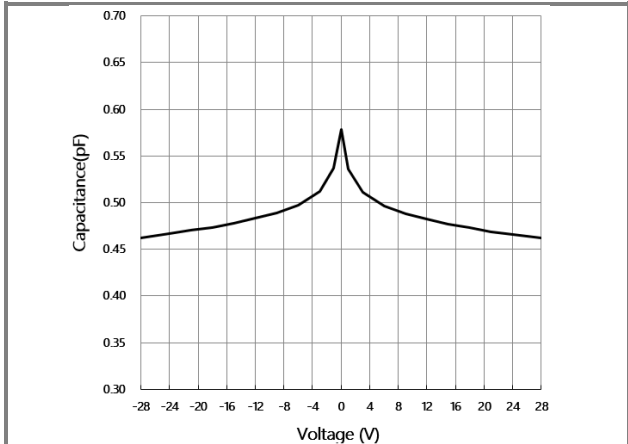


Fig.3 Typical Junction Capacitance

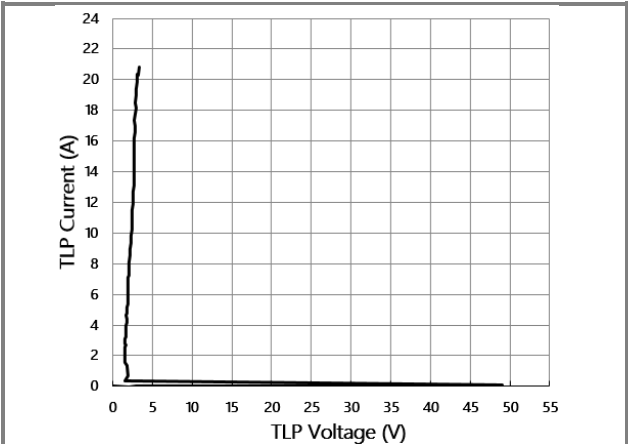


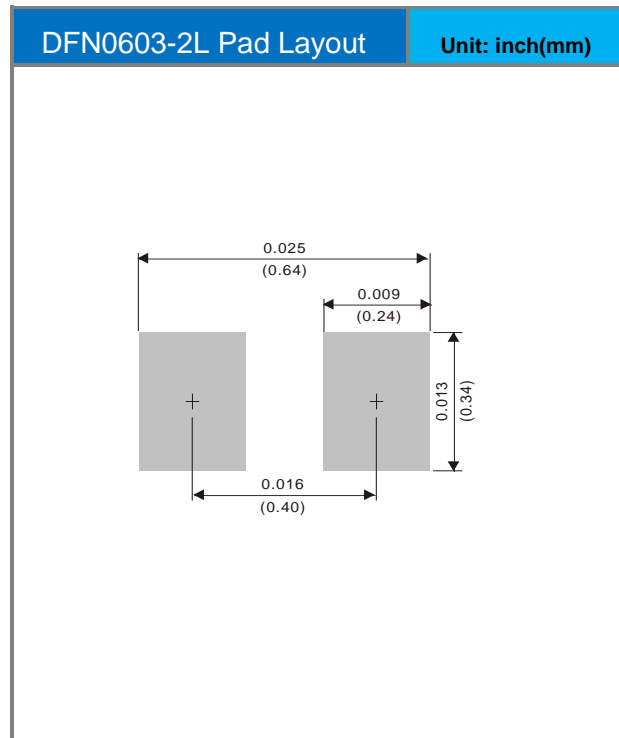
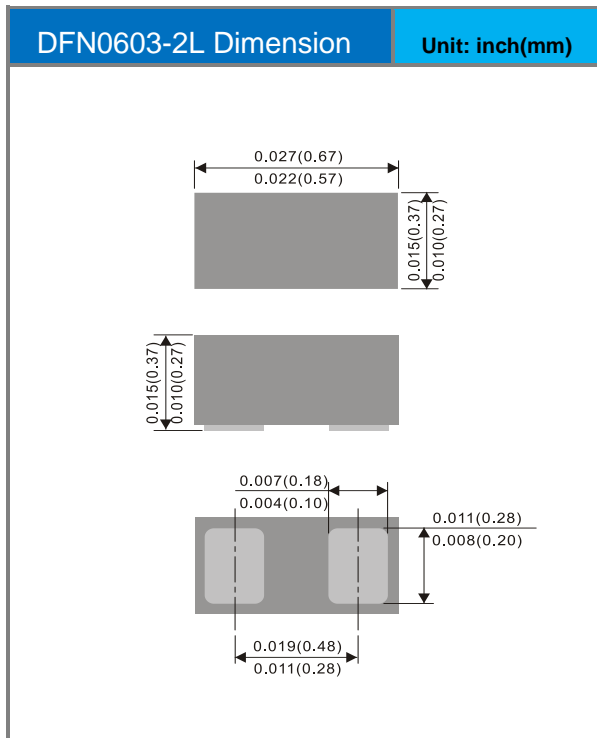
Fig.4 TLP Measurement

PS150P-D32

Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PS150P-D32	DFN0603-2L	10K pcs / 7" reel	G

Packaging Information & Mounting Pad Layout



PS150P-D32

Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Panjit manufacturer](#):

Other Similar products are found below :

[1.5KE150A_AY_10001](#) [1.5KE200A_AY_10001](#) [1.5KE200CA_AY_10001](#) [1.5KE33A_AY_10001](#) [1.5KE75A_AY_10001](#) [1.5SMC27CA-AU_R1_000A1](#) [1.5SMC36A_R1_00001](#) [1.5SMC36CA_R1_00001](#) [1.5SMC39CA_R1_00001](#) [1.5SMC82CA_R1_00001](#)
[1.5SMCJ12A_R1_00001](#) [1.5SMCJ13A_R1_00001](#) [1.5SMCJ150CA_R1_00001](#) [1.5SMCJ15AS_R1_00001](#) [1.5SMCJ20CA_R1_00001](#)
[1.5SMCJ22CA-AU_R1_000A1](#) [1.5SMCJ24CA_R1_00001](#) [1.5SMCJ28CA_R1_00001](#) [1.5SMCJ33A_R1_00001](#) [1.5SMCJ33A-AU_R1_000A1](#) [1.5SMCJ33CA_R1_00001](#) [1.5SMCJ36A_R1_00001](#) [1.5SMCJ36A_R1_10001](#) [1.5SMCJ36CA_R1_00001](#)
[1.5SMCJ40A_R1_00001](#) [1.5SMCJ48A_R1_00001](#) [1.5SMCJ51CAS](#) [1.5SMCJ54A_R1_00001](#) [1.5SMCJ75CA_R1_00001](#) [1N4007_AY_10001](#)
[1N4007G_AY_00101](#) [1N4007G_AY_10001](#) [1N4148-35_AX_10001](#) [1N4148-35_AY_10001](#) [1N4148W_R1_00001](#) [1N4148W_R1_000A7](#)
[1N4148W_R1_000Z8](#) [1N4148W_R1_00101](#) [1N4148W_R2_00001](#) [1N4148W-AU_R1_000A1](#) [1N4148W-AU_R2_000A1](#)
[1N4148WS_R1_00001](#) [1N4148WS_R1_00101](#) [1N4148WS-AU_R1_00001](#) [1N4148WS-AU_R1_000A1](#) [1N4148WS-R1_000A4](#)
[1N4448W_R1_00001](#) [1N4448WS_R1_00001](#) [1N4737A_AY_10001](#) [1N4743A-G](#)