

# PS5915-S26

## Low Capacitance ESD Protection

**Voltage**

**5 V**

### Features

- IEC61000-4-2 (ESD) :  $\pm 30\text{kV}$  Air,  $\pm 30\text{kV}$  Contact
- IEC61000-4-4(EFT) : 40A(5/50ns)
- IEC61000-4-5 (Lightning) : 10A (8/20uS)
- Low leakage current, maximum of 1uA at rated voltage
- Ultra low clamping voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

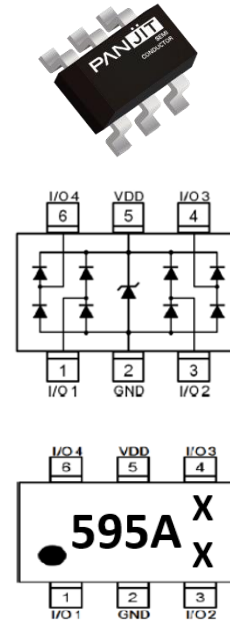
### Mechanical Data

- Case : SOT-23 6L-1 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0142 grams

### Applications

- USB 2.0 Power and Data lines protection
- Notebook/Desktop Computers
- SIM ports
- Video Graphics Cards

SOT-23 6L-1



Part Marking	Parameter
595A X	595A = Marking Code
X	X = Tracking Code

## Maximum Ratings and Thermal Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNITS
ESD IEC61000-4-2(Air)	V <sub>ESD</sub>	±30	kV
ESD IEC61000-4-2(Contact)		±30	
Typical Thermal Resistance	R <sub>θJA</sub>	350	°C/W
Operating Junction Temperature Range	T <sub>J</sub>	-55 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

# PS5915-S26

## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

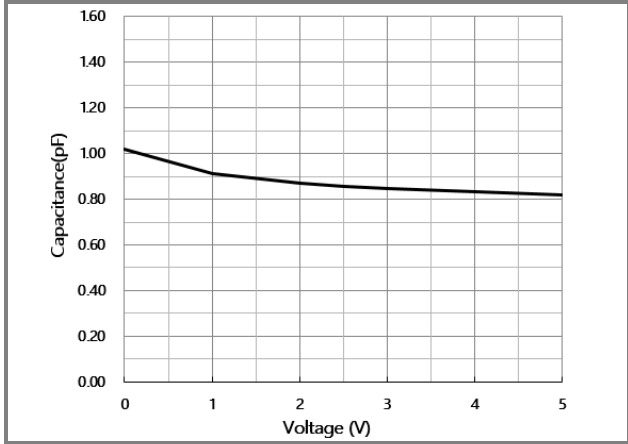
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage <sup>(Note 1)</sup>	V <sub>RWM</sub>	IO Pin to GND	-	-	3.3	V
Reverse Stand-Off Voltage <sup>(Note 1)</sup>	V <sub>RWM</sub>	VDD Pin to GND	-	-	5	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>BR</sub> = 1mA, I/O Pin to GND	5	-	11	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>BR</sub> = 1mA, VDD Pin to GND	5.5	-	11	V
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 15mA, I/O Pin to GND	-	1	-	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 3.3V, I/O Pin to GND	-	0.5	1	uA
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 5V, VDD Pin to GND	-	0.5	1	uA
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> = 5A, t <sub>P</sub> = 8/20μs, any I/O pin to GND	-	3.8	4.8	V
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> = 5A, t <sub>P</sub> = 8/20μs, VDD pin to GND	-	6.5	7.5	V
Clamping Voltage TLP <sup>(Note 2)</sup>	V <sub>CL</sub>	I <sub>TLP</sub> = 16A, t <sub>P</sub> = 100ns, I/O pin to GND	-	4	-	V
Clamping Voltage TLP <sup>(Note 2)</sup>	V <sub>CL</sub>	I <sub>TLP</sub> = 16A, t <sub>P</sub> = 100ns, VDD pin to GND	-	6.5	-	V
Off State Junction Capacitance <sup>(Note 3)</sup>	C <sub>J</sub>	V <sub>R</sub> = 1.65V, f = 1MHz, I/O Pin to GND	-	0.9	1.4	pF

NOTES :

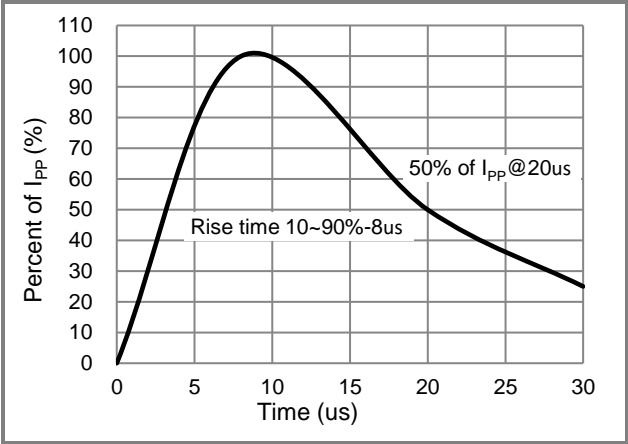
1. A transient suppressor is selected according to the working peak reverse voltage(V<sub>RWM</sub>), which should be equal to or greater than the DC or continuous peak operation voltage level.
2. Testing using Transmission Line Pulse (TLP) conditions: Z<sub>0</sub> = 50Ω, t<sub>P</sub> = 100 ns.
3. This parameter is guaranteed by design.

# PS5915-S26

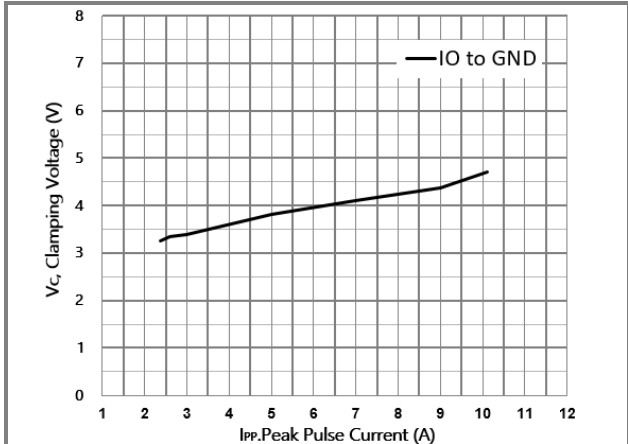
## TYPICAL CHARACTERISTIC CURVES



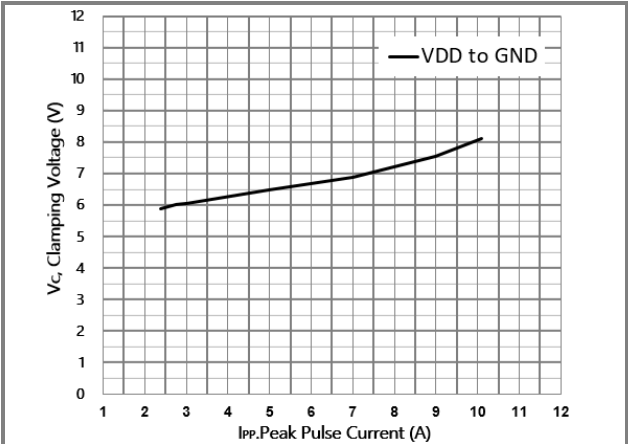
**Fig.1 Typical Junction Capacitance**



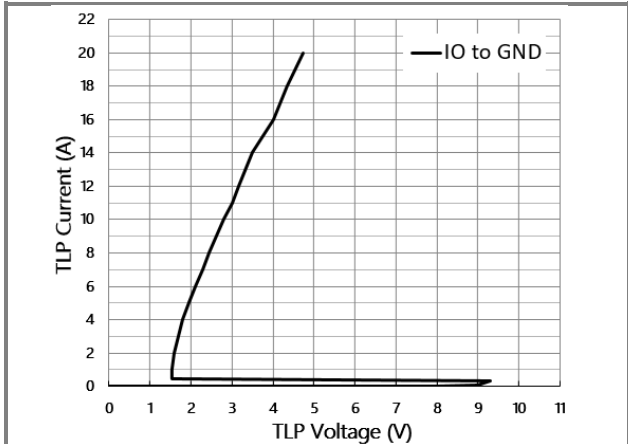
**Fig.2 Pulse Waveform**



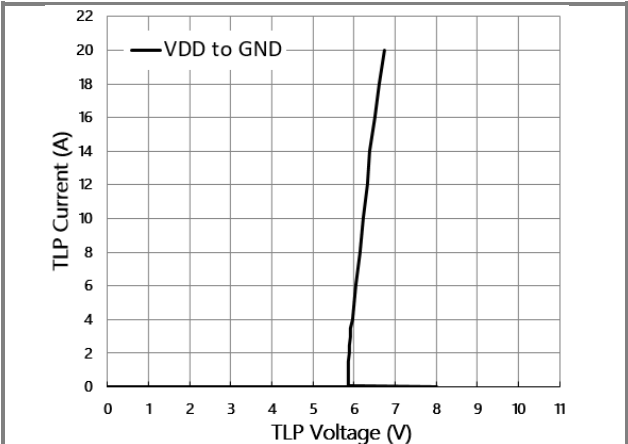
**Fig.3 Typical Peak Clamping Voltage**



**Fig.4 Typical Peak Clamping Voltage**



**Fig.5 TLP Measurement**



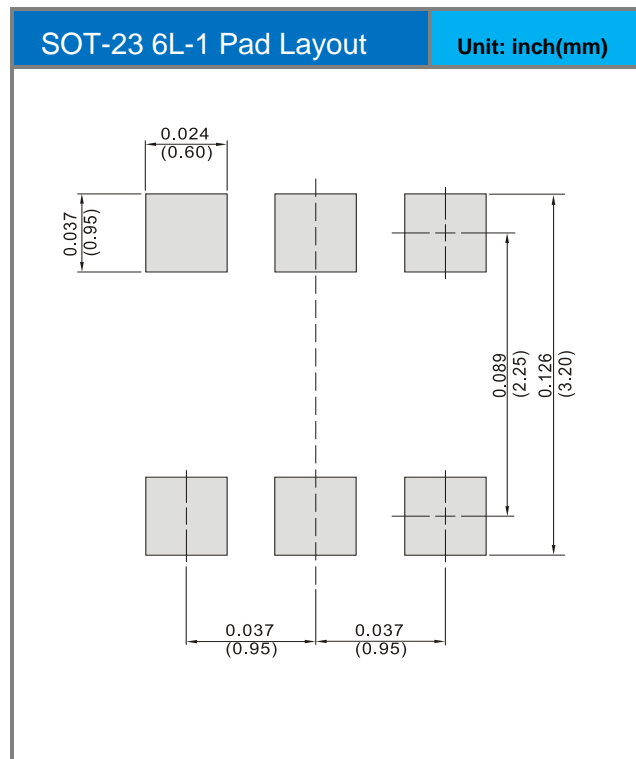
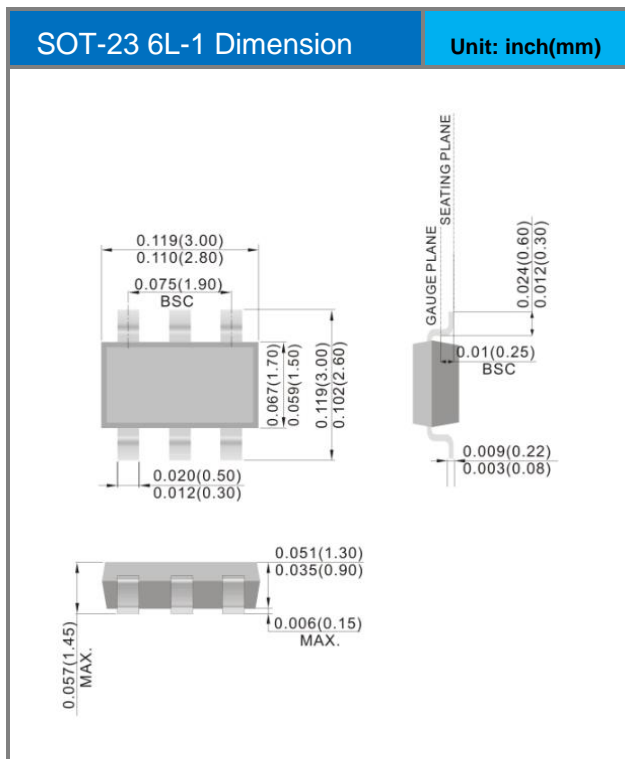
**Fig.6 TLP Measurement**

# PS5915-S26

## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PS5915-S26	SOT-23 6L-1	3K pcs / 7" reel	595A

## Packaging Information & Mounting Pad Layout



## PS5915-S26

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