

**100V N-Channel Enhancement Mode MOSFET**

<b>Voltage</b>	<b>100 V</b>	<b>R<sub>DS(ON)</sub></b>	<b>28 mΩ</b>
<b>Current</b>	<b>21 A</b>	<b>Q<sub>G</sub> (TYP)</b>	<b>9.0 nC</b>

**Feature**

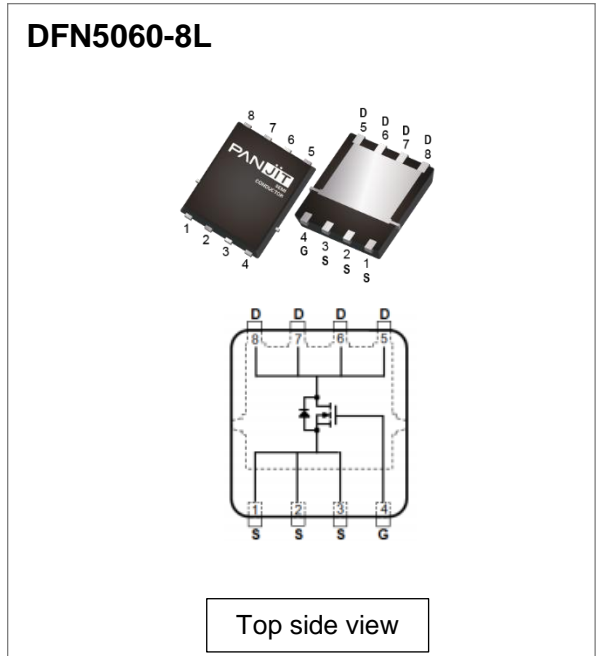
- R<sub>DS(ON)</sub> < 28 mΩ at V<sub>GS</sub> = 10 V, I<sub>D</sub> = 10 A
- R<sub>DS(ON)</sub> < 39 mΩ at V<sub>GS</sub> = 4.5 V, I<sub>D</sub> = 5 A
- High switching speed
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard
- 100% UIS / Rg test in mass production

**Mechanical Data**

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 94 mg

**Application**

- Lighting / Home Appliance.



**Absolute Maximum Ratings** (T<sub>A</sub> = 25 °C unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	±20	
Continuous Drain Current (Note 3)	T <sub>C</sub> =25 °C	I <sub>D</sub>	21	A
	T <sub>C</sub> =100 °C		15	
Pulsed Drain Current		I <sub>DM</sub>	84	A
Single Pulse Avalanche Current (Note 5)		I <sub>AS</sub>	6.3	A
Single Pulse Avalanche Energy (Note 5)		E <sub>AS</sub>	4.5	mJ
Power Dissipation	T <sub>C</sub> =25 °C	P <sub>D</sub>	29.4	W
	T <sub>C</sub> =100 °C		14.7	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~175	°C

**Thermal Characteristics**

PARAMETER	SYMBOL	VALUES			UNITS	
		MIN.	TYP.	MAX.		
Thermal Resistance	Junction-to-Case (Bottom)	R <sub>θJC</sub>	-	3.4	5.1	°C/W
	Junction-to-Ambient (Note 4)	R <sub>θJA</sub>	-	-	50	°C/W

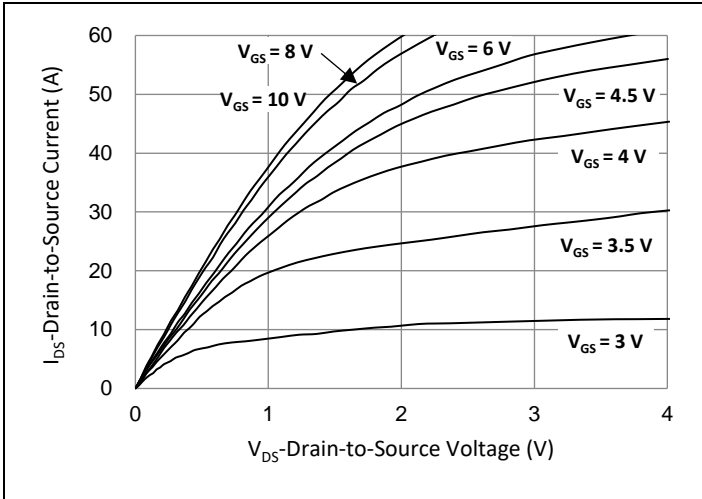
**Electrical Characteristics** ( $T_A = 25\text{ }^\circ\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0\text{ V}, I_D=250\text{ }\mu\text{A}$	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=37\text{ }\mu\text{A}$	1.1	1.7	2.3	
Drain-Source On-State Resistance (Note 1)	$R_{DS(on)}$	$V_{GS}=10\text{ V}, I_D=10\text{ A}$	-	24	28	m $\Omega$
		$V_{GS}=4.5\text{ V}, I_D=5\text{ A}$	-	30	39	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=100\text{ V}, V_{GS}=0\text{ V}$	-	-	1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20\text{ V}, V_{DS}=0\text{ V}$	-	-	$\pm 100$	nA
Transfer characteristics (Note 1)	$g_{fs}$	$V_{DS}=10\text{ V}, I_D=10\text{ A}$	-	10	-	S
<b>Dynamic Characteristics</b> (Note 6)						
Total Gate Charge	$Q_g$	$V_{DS}=50\text{ V}, I_D=10\text{ A}, V_{GS}=4.5\text{ V}$	-	4.4	-	nC
		$V_{DS}=50\text{ V}, I_D=10\text{ A}, V_{GS}=10\text{ V}$	-	9.0	11.7	nC
Gate-Source Charge	$Q_{gs}$		-	2.0	-	
Gate-Drain Charge	$Q_{gd}$		-	1.3	-	
Gate Plateau Voltage	$V_{plateau}$		-	3.0	-	V
Input Capacitance	$C_{iss}$	$V_{DS}=50\text{ V}, V_{GS}=0\text{ V}, f=250\text{ kHz}$	-	525	680	pF
Output Capacitance	$C_{oss}$		-	200	260	
Reverse Transfer Capacitance	$C_{riss}$		-	7.5	-	
Output Charge	$Q_{oss}$	$V_{DS}=50\text{ V}, V_{GS}=0\text{ V}$	-	13.4	17.4	nC
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=50\text{ V}, I_D=5\text{ A}, V_{GS}=10\text{ V}, R_G=1.6\text{ }\Omega$ (Note 2)	-	3.0	-	ns
Rise Time	$t_r$		-	2.2	-	
Turn-Off Delay Time	$t_{d(off)}$		-	8.2	-	
Fall Time	$t_f$		-	1.6	-	
Gate Resistance	$R_g$	$f=1.0\text{ MHz}$	-	1.05	2.1	$\Omega$
<b>Drain-Source Diode</b>						
Diode Forward Voltage	$V_{SD}$	$I_S=10\text{ A}, V_{GS}=0\text{ V}$	-	0.9	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F=10\text{ A}, V_{DD}=50\text{ V}, di/dt=100\text{ A}/\mu\text{s}$	-	27	-	nC
Reverse Recovery Time	$T_{rr}$		-	21	-	ns

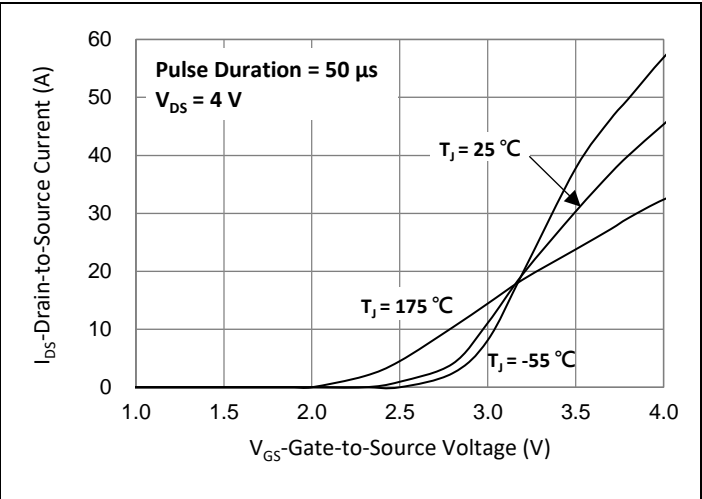
NOTES :

1. Pulse width  $\leq 300\text{ }\mu\text{s}$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. The maximum drain current calculated by maximum junction temperature and thermal impedance. It can be varied by application and environment.
4.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz. square pad of copper.
5.  $E_{AS}$  is calculated based on the condition of  $L = 1.0\text{ mH}$ ,  $I_{AS} = 3.5\text{ A}$ ,  $V_{DD} = 50\text{ V}$ ,  $V_{GS} = 10\text{ V}$ . 100% test at  $L = 0.1\text{ mH}$ ,  $I_{AS} = 6.3\text{ A}$  in production.
6. Guaranteed by design, not subject to production testing.

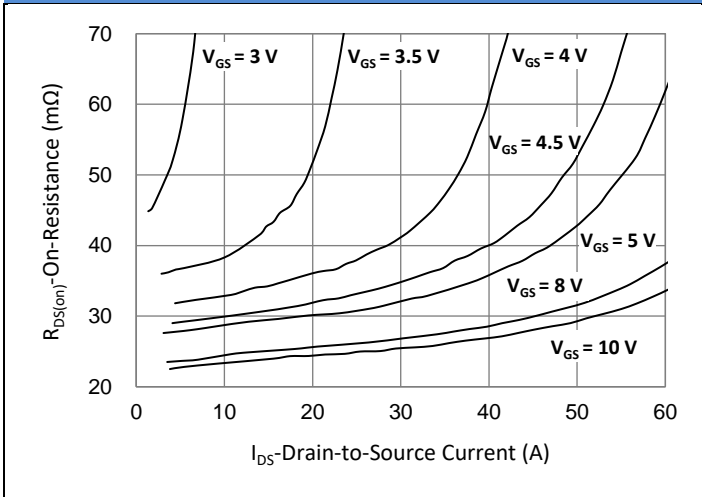
**TYPICAL CHARACTERISTIC CURVES**



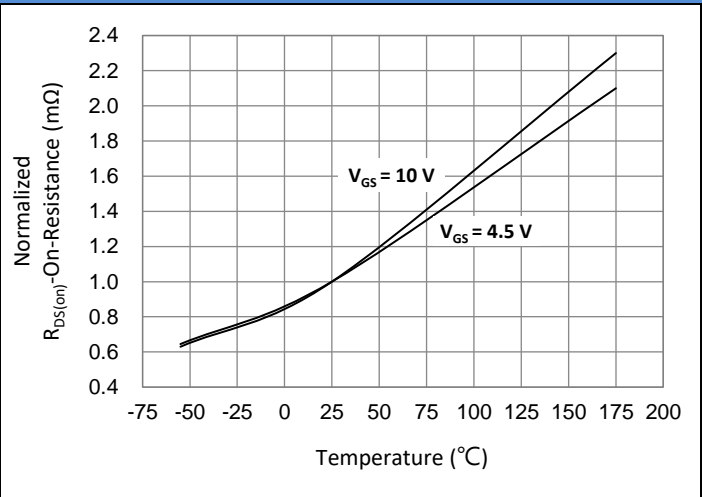
**Fig.1 Output Characteristics**



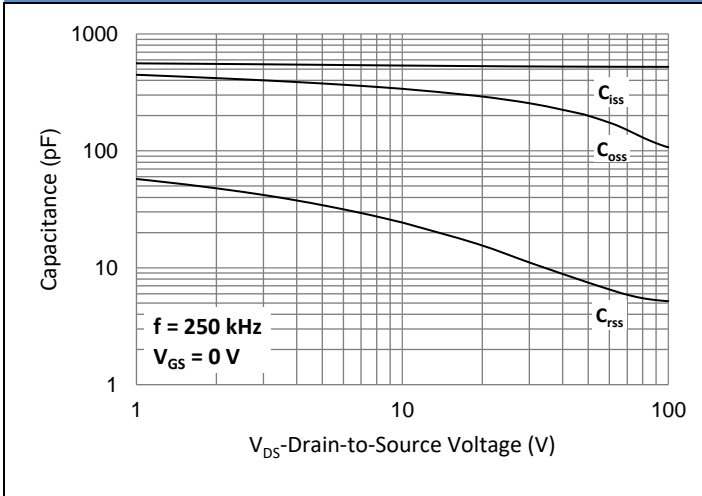
**Fig.2 Transfer Characteristics**



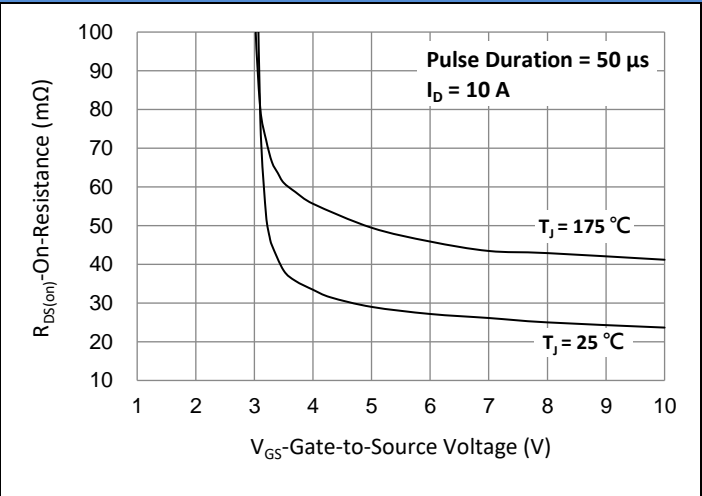
**Fig.3 On-Resistance vs. Drain Current**



**Fig.4 On-Resistance vs. Junction temperature**



**Fig.5 Capacitance vs. Drain-Source Voltage**



**Fig.6 On-Resistance vs. Gate-Source Voltage**

TYPICAL CHARACTERISTIC CURVES

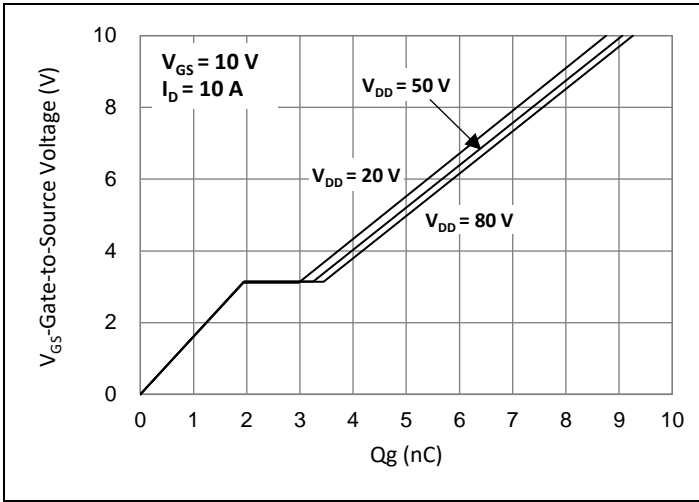


Fig.7 Gate-Charge Characteristics

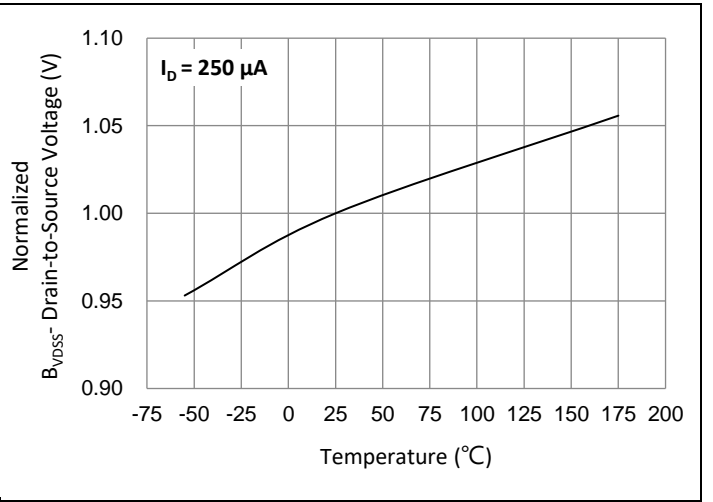


Fig.8 Breakdown Voltage Variation vs. Temperature

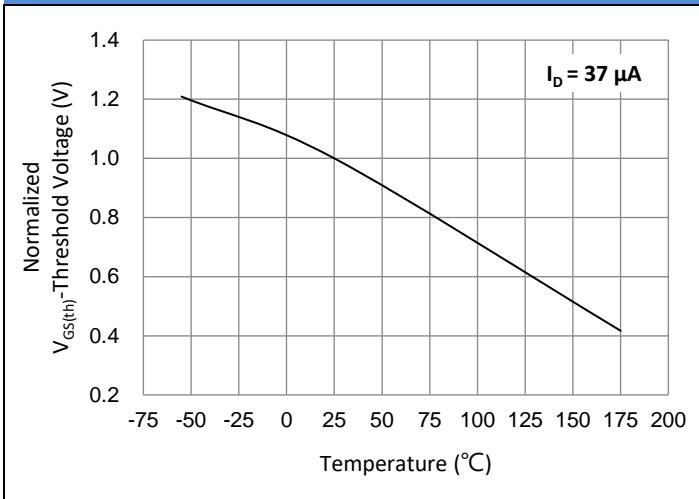


Fig.9 Threshold Voltage Variation with Temperature

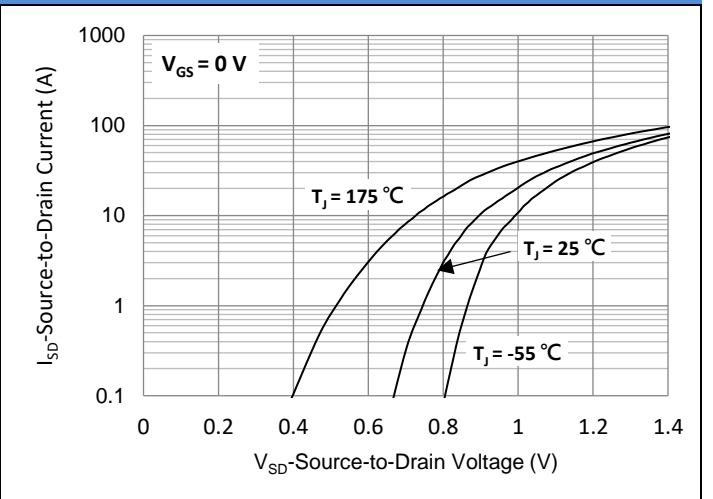


Fig.10 Source-Drain Diode Forward Voltage

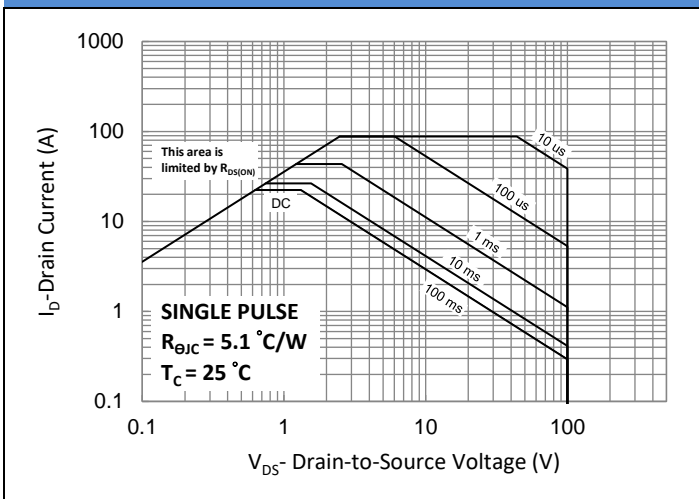


Fig.11 Maximum Safe Operating Area

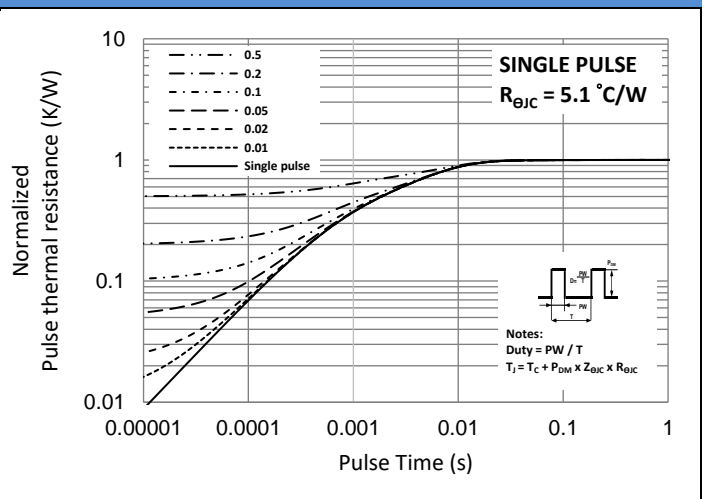
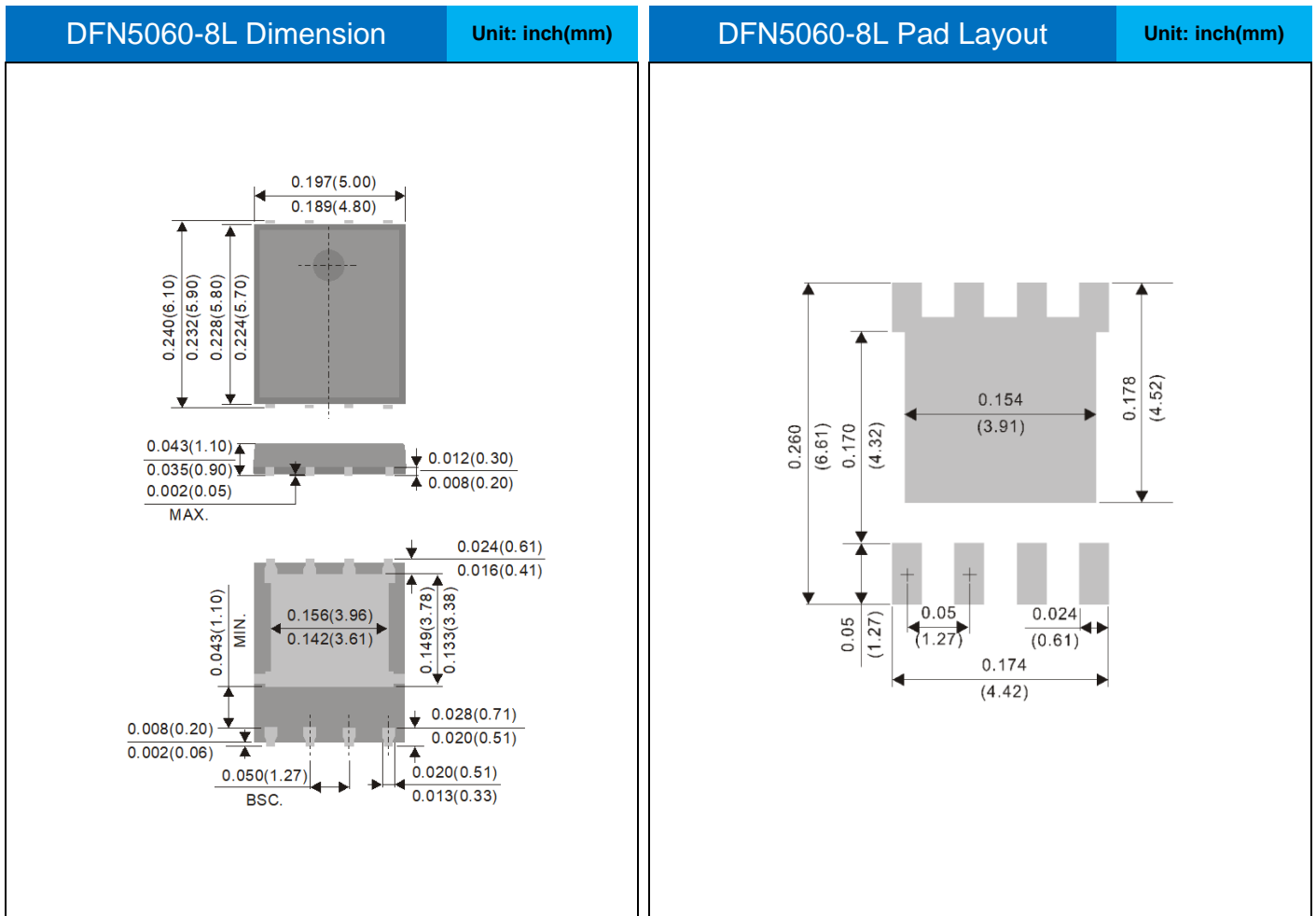


Fig.12 Normalized Transient Thermal Impedance

**Product and Packing Information**

Part No.	Package Type	Packing Type	Marking
PSMQC280N10LS2	DFN5060-8L	3000pcs / 13" reel	280N10LS

**Packaging Information & Mounting Pad Layout**



**Marking Diagram**

PJ  
280N10LS  
YWLL x

- Y** = Year Code
- W** = Week Code (A~Z)
- LL** = Lot Code (00~99)
- x** = Production Line Code

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