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DATA SHEET

PART NO.: L513GD-5

REV: <u>A/1</u>

PARA LIGHT ENGINEERING:

CUSTOMER'S APPROVAL:_

DRAWING NO. : DS-35-03-0528

DATE : 2010-07-05

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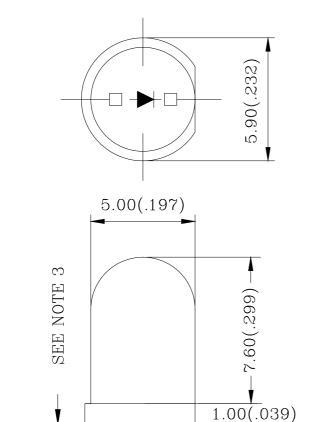
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PACKAGE DIMENSIONS



Cathode

2.54(.100)

 $5.00(.197)\pm0.5$

ITEM	MATERIALS
RESIN	Epoxy Resin
LEAD FRAME	Sn Plating iron Alloy

Note:

1.All Dimensions are in millimeters.

- 2.Tolerance is ±0.25mm(0.010 ") Unless otherwise specified.
- 3. Protruded resin under flange
 - is 1.5mm(0.059 ") max.

0.50(.020)



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FEATURES

- * High-brightness
- * High reliability
- * Low-voltage characteristics
- * Wide Viewing Angle
- * Pb FREE Products
- * RoHS Compliant

CHIP MATERIALS

- * Dice Material : GaP/GaP
- * Light Color : YELLOW GREEN
- * Lens Color : GREEN DIFFUSED

ABSOLUTE MAXIMUM RATING : (Ta = $25^{\circ}C$)

SYMBOL	PARAMETER	YELLOW GREEN	UNIT	
PD	Power Dissipation Per Chip	85	mW	
Vr	Reverse Voltage Per Chip	5	V	
laf	Continuous Forward Current Per Chip	30	mA	
IFP	Peak Forward Current Per Chip (Duty-0.1,1KHz)	120	mA	
—	Derating Linear From 25°C Per Chip	0.40	mA/°C	
Topr	Operating Temperature Range	-25°C to 95°C		
Tstg	Storage Temperature Range	-25°C to 95°C		

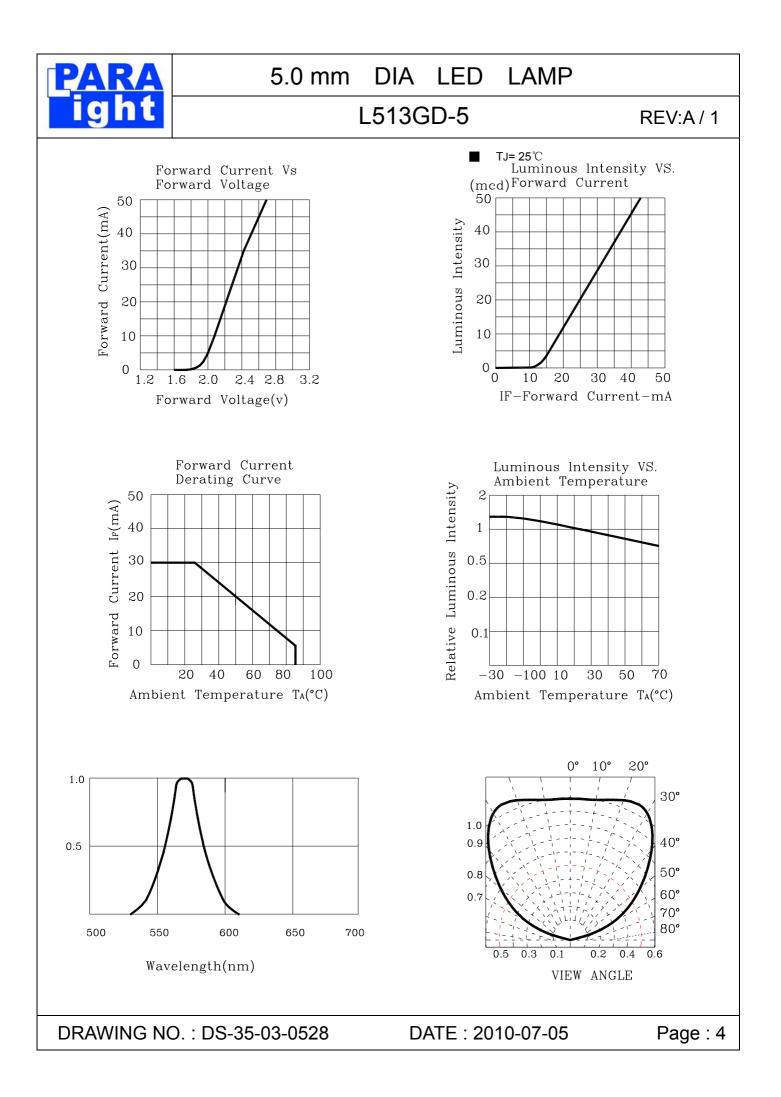
IFP Condition : Pulse Width≤10msec, 10% duty cycle

ELECTRO-OPTICAL CHARACTERISTICS : (Ta = 25°C)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
VF	Forward Voltage	IF = 20mA		2.2	2.8	V
IR	Reverse Current	VR = 5V			100	μA
λD	Dominant Wavelength	IF = 20mA		570		nm
Δλ	Spectral Line Half-Width	IF = 20mA		30		nm
201/2	Half Intensity Angle	IF = 20mA		110		deg
١v	Luminous Intensity	IF = 20mA		13		mcd

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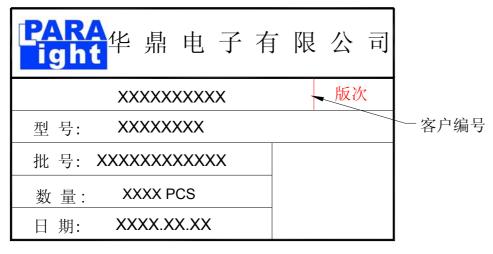




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Label Explanation



PARA NO. : L513GD-5 Refer to page 12

LOT NO. : E LL 4 7 0009 С F А В D Е A---E: For series number B---L: Local F: Foreign C---L: LAMP

- D----Year
- E---Month
- F--- Serial number

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•SOLDERING

-SOLDERING	9			
METHOD	SOLDERING CONDITIONS	REMARK		
DIP SOLDERING	Bath temperature: 260° C Immersion time: within 5 sec, 1 time	 Solder no closer than 3mm from the base of the package Using soldering flux," RESIN FLUX" is recommended. 		
SOLDERING IRON	Soldering iron: 30W or smaller Temperature at tip of iron: 300°C or lower Soldering time: within 3 sec.	transferred directly to the lead, hold the lead with a pair of tweezers while soldering)		
1) When solderi	ng the lead of LED in a condition that the	package is fixed with a panel (See Fig.1),		
be careful not	t to stress the leads with iron tip.			
2) When solderi	Panel (Fig. 1)	^I wries Fig.2) to avoid stressing the package.		
2) When soldering wire to the lead, work with a jig (See Fig.2) to avoid stressing the package.				
	a slight learance	(Fig. 2)		
Regarding tinning	g the leads, compound made of tin ,copp	er and sliver is proposed with the		

temperature of 260° C. The proportion of the alloyed solution is 95.5% tin, 3.5% copper, 0.5% silver. The time of tinning is 3 seconds.

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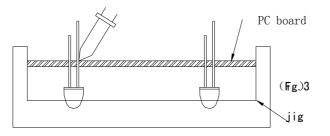
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5.0 mm DIA LED LAMP

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3) Similarly, when a jig is used to solder the LED to PC board, take care as much as possible to avoid stressing the leads (See Fig.3).



- Repositioning after soldering should be avoided as much as possible. If inevitable: select a best-suited method that assures the least stress to the LED.
- Lead cutting after soldering should be performed only after the LED temperature has returned to normal temperature.

• STORAGE

- 1) The LEDs should be stored at 30 $^\circ\!\!\!{\rm C}$ or less and 70% RH or less after being shipped from PARA and the storage life limit is 1 year .
- 2) PARA LED lead frames are comprised of a tin plated iron alloy. The surface may be affected by environments which contain corrosive gases and so on. Please avoid conditions which may cause the LEDs to corrode, tarnish or discolor. This corrosion or discoloration may cause difficulty during soldering operations. It is recommended that the LEDs be used as soon as possible.
- 3) Please avoid rapid changes in ambient temperature, especially, in high humidity environments where condensation can occur.



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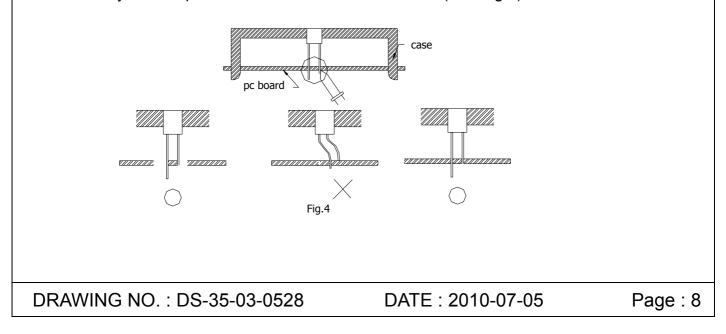
• STATIC ELECTRICITY

- Static electricity or surge voltage damages the LEDs.
 It is recommended that a wrist band and an anti-electrostatic glove be used when handling the LEDs.
- 2) All devices, equipment and machinery must be properly grounded. It is recommended that measures be taken against surge voltage to the LED mounting equipment.
- 3) When inspecting the final products in which LEDs were assembled, it is recommended to check whether the assembled LEDs are damaged by static electricity. To find static-damaged LEDs, perform a light-on test or a VF test at a lower current (below 1mA is recommended).
- 4) Damaged LEDs will show some unusual characteristics such as the leakage current remarkably increases, the forward voltage becomes lower, or the LEDs do not light at the low current.

Criteria : (VF>2.0V at IF=0.5mA)

•LED MOUNTING METHOD

1) When mounting the LED to a housing, as shown on Fig.4, ensure that the mounting holes on the PC board match the pitch of the leads correctly. Tolerance of dimensions of the respective components including the LEDs should be taken into account especially when designing the housing, PC board, etc. to prevent pitch misalignment between the leads and holes on PCB, the diameter of the holes should be slightly larger than the size of the lead. Alternatively, the shape of the holes could be made oval. (See Fig.4)





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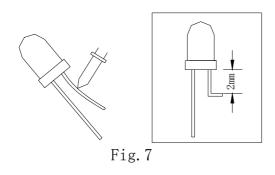
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 Use LEDs with stand-off (Fig.5) or the tube or spacer made of plastic (Fig.6) to position the LEDs.

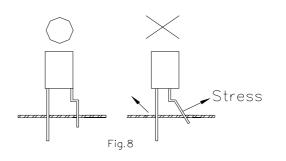


•FORMING LEAD

1) The lead should be bent at least 2mm away from the package. Bending should be performed with base fixed to a jig to pliers (Fig.7)



- 2) Forming lead should be carried our prior to soldering and never during or after soldering.
- Form the lead to ensure alignment between the leads and the holes on PCB, so that stress against the LED is prevented. (Fig.8)



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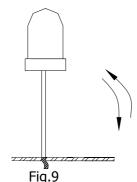
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•LEAD STRENGTH

1) Bend strength

Do not bend the lead more than twice. (Fig.9)



Tensile strength (@Room Temperature)
 If the force is 1kg or less, there will be no problem. (Fig.10)



• HEAT MANAGEMANT

- 1) Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when designing the system. The temperature increase is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.
- The operating current (IF) should be decided after considering the ambient maximum temperature of LEDs.



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•CHEMICAL RESISTANCE

- 1) Avoid exposure to chemicals as it may attack the LED surface and cause discoloration.
- When washing is required, refer to the following table for the proper chemical to be used. (Immersion time: within 3 minutes at room temperature.)

SOLVENT	ADAPTABILITY	
Freon TE	\odot	
Chlorothene	\times	
Isopropyl Alcohol	\odot	
Thinner	\times	
Acetone	\times	
Trichloroethylene	×	

NOTE: Influences of ultrasonic cleaning of the LED resin body differ depending on factors such as the oscillator output, size of the PC board and the way in which the LED is mounted. Therefore, ultrasonic cleaning should only be performed by confirming an ultrasonic cleaning trial run.

 \odot --Usable X--Do not use.

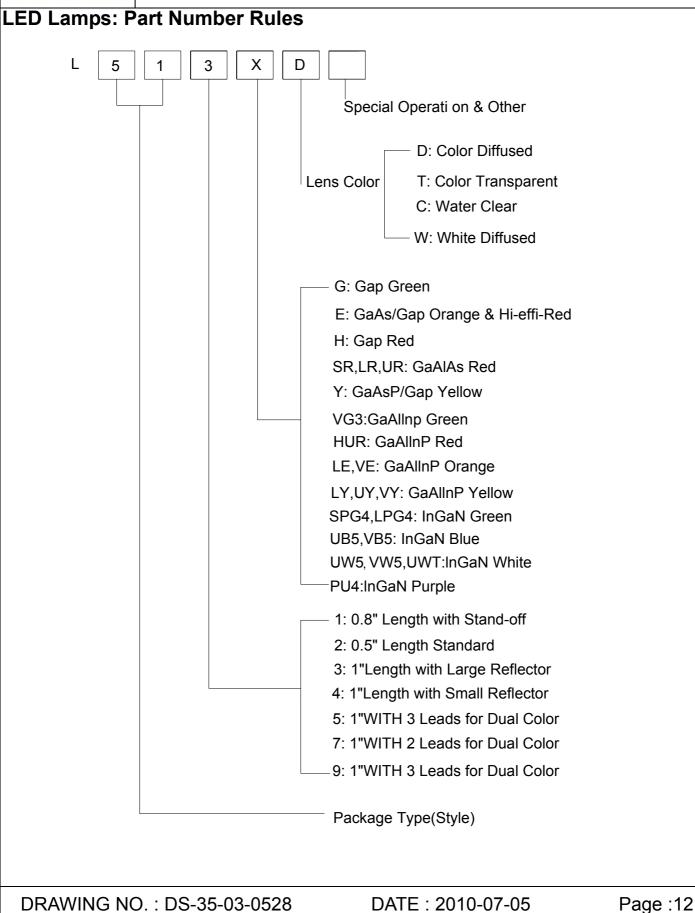
•OTHER CONSIDERTIONS

- 1) Care must be taken to ensure that the reverse voltage will not exceed the absolute maximum rating when using the LEDs with matrix drive.
- 2) The LEDs described in this data sheet are intended to be used for ordinary electronic equipment (such as office equipment, communications equipment, measurement instruments and household appliances). Consult PARA's sales staff in advance for information on the applications in which exceptional quality and reliability are required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as for airplanes, spacecraft, automobiles, traffic control equipment etc).
- 3) The formal specifications must be exchanged and signed by both parties before large volume purchase begins.



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