


Master protocol chip - IBS IPMS 3 QFP - 2751807

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The INTERBUS protocol microcontroller IPMS-3 is a CMOS gate array. The IPMS-3 is used with a variety of processors



Key Commercial Data

Packing unit	66 pc
Minimum order quantity	66 pc
GTIN	 4 017918 272364
GTIN	4017918272364
Sales Key	14

Technical data

Dimensions

Width	19.4 mm
Depth	25.4 mm

General

IC power supply voltage	5 V DC ±10 % (DC)
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Environmental Product Compliance

China RoHS	Environmentally Friendly Use Period = 50 years
	For details about hazardous substances go to tab "Downloads", Category "Manufacturer's declaration"

Classifications

eCl@ss

eCl@ss 10.0.1	27242605
eCl@ss 4.0	27250200
eCl@ss 4.1	27250200

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Classifications

eCl@ss

eCl@ss 5.0	27249200
eCl@ss 5.1	27249200
eCl@ss 6.0	27242600
eCl@ss 7.0	27242608
eCl@ss 8.0	27242608
eCl@ss 9.0	27242605

ETIM

ETIM 2.0	EC001433
ETIM 3.0	EC001601
ETIM 4.0	EC001601
ETIM 5.0	EC001604
ETIM 6.0	EC001601
ETIM 7.0	EC001601

UNSPSC

UNSPSC 6.01	43172015
UNSPSC 7.0901	43201404
UNSPSC 11	43172015
UNSPSC 12.01	43201404
UNSPSC 13.2	32151602
UNSPSC 18.0	32151602
UNSPSC 19.0	32151602
UNSPSC 20.0	32151602
UNSPSC 21.0	32151602

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