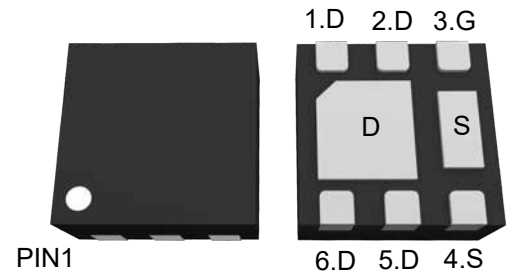


### Features

- Low gate charge and  $R_{DS(ON)}$
- $V_{DS} = -20V, I_D = -12A$   
 $R_{DS(on)} < 22m\Omega @ V_{GS} = -4.5V$

### DFN2x2-6L

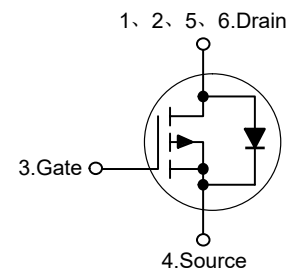


**Marking Code: 12P20**

### Applications

- Load switch
- PWM application

### Schematic Diagram



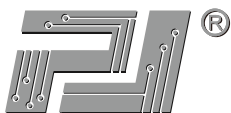
### Absolute Maximum Ratings

Ratings at 25°C Case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$-I_D$	12	A
Drain Current-Pulsed <sup>Note1</sup>	$-I_{DM}$	48	A
Maximum Power Dissipation	$P_D$	4.5	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Case <sup>Note2</sup>	$R_{\theta JC}$	27.8	°C/W
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# PJM12P20DF

## P-Channel Enhancement Mode Power MOSFET

### Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$-V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	20	--	--	V
Zero Gate Voltage Drain Current	$-I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$	--	--	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$	--	--	$\pm 100$	nA
Gate Threshold Voltage <sup>Note3</sup>	$-V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	0.4	0.7	1	V
Drain-Source On-Resistance <sup>Note3</sup>	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-10A$	--	17	22	m $\Omega$
		$V_{GS}=-2.5V, I_D=-5A$	--	22	30	m $\Omega$
Forward Transconductance <sup>Note3</sup>	$g_{FS}$	$V_{DS}=-5V, I_D=-6.7A$	--	18	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	--	2000	--	pF
Output Capacitance	$C_{oss}$		--	242	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	231	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=-10V, I_D=-12A$ $V_{GEN}=-4.5V, R_G=2.5\Omega$	--	10	--	nS
Turn-on Rise Time	$t_r$		--	31	--	nS
Turn-off Delay Time	$t_{d(off)}$		--	28	--	nS
Turn-off Fall Time	$t_f$		--	8	--	nS
Total Gate Charge	$Q_g$	$V_{DS}=-10V, I_D=-6A, V_{GS}=-4.5V$	--	15.3	--	nC
Gate-Source Charge	$Q_{gs}$		--	2.2	--	nC
Gate-Drain Charge	$Q_{gd}$		--	4.4	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	$-V_{SD}$	$V_{GS}=0V, I_S=-12A$	--	--	1.2	V
Diode Forward Current <sup>Note2</sup>	$-I_S$		--	--	12	A

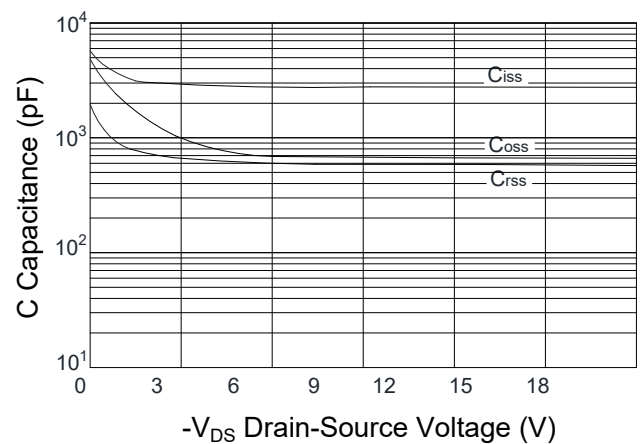
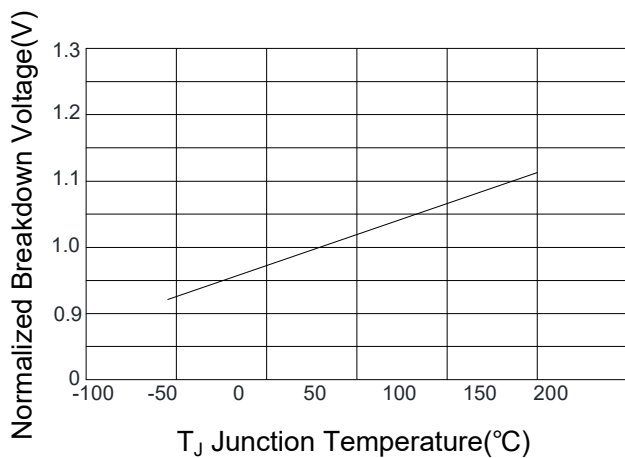
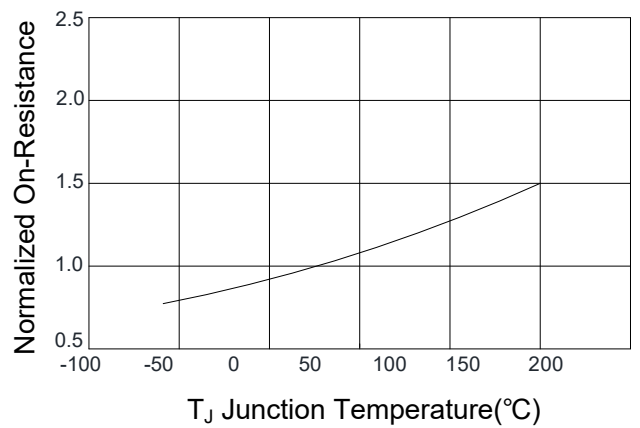
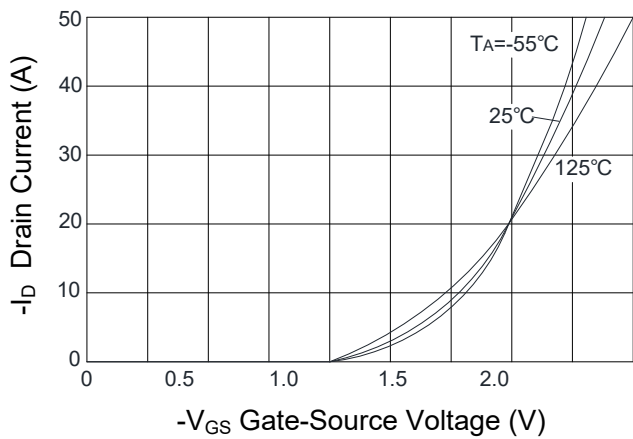
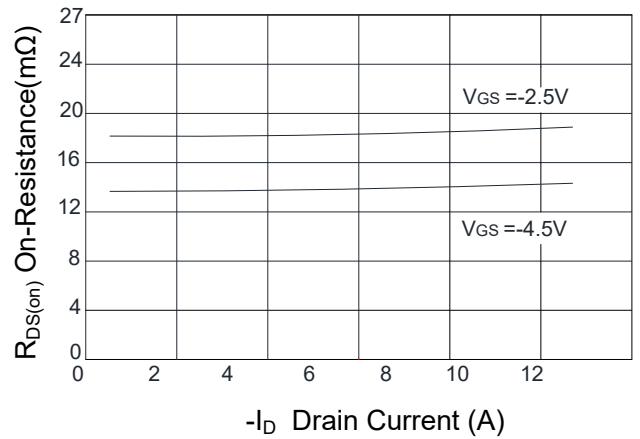
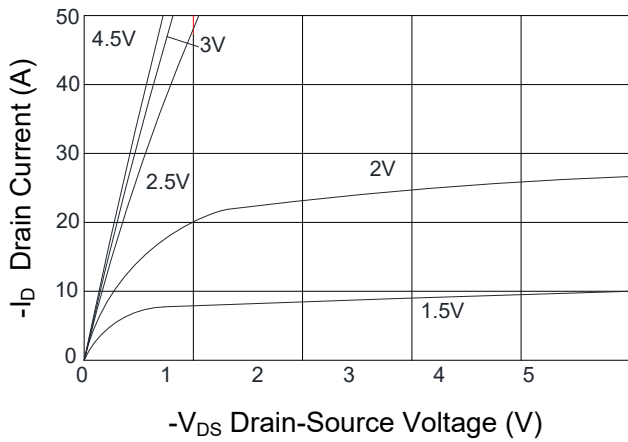
- Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.  
 2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.  
 3. Pulse Test: Pulse width  $\leq 300\mu s$ , duty cycles  $\leq 2\%$ .



# PJM12P20DF

## P-Channel Enhancement Mode Power MOSFET

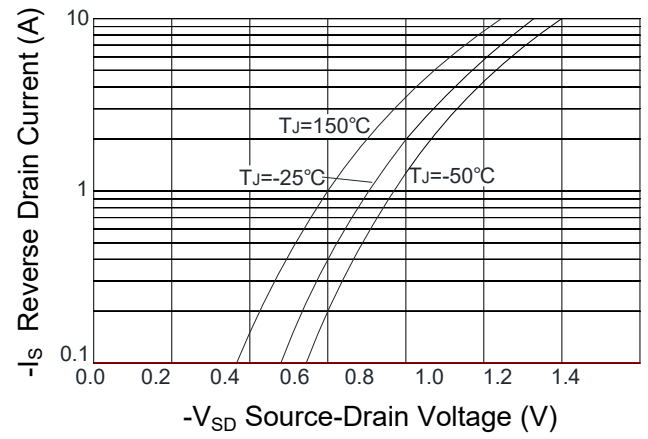
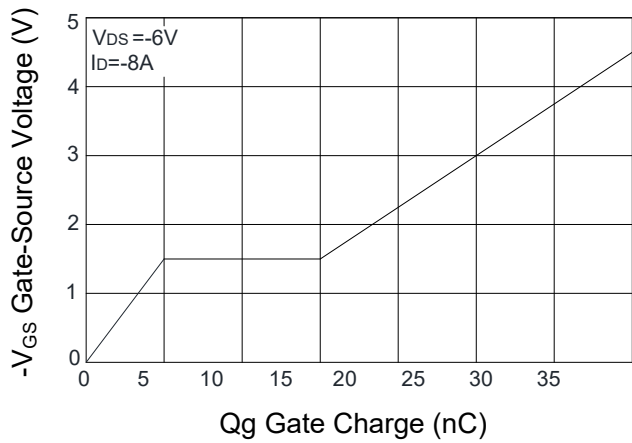
### Typical Characteristic Curves





# PJM12P20DF

## P-Channel Enhancement Mode Power MOSFET





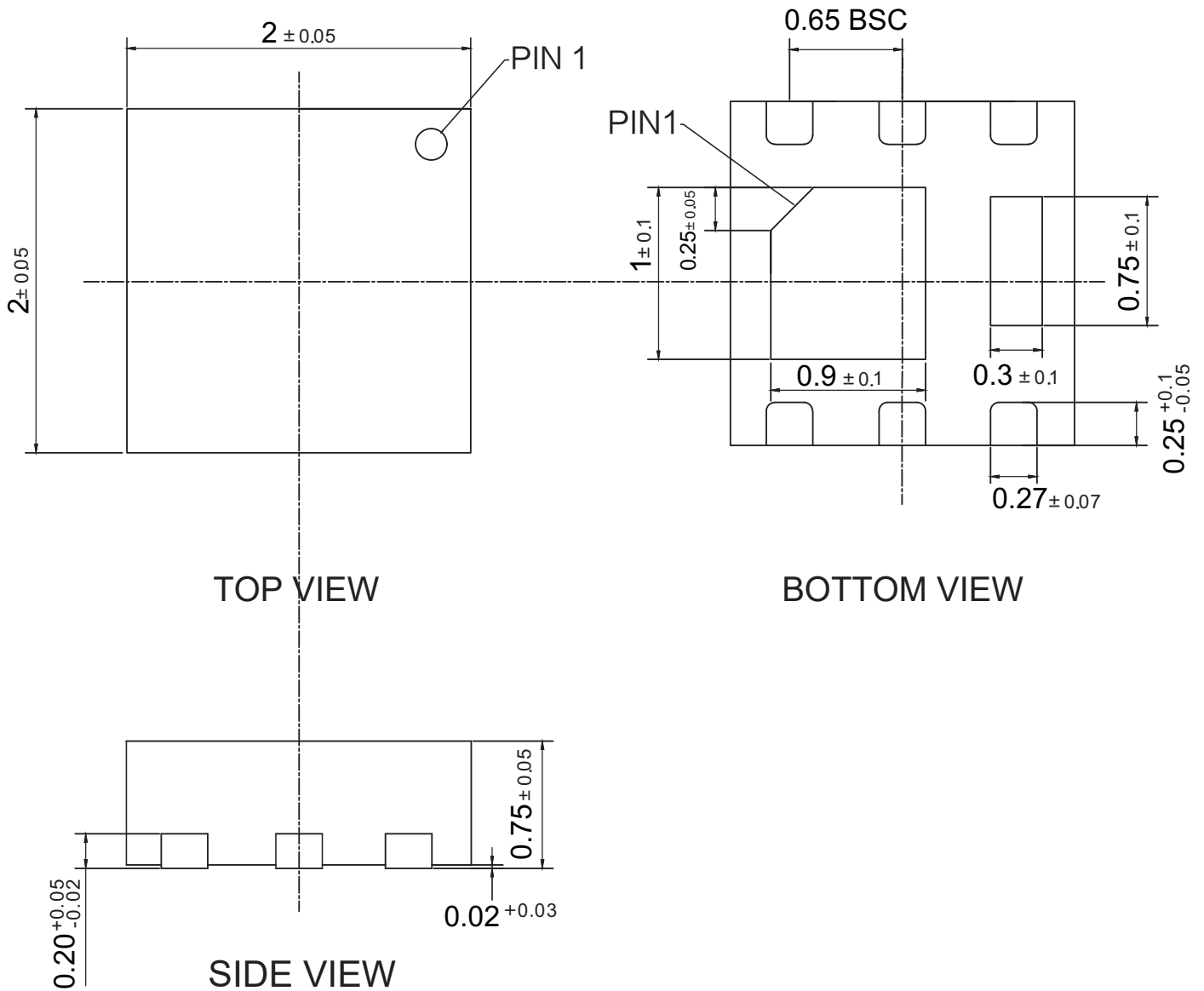
# PJM12P20DF

## P-Channel Enhancement Mode Power MOSFET

### Package Outline

DFN2x2-6L-0001

Dimensions in mm



### Ordering Information

Device	Package	Shipping
PJM12P20DF	DFN2x2-6L	3,000PCS/Reel&7inches

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