



# PJM20N60TE

## N-Channel Enhancement Mode Power MOSFET

### Product Summary

- $V_{DS} = 60V, I_D = 20A$
- $R_{DS(on)} < 33m\Omega @ V_{GS} = 10V$
- $R_{DS(on)} < 45m\Omega @ V_{GS} = 4.5V$

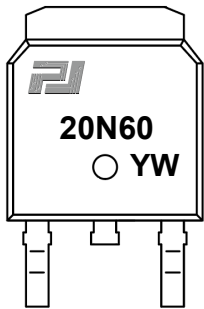
### Features

- Advanced Trench Technology
- 100% Avalanche Tested
- RoHS and Reach Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

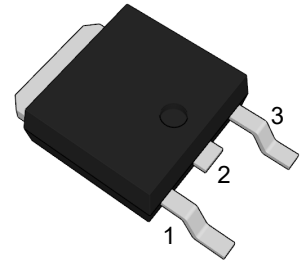
### Application

- Load Switch
- PWM Application
- Power management

### Marking Code



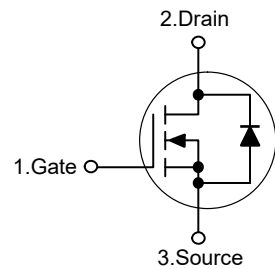
### TO-252



(Top View)

Pin	Description
1	Gate
2	Drain
3	Source

### Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	20	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	80	A
Single Pulse Avalanche Energy <sup>Note2</sup>	$E_{AS}$	25	mJ
Maximum Power Dissipation	$P_D$	27.7	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.5	°C/W
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### Electrical Characteristics

( $T_J=25^{\circ}\text{C}$  unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$	--	--	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	$\pm 100$	nA
Gate Threshold Voltage <sup>Note3</sup>	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
Drain-Source On-Resistance <sup>Note3</sup>	$R_{DS(on)}$	$V_{GS}=10V, I_D=10A$	--	26	33	m $\Omega$
		$V_{GS}=4.5V, I_D=5A$	--	31	45	m $\Omega$
Forward Transconductance <sup>Note3</sup>	$g_{FS}$	$V_{DS}=5V, I_D=3A$	--	9	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V, f=1\text{MHz}$	--	1180	--	pF
Output Capacitance	$C_{oss}$		--	56	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	50	--	pF
Gate Resistance	$R_g$	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	--	1.6	--	$\Omega$
Total Gate Charge	$Q_g$	$V_{DS}=30V, I_D=10A,$ $V_{GS}=10V$	--	20.3	--	nC
Gate-Source Charge	$Q_{gs}$		--	3.7	--	nC
Gate-Drain Charge	$Q_{gd}$		--	5.3	--	nC
<b>Switching Characteristics</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30V, I_D=20A,$ $V_{GS}=10V, R_{GEN}=1.8\Omega$	--	7.6	--	nS
Turn-on Rise Time	$t_r$		--	20	--	nS
Turn-off Delay Time	$t_{d(off)}$		--	15	--	nS
Turn-off Fall Time	$t_f$		--	24	--	nS
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=20A$	--	--	1.2	V
Diode Forward Current	$I_S$		--	--	20	A

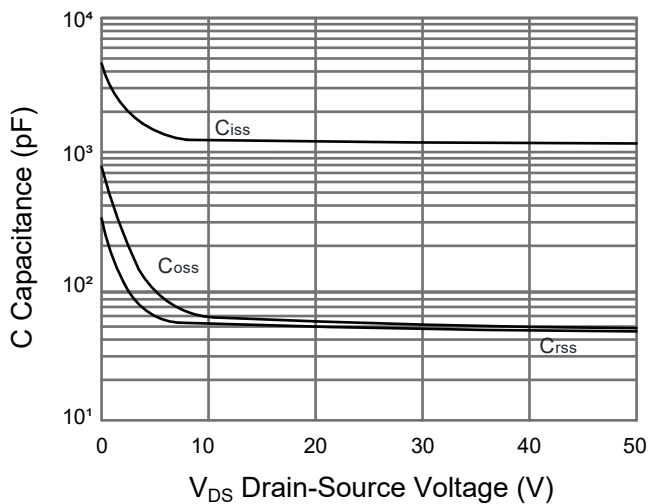
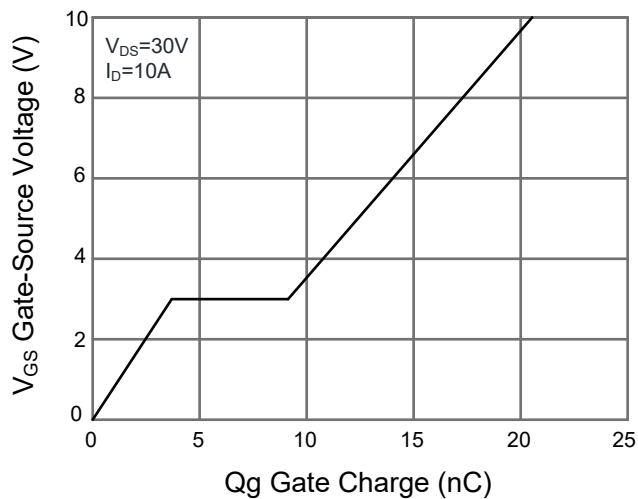
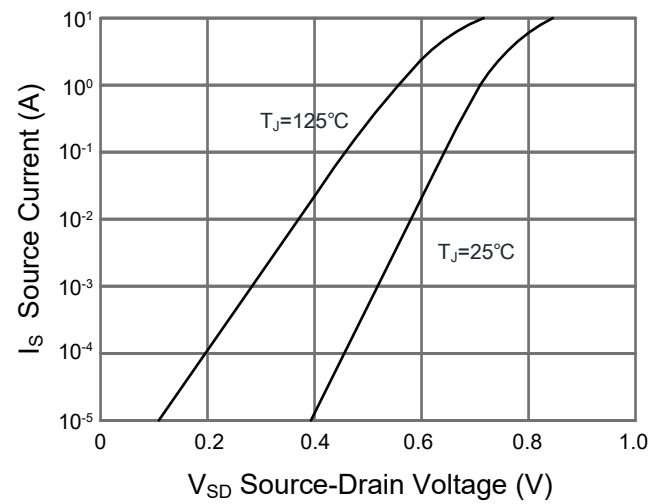
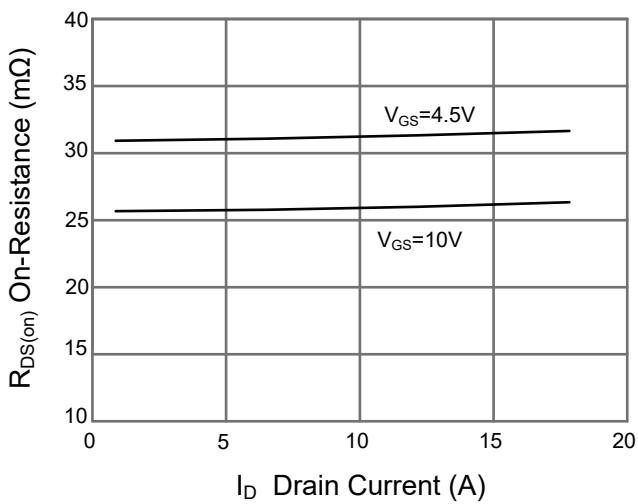
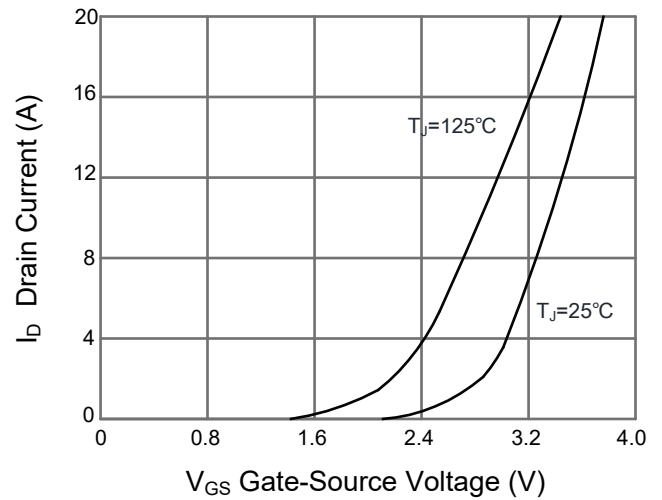
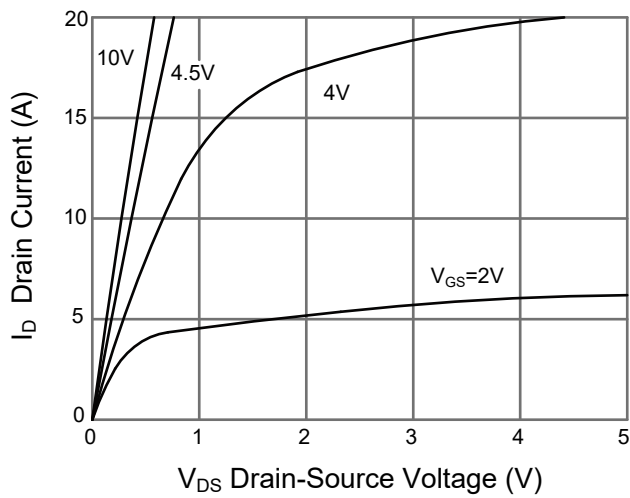
- Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature  
2. EAS condition:  $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, R_G=25\Omega, L=0.5\text{mH}, I_{AS}=6.1A$   
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

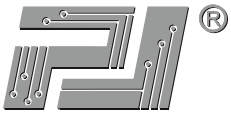


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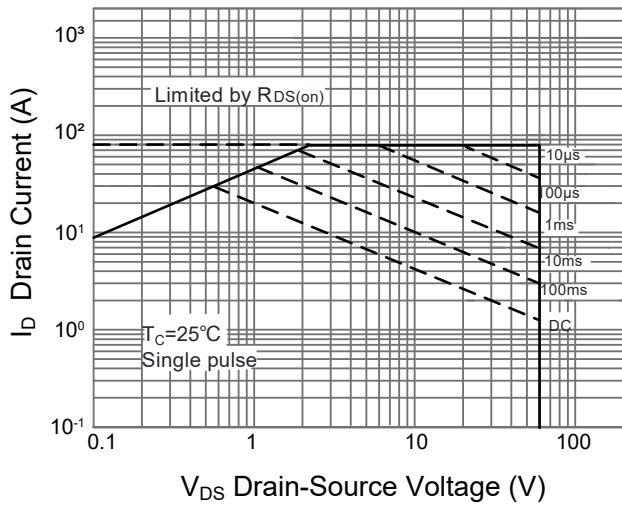
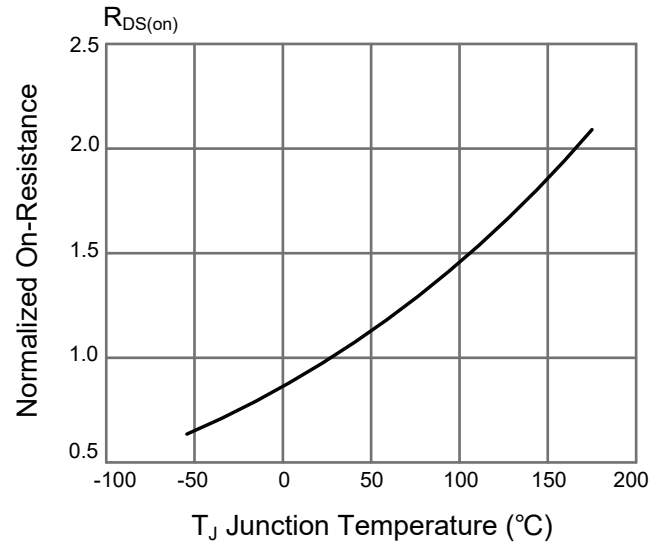
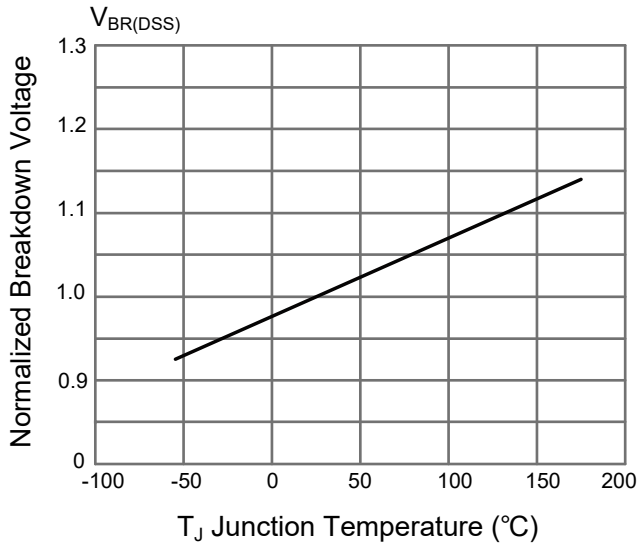
### Typical Characteristic Curves





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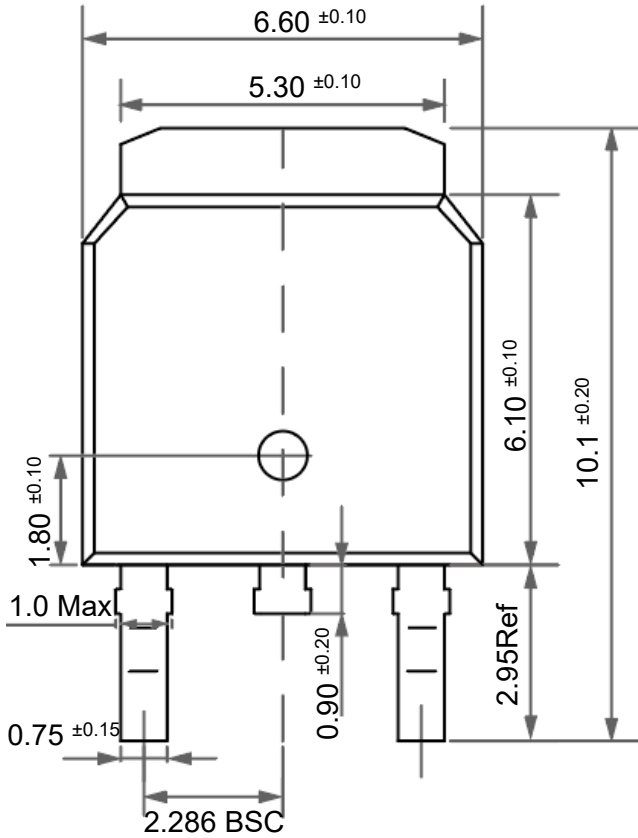
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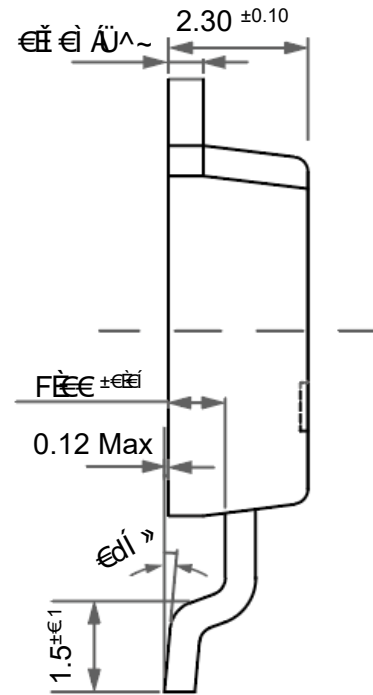
### Package Outline

TO-252

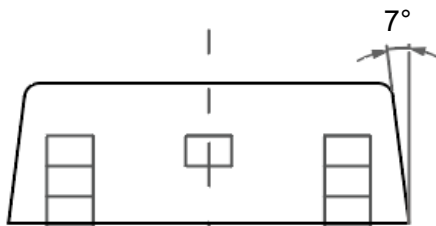
Dimensions in mm



Front View



Side View



Bottom View

### Ordering Information

Device	Package	Shipping
PJM20N60TE	TO-252	2,500PCS/Reel&13inches

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