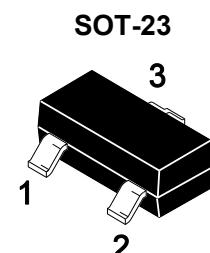


PJM2301PSA

P- Enhancement Mode Field Effect Transistor

Features

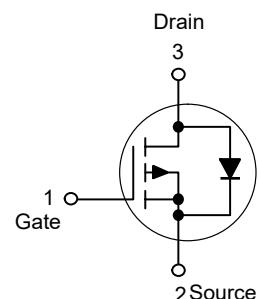
- ◆ High power and current handing capability
- ◆ Halogen free product is acquired
- ◆ Surface mount package



1. Gate 2. Source 3. Drain

Marking: M01

Schematic Diagram



Applications

- ◆ Battery protection
- ◆ Load switch
- ◆ Power management

Absolute Maximum Ratings

Ratings at $T_A = 25^\circ\text{C}$ unless otherwise specified.

Parameter	Symbol	Value	Units
Drain-Source Voltage	$-V_{DS}$	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	$-I_D$	2.8	A
Pulsed Drain Current ^{Note1}	$-I_{DM}$	10	
Power Dissipation	P_D	1.25	W
Junction and Storage Temperature Range	T_J, T_{STG}	150, -55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ.	Units
Maximum Junction-to-Ambient ^{Note2}	$R_{\theta JA}$	100	°C/W



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P- Enhancement Mode Field Effect Transistor

Electrical Characteristics

$T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$-V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	20			V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	$-I_{DSS}$	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
Gate-Source Threshold Voltage ^{Note3}	$-V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	0.4		1	V
Drain-Source On-State Resistance ^{Note3}	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$		90	110	$\text{m}\Omega$
		$V_{GS} = -2.5 \text{ V}, I_D = -2 \text{ A}$		110	140	
Forward Transconductance ^{Note3}	g_{FS}	$V_{DS} = -5 \text{ V}, I_D = -2.8 \text{ A}$		2		S
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		405		pF
Output Capacitance	C_{oss}			75		
Reverse Transfer Capacitance	C_{rss}			55		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$		5.5	10	nC
				3.3	6	
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_D = -2.8 \text{ A}$		0.7		
Gate-Drain Charge	Q_{gd}			1.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega, I_D = -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_G = 1 \Omega$		11	20	ns
Rise Time	t_r			35	60	
Turn-Off Delay Time	$t_{d(off)}$			30	50	
Fall Time	t_f			10	20	
Source-Drain Diode characteristics						
Continuous Source-Drain Diode Current	$-I_S$				1.3	A
Pulse Diode Forward Current ^{Note1}	$-I_{SM}$				10	
Body Diode Voltage	$-V_{SD}$	$I_S = -1 \text{ A}$	0.5		1.2	V

Notes:

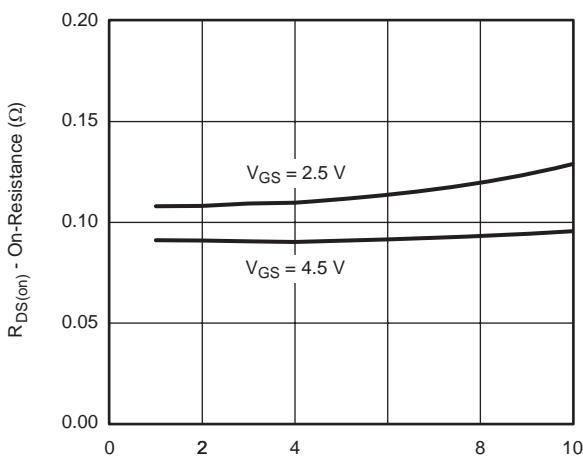
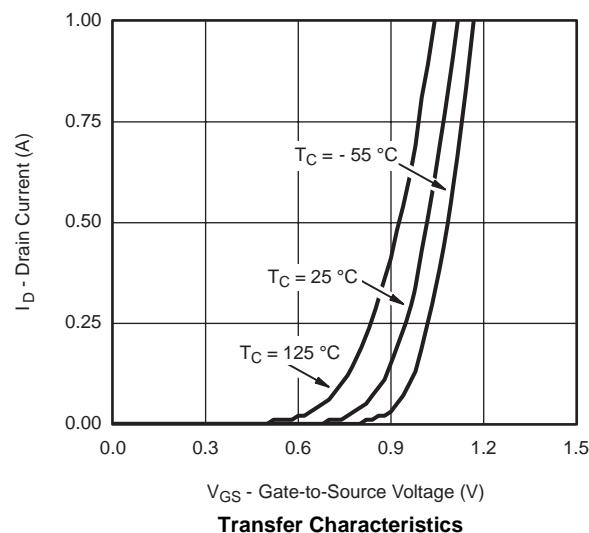
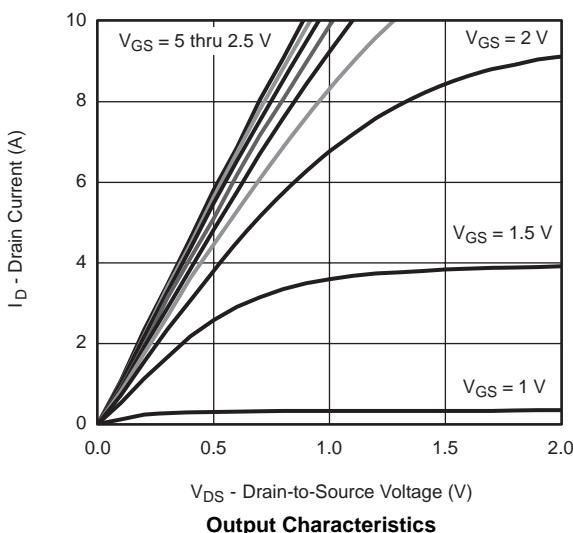
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10 \text{ sec.}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.



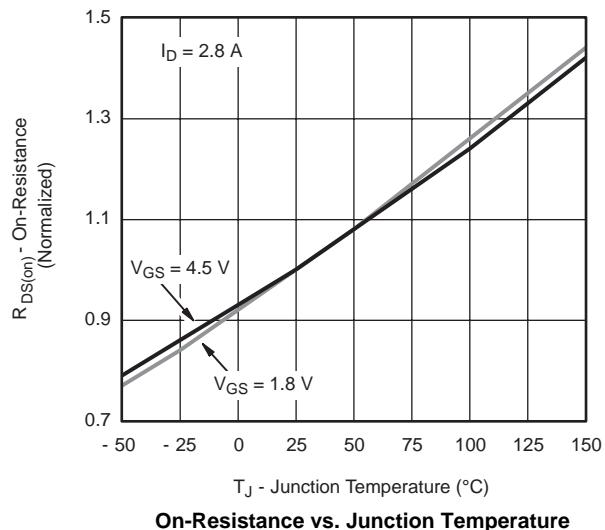
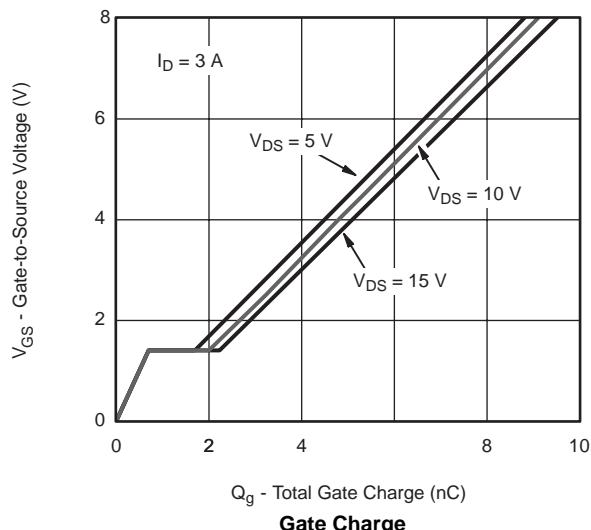
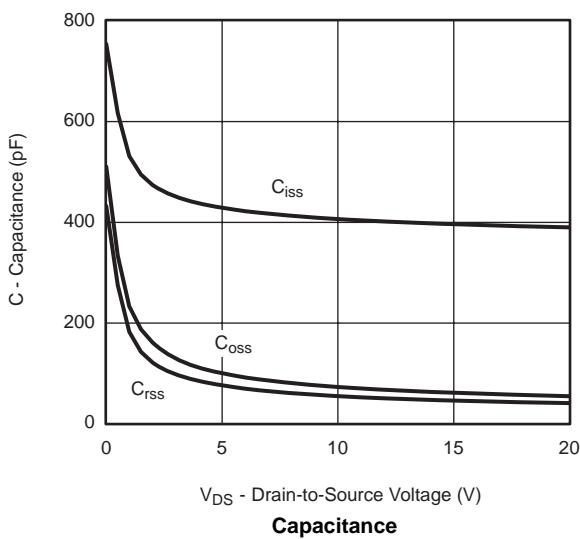
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Typical Characteristics Curves



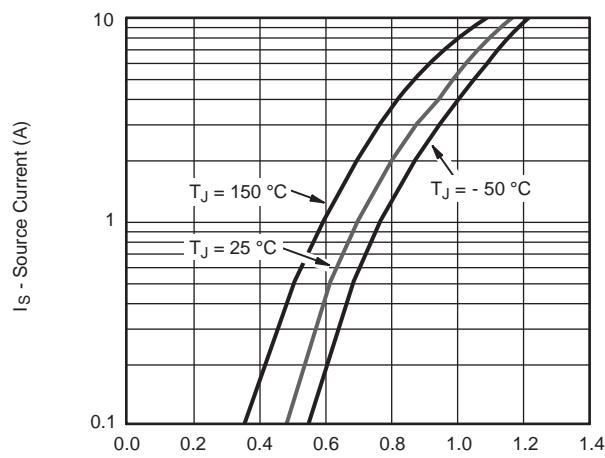
On-Resistance vs. Drain Current and Gate Voltage



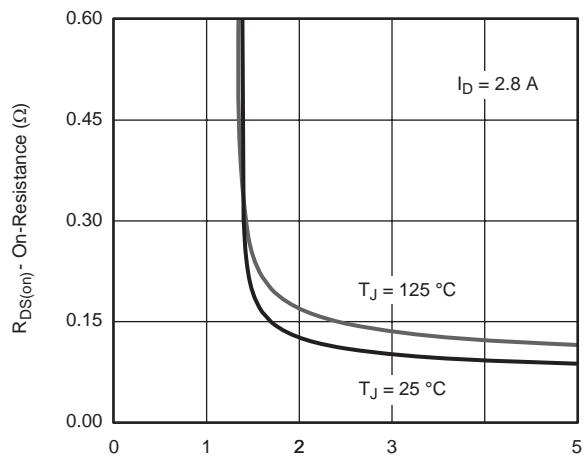


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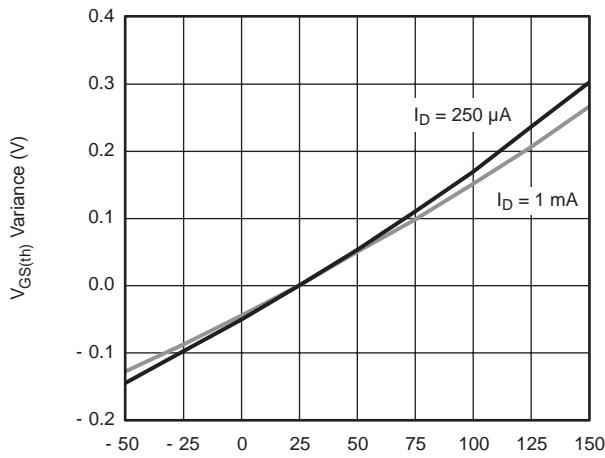
P- Enhancement Mode Field Effect Transistor



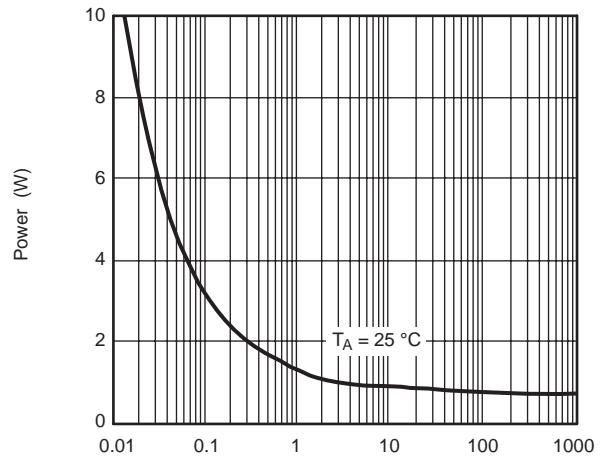
Source-Drain Diode Forward Voltage



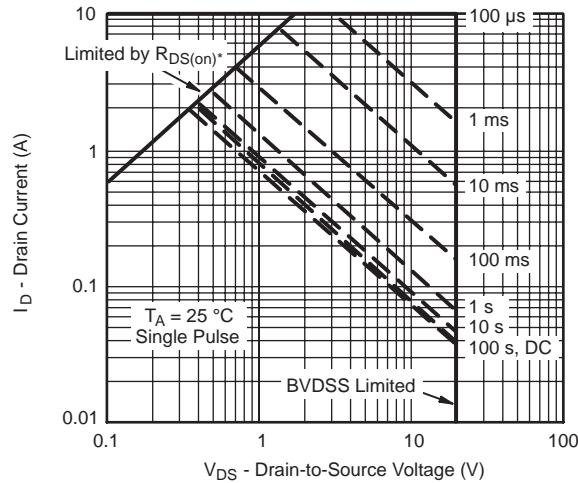
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power

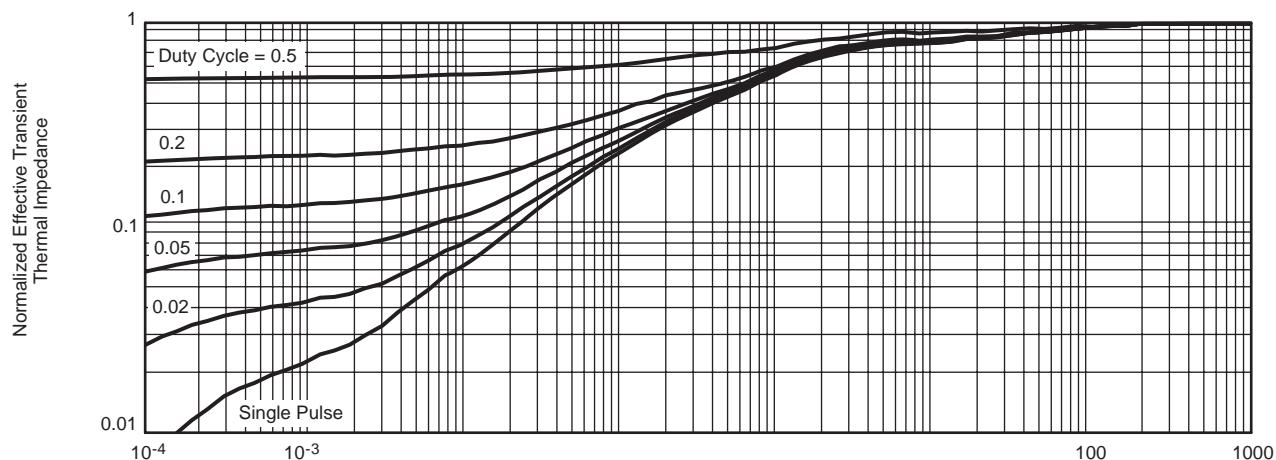


Safe Operating Area

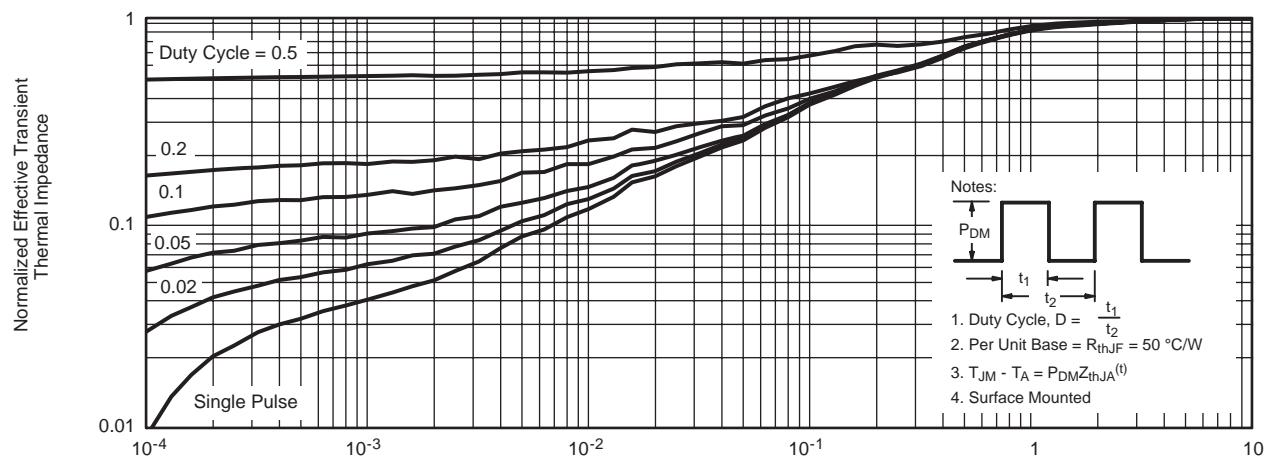


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P- Enhancement Mode Field Effect Transistor



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

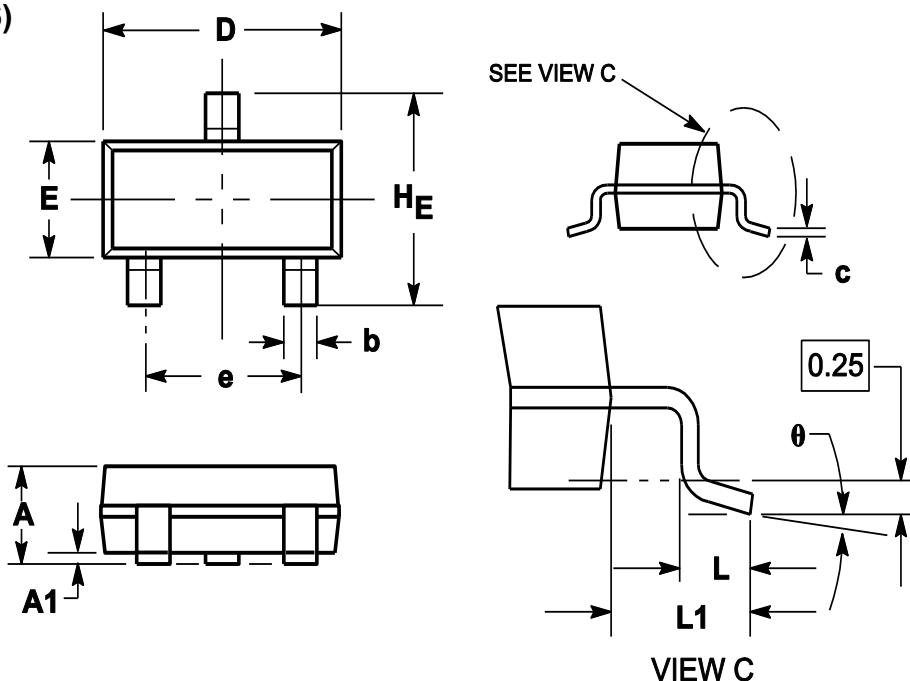


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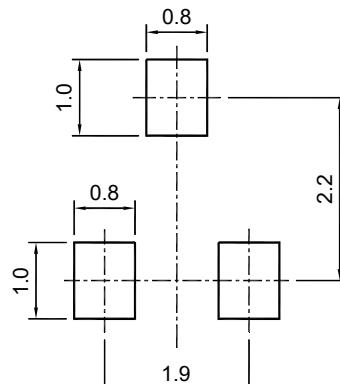
P- Enhancement Mode Field Effect Transistor

Package Outline

SOT-23 (TO-236)



Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.900	1.025	1.150
A1	0.000	0.050	0.100
b	0.300	0.400	0.500
c	0.080	0.115	0.150
D	2.800	2.900	3.000
E	1.200	1.300	1.400
H _E	2.250	2.400	2.550
e	1.800	1.900	2.000
L1	0.550REF		
L	0.300		0.500
θ	0°		8°



SOT-23 (TO-236)

Recommended soldering pad

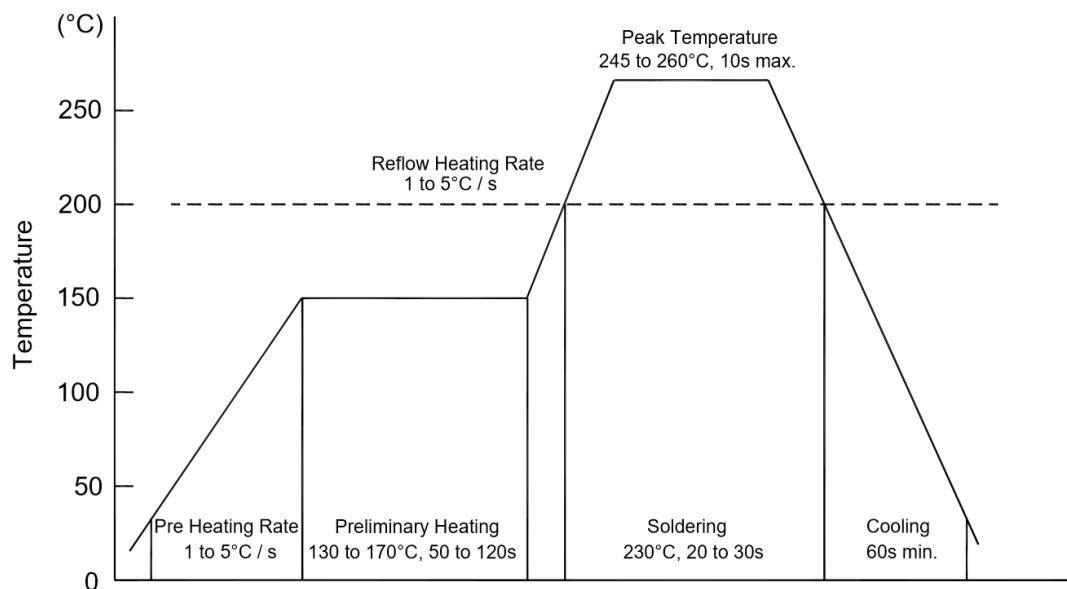
Ordering Information

Device	Package	Shipping
PJM2301PSA	SOT-23	3000/Reel&Tape(7inch)



Conditions of Soldering And Storage

◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

◆ Conditions of hand soldering

- Temperature: 370 °C
- Time: 3s max.
- Times: one time

◆ Storage conditions

- **Temperature**
5 to 40 °C
- **Humidity**
30 to 80% RH
- **Recommended period**
One year after manufacturing

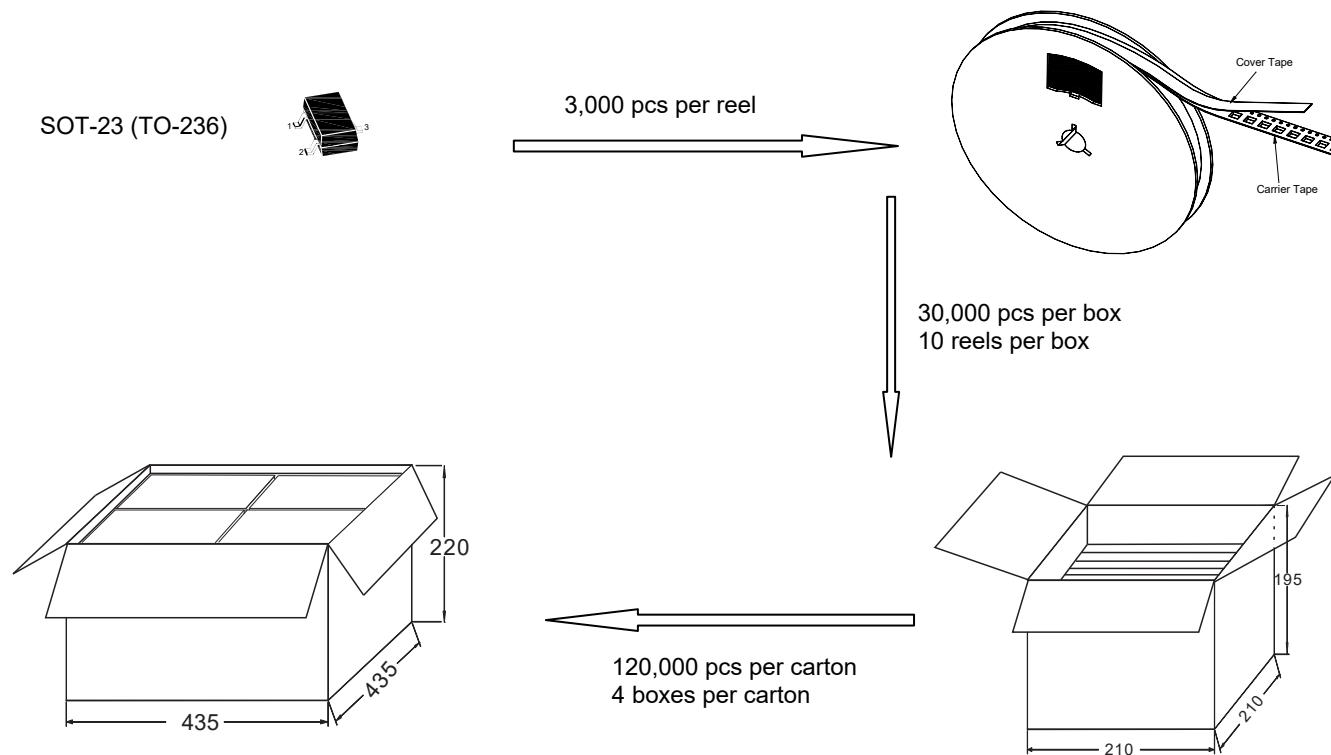


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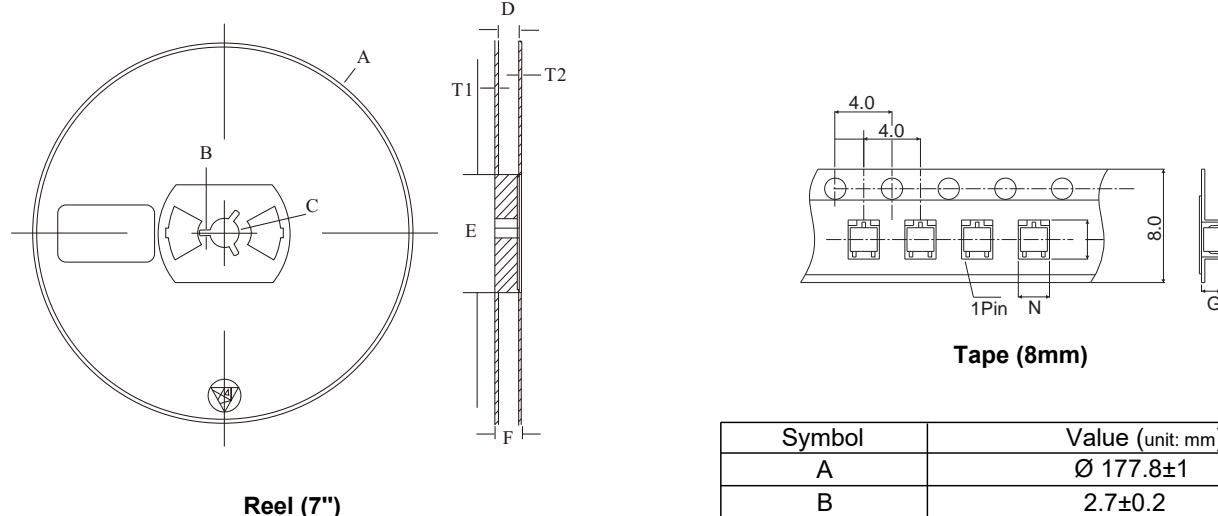
P- Enhancement Mode Field Effect Transistor

Package Specifications

◆ The method of packaging



◆ Embossed tape and reel data



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