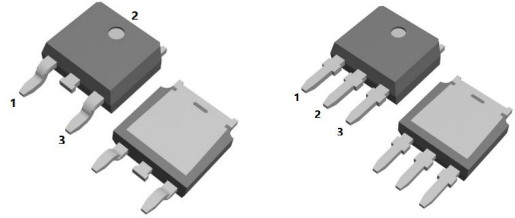
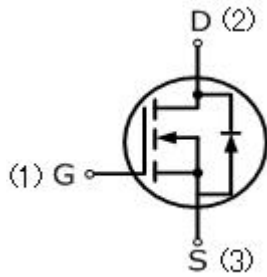


8N50(G,D)

8 Amps,500 Volts N-CHANNEL Power MOSFET

FEATURE

- 8A,500V, $R_{DS(ON)MAX}=0.90\ \Omega$ @ $V_{GS}=10V/4A$
- Low gate charge
- Low C_{iss}
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-252-2L
8N50G

TO-251-3L
8N50D

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	8N50(G,D)	UNIT
Drain-Source Voltage	V_{DSS}	500	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current	I_D	8	A
Pulsed Drain Current(Note1)	I_{DM}	32	
Single Pulse Avalanche Energy (Note 2)	E_{AS}	331	mJ
Reverse Diode dV/dt (Note 3)	dv/dt	5	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55to+150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	260	$^\circ\text{C}$

Thermal Characteristics

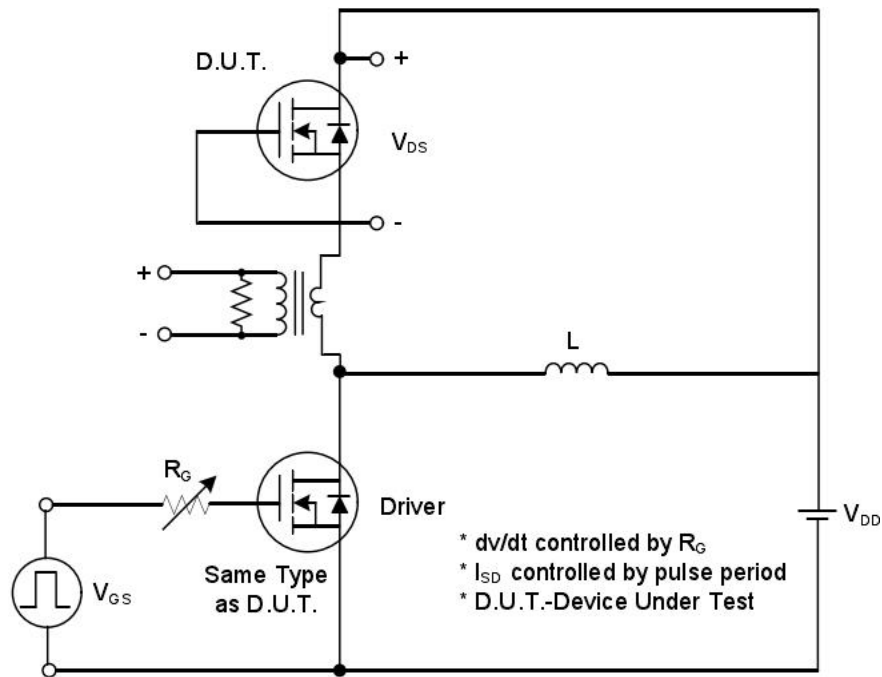
Parameter	Symbol	8N50(G,D)	Units
Thermal resistance , Channel to Case	$R_{th(ch-c)}$	1.25	$^\circ\text{C}/\text{W}$
Maximum Power Dissipation	P_D	100	W

Electrical Characteristics (T_c=25°C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	500	—	—	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V	—	—	1	uA
Gate-Body Leakage Current, Forward	I _{GSSF}	V _{GS} =30V, V _{DS} =0V	—	—	100	nA
Gate-Body Leakage Current, Reverse	I _{GSSR}	V _{GS} =-30V, V _{DS} =0V	—	—	-100	nA
On Characteristics						
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	—	4.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =4A	—	0.7	0.9	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	—	1118	—	pF
Output Capacitance	C _{oss}		—	88	—	pF
Reverse Transfer Capacitance	C _{rss}		—	26	—	pF
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	V _{DD} =250V, I _D =8A, R _G =10Ω	—	14.4	—	ns
Turn-On Rise Time	t _r		—	1.2	—	ns
Turn-Off Delay Time	t _{d(off)}		—	29.8	—	ns
Turn-Off Fall Time	t _f		—	3.6	—	ns
Total Gate Charge	Q _g	V _{DS} =400V, I _D =8A, V _{GS} =10V	—	20.3	—	nC
Gate-Source Charge	Q _{gs}		—	6.4	—	nC
Gate-Drain Charge	Q _{gd}		—	5	—	nC
Drain-Source Body Diode Characteristics and Maximum Ratings						
Diode Forward Voltage	V _{SD}	I _S =4A, V _{GS} =0V	—	—	1.2	V
Reverse Recovery Time	t _{rr}	V _{DS} =30V, I _S =1A, dI _F /dt=100A/us, (Note3)	—	145	—	ns
Reverse Recovery Charge	Q _{rr}		—	406.4	—	nC

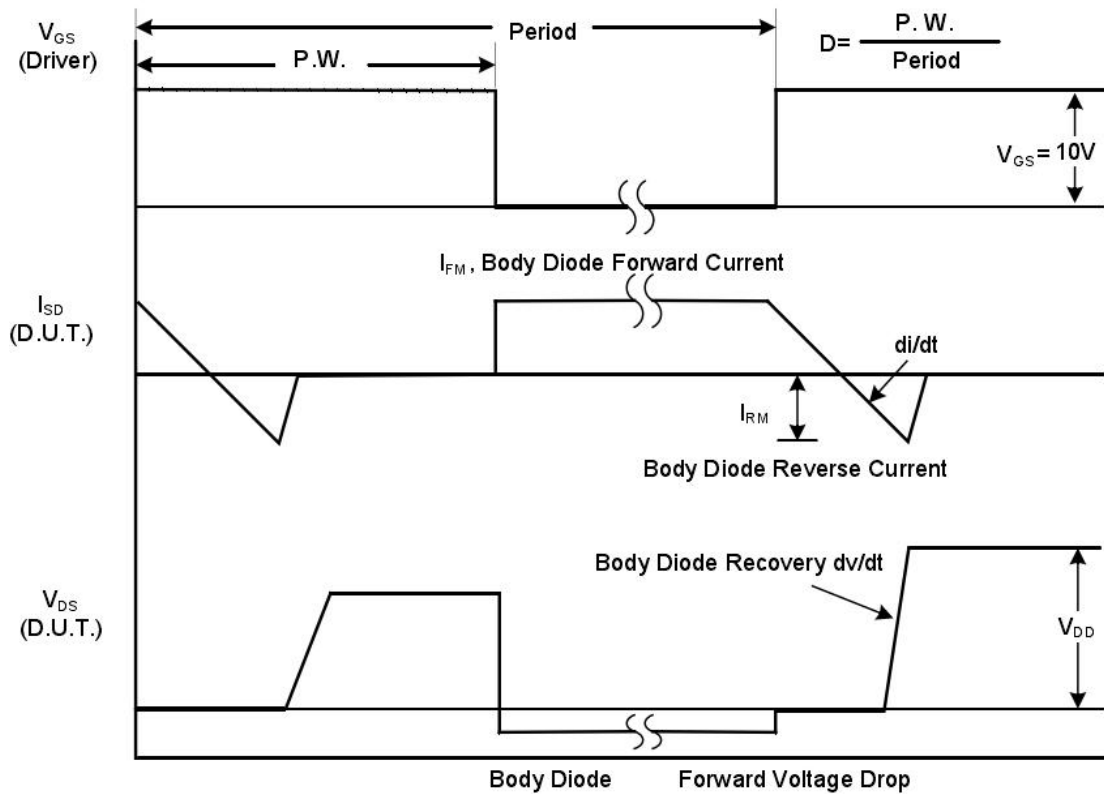
Notes

1. Repetitive Rating: pulse width limited by maximum junction temperature.
2. V_{DD}=50V, L=10mH, R_g=25Ω, starting T_J=25°C.
3. Pulse width ≤ 300us; duty cycle ≤ 2%.

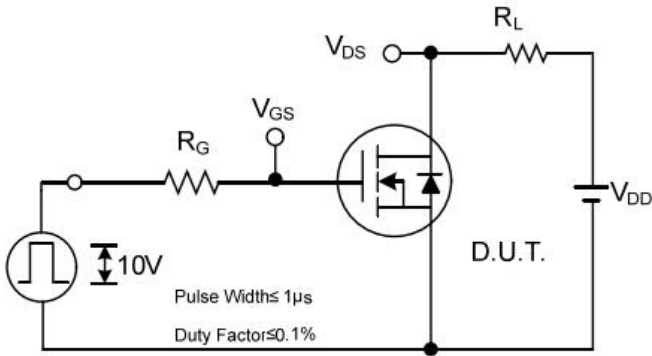
RATING AND CHARACTERISTIC CURVES



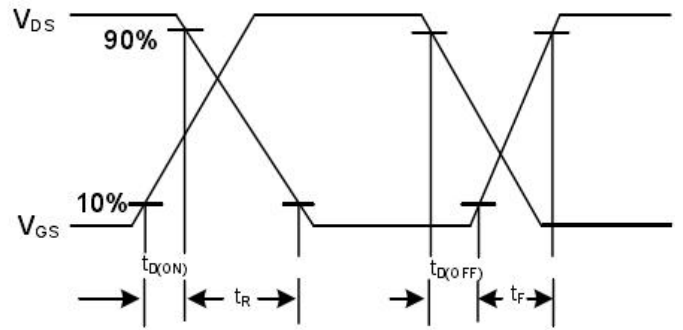
Peak Diode Recovery dv/dt Test Circuit



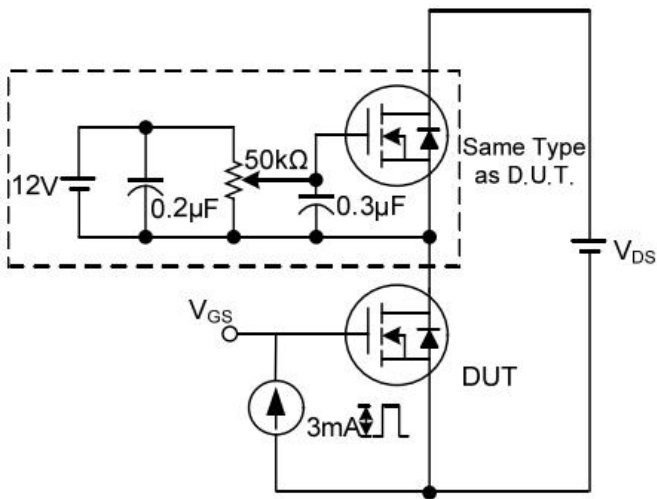
Peak Diode Recovery dv/dt Waveforms



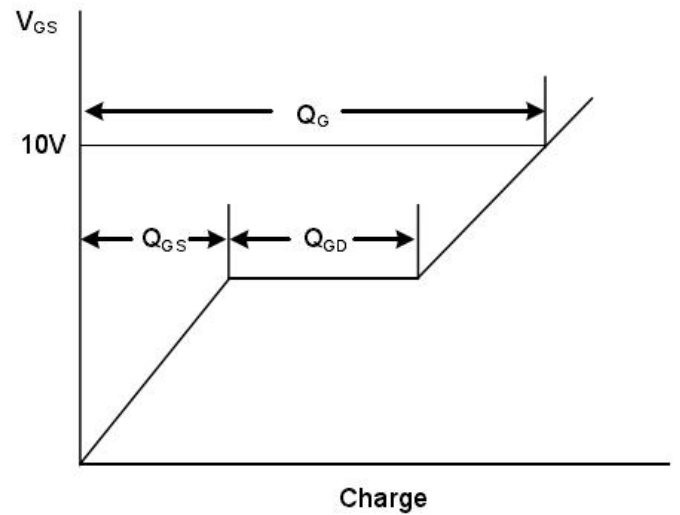
Switching Test Circuit



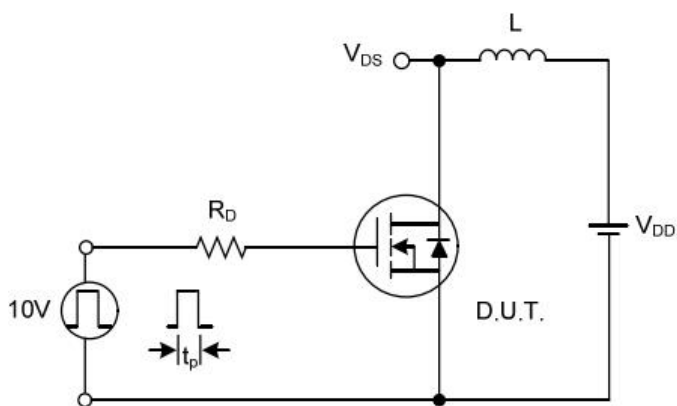
Switching Waveforms



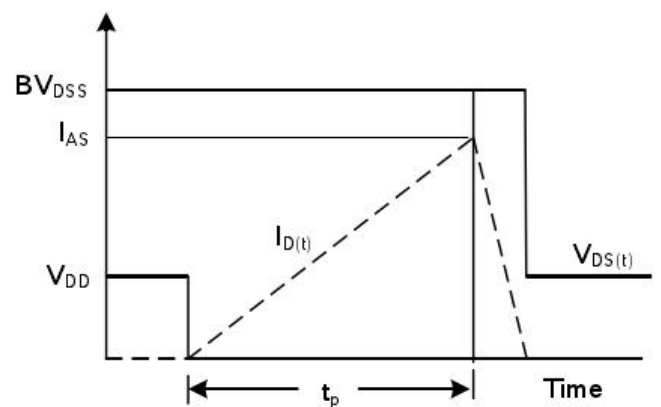
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Typical Output Characteristics

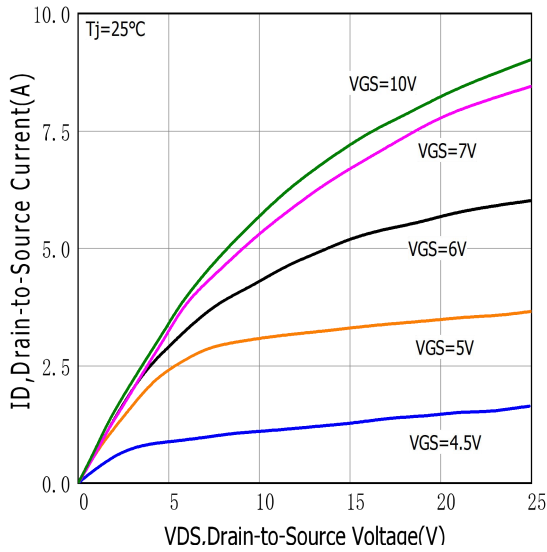


Figure.2 Typical Gate Charge vs Gate to Source Voltage

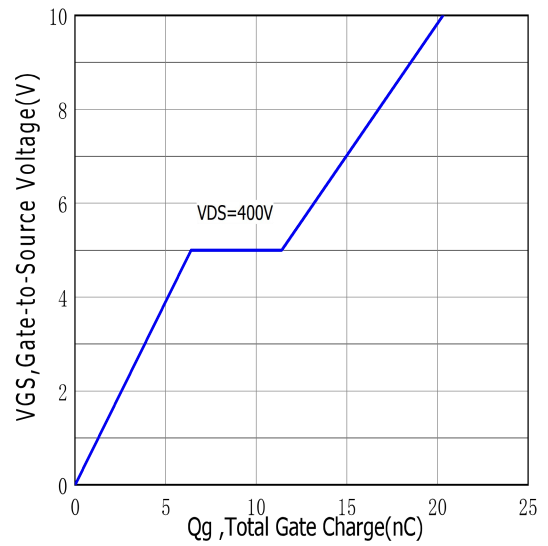


Figure.3 Typical Body Diode Transfer Characteristics

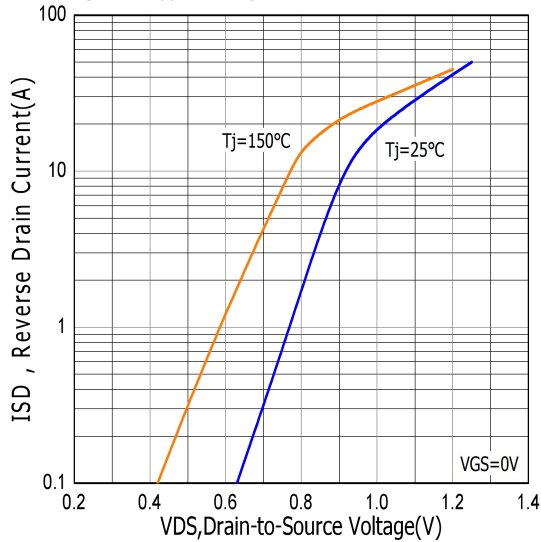


Figure.4 Typical Capacitance vs Drain to Source Voltage

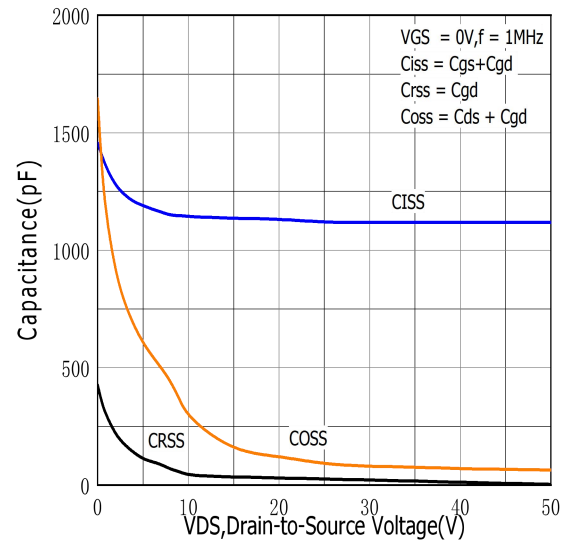


Figure.5 Typical Breakdown Voltage vs Junction Temperature

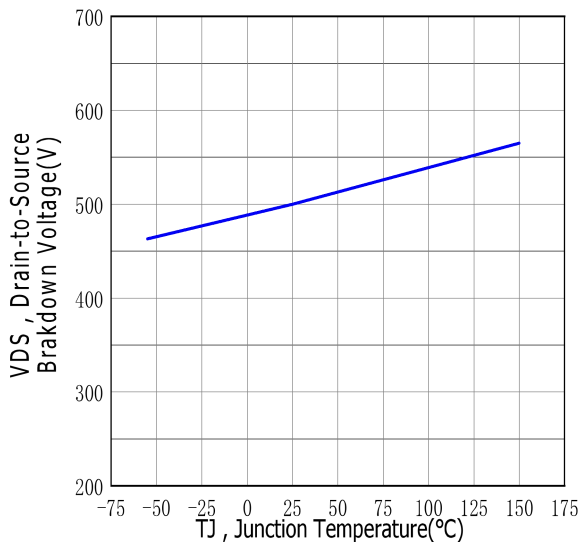


Figure.6 Typical Drain to Source on Resistance vs Junction Temperature

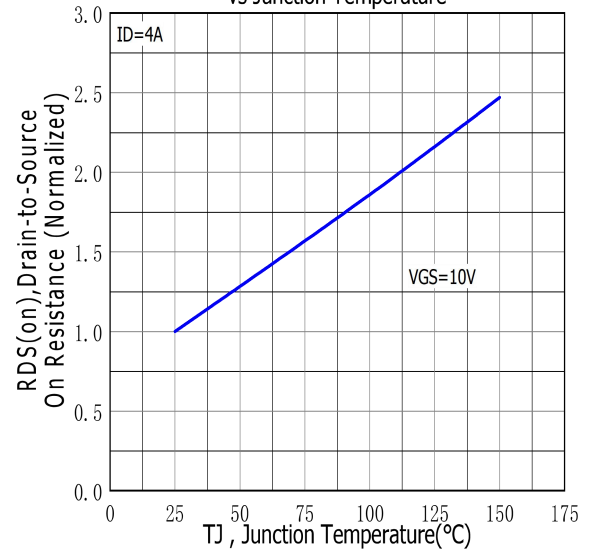


Figure.7 Maximum Forward Bias Safe Operating Area

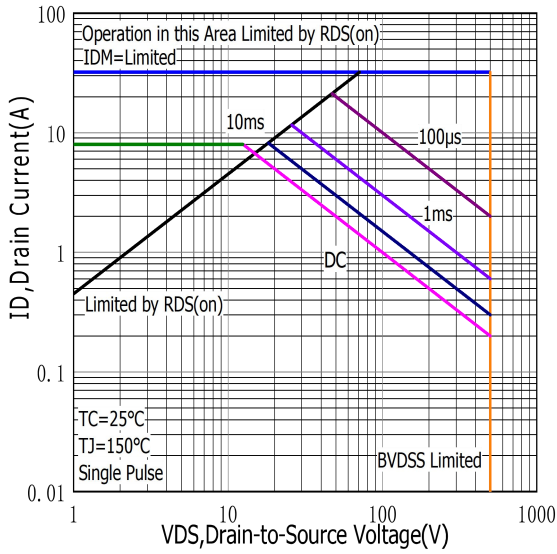


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

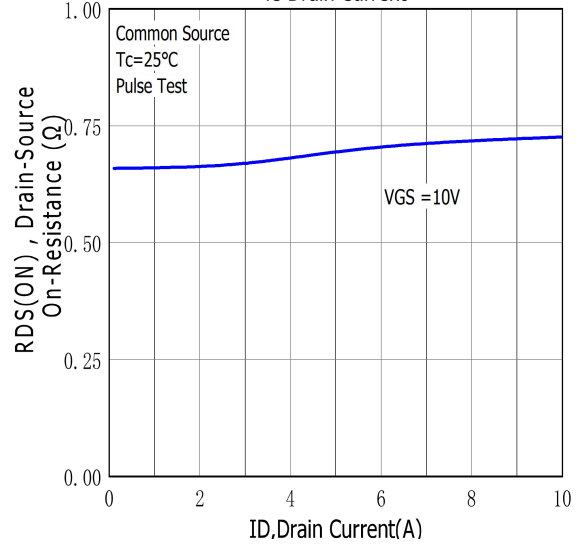


Figure.9 Maximum EAS vs Channel Temperature

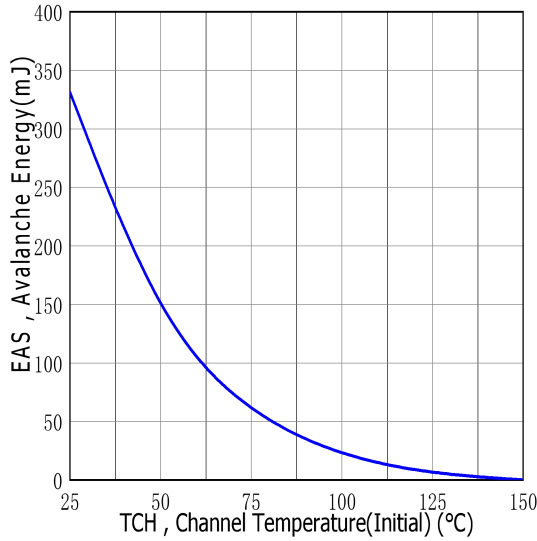


Figure.10 Typical Threshold Voltage vs Case Temperature

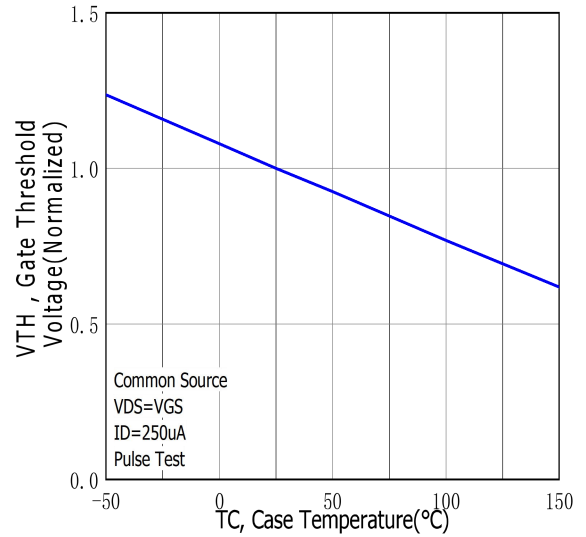


Figure.11 Maximum Effective Thermal Impedance, Junction to Case

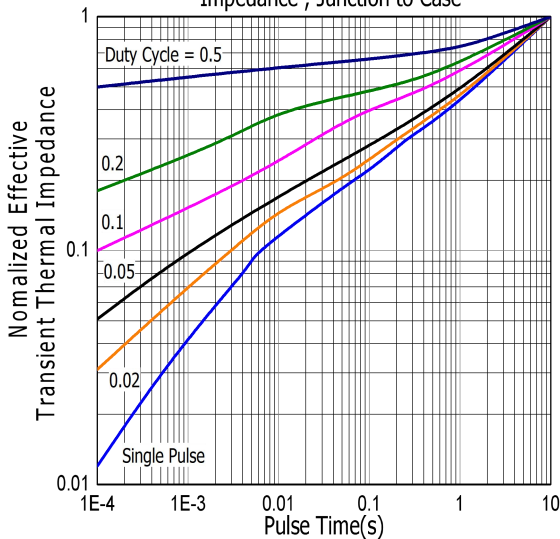
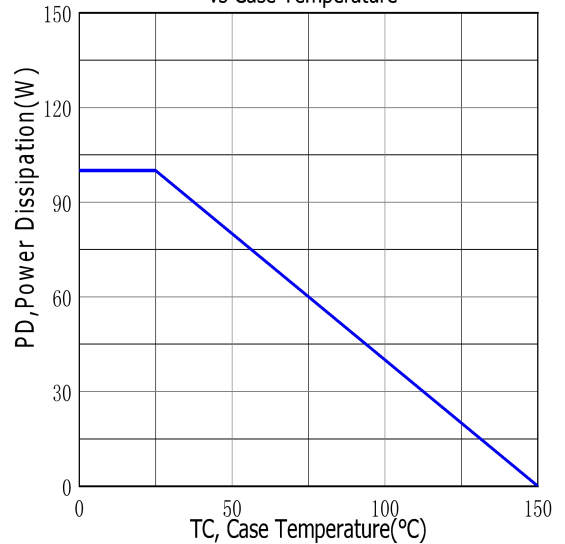
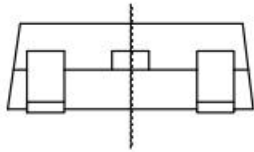
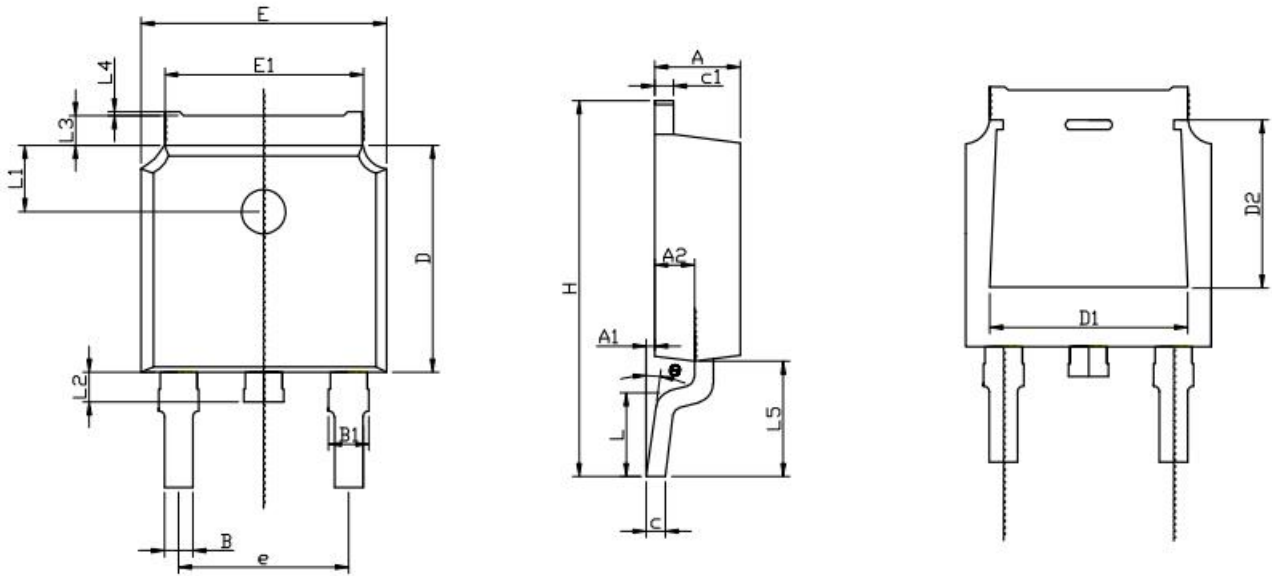


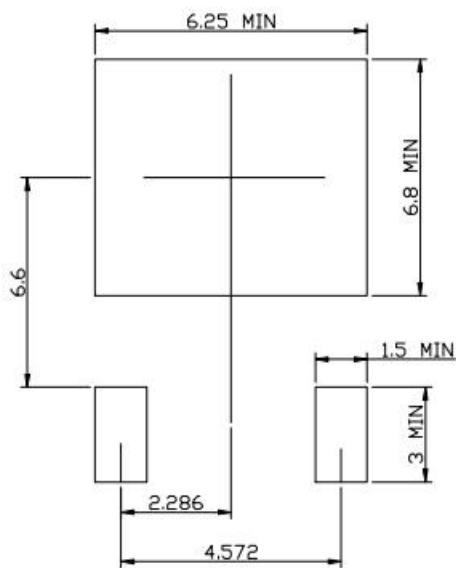
Figure.12 Maximum Power Dissipation vs Case Temperature



TO-252-2L PACKAGE OUTLINE



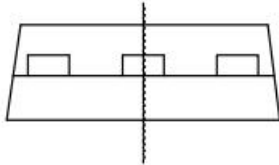
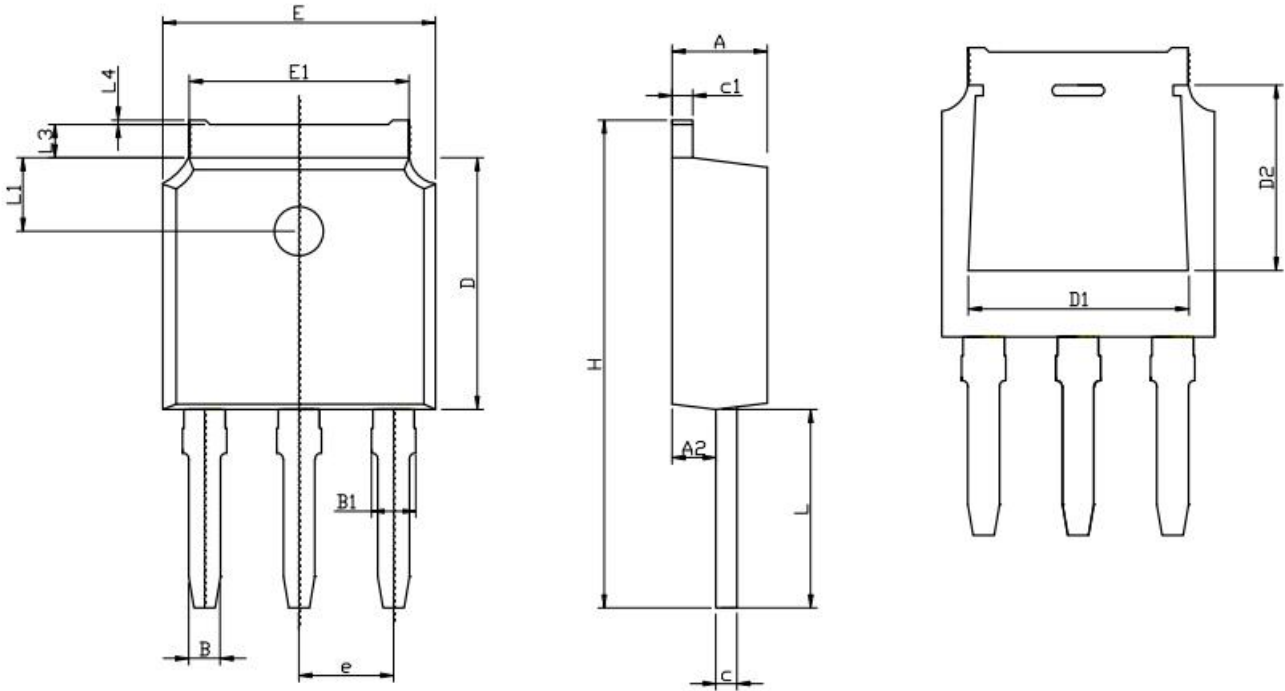
RECOMMENDED LAND PATTERN



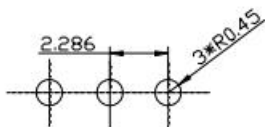
	MIN	NOM	MAX
A	2.15	2.30	2.45
A1	0.05	0.10	0.20
A2	0.91	1.07	1.22
B	0.66	0.76	0.86
B1	0.93	1.08	1.23
C	0.40	0.50	0.60
C1	0.40	0.50	0.60
D	5.95	6.10	6.25
D1	-	4.8REF	-
D2	-	3.8REF	-
E	6.45	6.60	6.75
E1	5.12	5.32	5.52
L		1.65	
L1	1.58	1.78	1.98
L2	0.60	0.80	1.00
L3	0.70	0.85	1.00
L4	0.00	0.05	0.20
L5	2.80	3.10	3.40
H	9.80	10.10	10.40
θ	0°		8°
e		4.572REF	

UNIT: mm

TO-251-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN

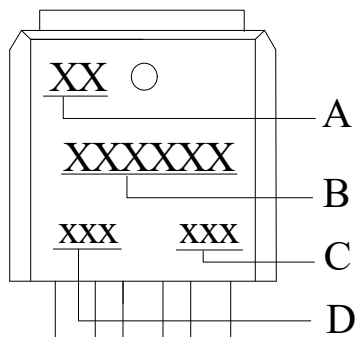


UNIT: mm

	MIN	NOM	MAX
A	2.15	2.30	2.45
A2	0.91	1.07	1.22
B	0.66	0.76	0.86
B1	0.93	1.08	1.23
C	0.40	0.50	0.60
C1	0.40	0.50	0.60
D	5.95	6.10	6.25
D1	-	4.8REF	-
D2	-	3.8REF	-
E	6.45	6.60	6.75
E1	5.12	5.32	5.52
L	4.50	4.80	5.10
L1	1.58	1.78	1.98
L3	0.70	0.85	1.00
L4	0.00	0.05	0.20
H	11.50	11.80	12.10
e		2.286REF	

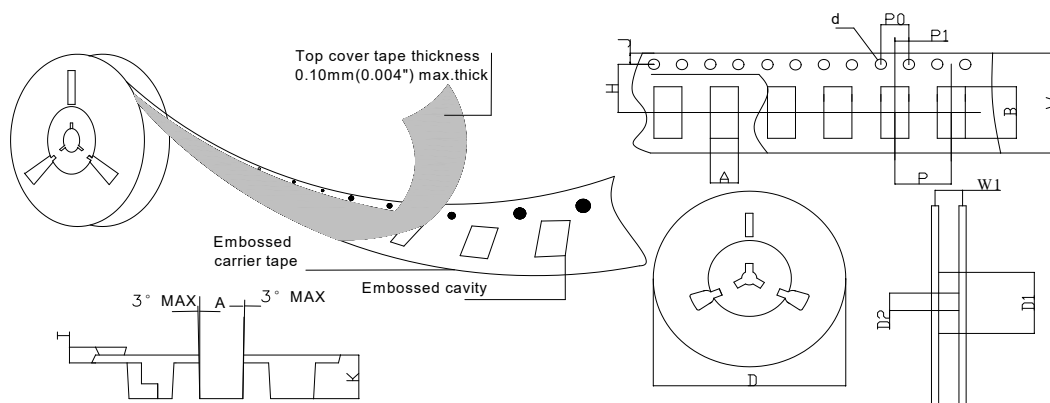
Marking and packaging illustration

1、Marking



SYMBOL	Explanation
A	Trademark
B	Product Name
C	Date Code
D	Product Information

3、Packaging



SPECIFICATIONS mm(inch)		PACKAGE	SPECIFICATIONS mm(inch)		PACKAGE
ITEM	SYM BOL	TO-252	ITEM	SYM BOL	TO-252
Carrier width	A	7.10(0.280)Typ	Carrier depth	K	2.79(0.110)Typ
Carrier length	B	10.7(0.421)Typ	Punch hole pitch	P	8.00(0.315)Typ
Sprocket hole	d	ø1.50(0.059)Typ	Sprocket hole pitch	P0	4.00(0.157)Typ
Reel outer diameter	D	330.0(13.0)Typ	Embossment center	P1	2.00(0.079)Typ
Reel inner diameter	D1	100.0(3.937)Typ	Overall tape thickness	T	0.25(0.010)Typ
Feed hole diameter	D2	13.0(0.512)Typ	Tape width	W	16.0(0.630)Typ
Sprocket hole position	J	1.75(0.069)Typ	Reel width	W1	16.5(0.650)Typ
Punch hole position	H	7.50(0.295)Typ			

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[US6M2GTR](#) [DMN31D5UDJ-7](#) [DMP22D4UFO-7B](#) [IPS60R3K4CEAKMA1](#) [DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#)
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