

ES3A THRU ES3J

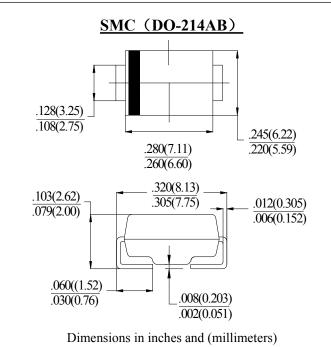
PINGWEIENTERPRISE 3.0AMPS. SUPER FAST SURFACE MOUNT RECTIFIER

FEATURE

- . High current capability
- . Low forward voltage drop
- . Low power loss, high efficiency
- . High surge capability
- . High temperature soldering guaranteed: $260^{\circ}C/10$ seconds at terminals.
- . Superfast recovery time for high efficiency.
- . For surface mounted application.
- . Easy pick and place.

MECHANICAL DATA

- . Case: Molded plastic
- . Epoxy: UL94V-0 rate flame retardant
- . Lead: MIL-STD- 202E, Method 208 guaranteed
- . Polarity:Color band denotes cathode end
- . Packaging:12mm tape per EIA STD RS-481
- . Mounting position: Any



MAXIMUM RATINGS AND ELETRICAL CHARACTERISTICS

Ratings at $25\,^\circ\!\mathrm{C}$ ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

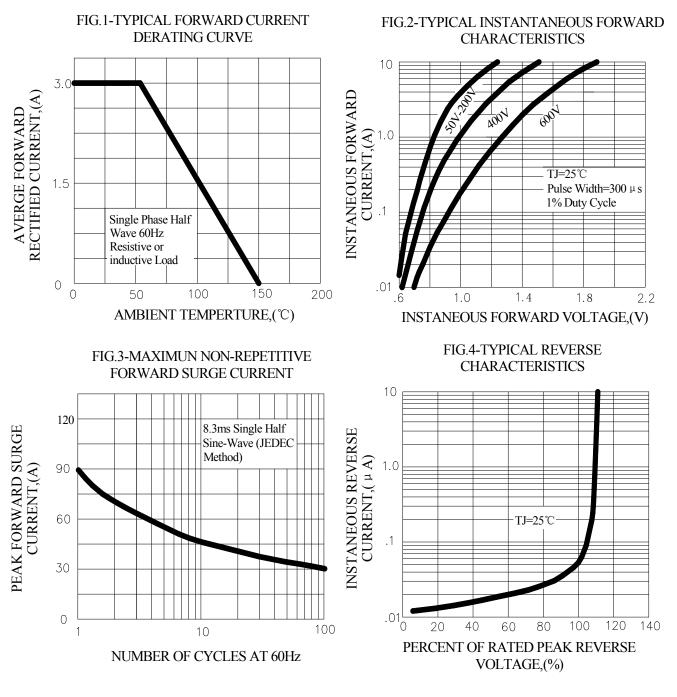
Type Number	SYM BOL	ES3A	ES3B	ES3D	ES3G	ES3J	units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	V
Maximum DC blocking Voltage	V _{DC}	50	100	200	400	600	V
Maximum Average Forward Rectified Current at $T_A=55^{\circ}C$	I _{F(AV)}	3.0					A
Peak Forward Surge Current 8.3ms single half sine- wave superimposed on rated load (JEDEC method)	<i>I</i> _{FSM}	90.0					A
Maximum Forward Voltage at 3.0A DC	VF		0.95		1.3	1.7	V
Maximum DC Reverse Current $@T_A = 25^{\circ}C$ at rated DC blocking voltage $@T_A = 100^{\circ}C$	I _R	5.0 100.0					μΑ
Maximum Reverse Recovery Time (Note 1)	t _{rr}	35					nS
Typical Junction Capacitance (Note 2)	Сл	45 30					pF
Typical Thermal Resistance (Note 3)	R (JA)	50					°C /W
Storage Temperature	T _{STG}	-55 to +150					°C
Operation Junction Temperature	TJ	-55 to +150					°C

Note:

1. Test Conditions: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

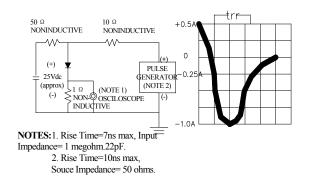
2. Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc

3. Measured on P.C.Board with 0.2×0.2"(5.0×5.0mm)Copper Pad Areas.



RATING AND CHARACTERISTIC CURVES (ES3A THRU ES3J)

FIG.5-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERSITIC



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