

S2M

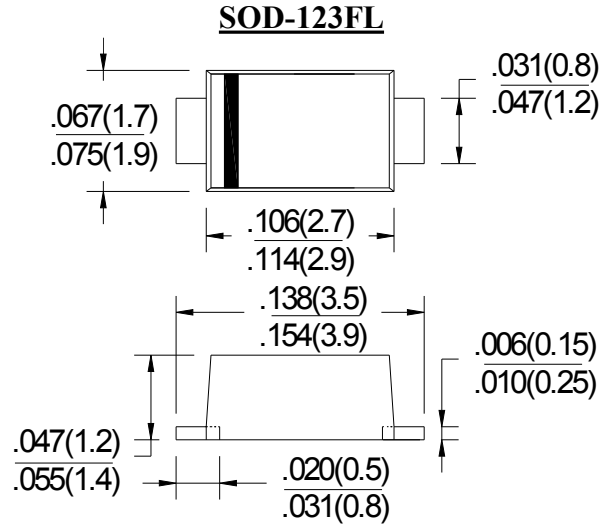
1.5AMPS. SURFACE MOUNT GENERAL PURPOSE SILICON RECTIFIERS

FEATURES

- . Glass passivated device
- . Ideal for surface mounted applications
- . Metallurgically bonded construction
- . High temperature soldering guaranteed:
250°C/10 seconds at terminals.

MECHANICAL DATA

- . Case: Molded plastic
- . Epoxy: UL94V-0 rate flame retardant(Halogen free)
- . Lead: MIL-STD- 202E, Method 208 guaranteed
- . Polarity:Color band denotes cathode end
- . Weight: 0.006 ounces, 0.02 gram
- . Mounting position: Any



Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

MAXIMUM RATINGS (T_C=25°C unless otherwise noted)

Parameter	Symbol	S2M	Units
	Marking	S2M	
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	1000	V
Maximum RMS Voltage	V_{RMS}	700	V
Maximum DC blocking Voltage	V_{DC}	1000	V
Average Forward Rectified Current at T _A ≤ 55°C	$I_{F(AV)}$	1.5	A
Non-repetitive forward surge current, 8.3ms half sine-wave	I_{FSM}	40	A
Minimum Reverse Recovery Time (Note 1)	t_{rr}	800	nS
Typical Junction Capacitance (Note 2)	C_J	12	pF
Operation Junction Temperature and Storage Temperature	T_J, T_{STG}	-55 to + 150	°C

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise noted)

Parameter	Symbol	Min	Typ	Max	Units
Instantaneous Forward voltage at 1.5A	V_F	-----	0.94	1.1	V
@T _J =25°C		-----	0.88	----	
reverse current at rated DC blocking voltage	I_R	-----	-----	5.0	uA
@T _J =125°C		-----	-----	100.0	

THERMAL CHARACTERISTICS (T_C=25°C unless otherwise noted)

Parameter	Symbol	S2M	Units
Typical Thermal Resistance (Note 3)	$R_{(JC)}$	45	°C/W

Note:

1. Conditions: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A
2. T_J=25°C, V_R = 4V_{DC}@1Mhz
3. Measured on P.C.Board with 15.0mm*15.0mm*1.6mm Copper Pad Areas

RATING AND CHARACTERISTIC CURVES (S2M)

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

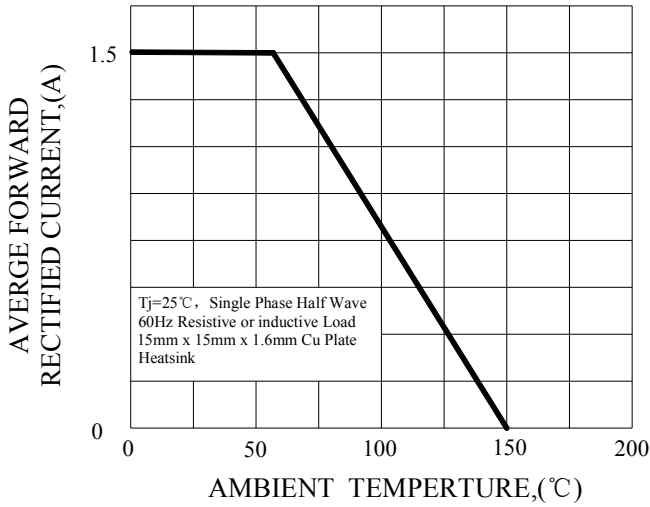


FIG.2-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

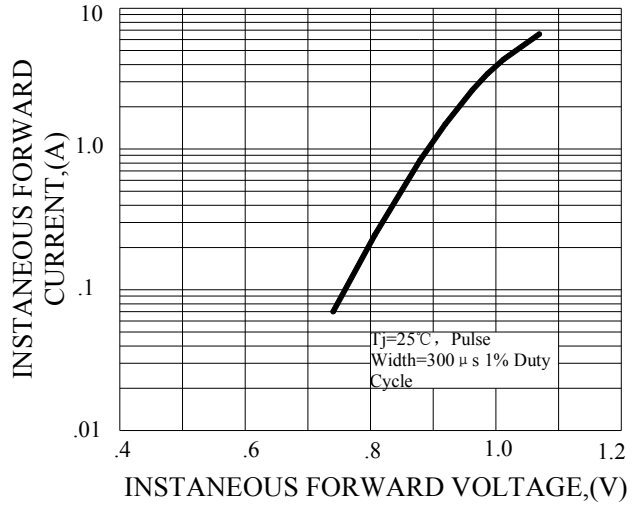


FIG.3-MAXIMUN NON-REPETITIVE FORWARD SURGE CURRENT

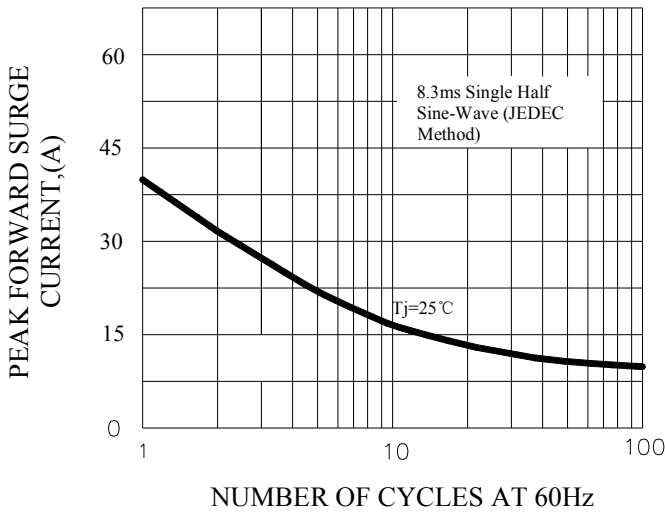


FIG.4-TYPICAL REVERSE CHARACTERISTICS

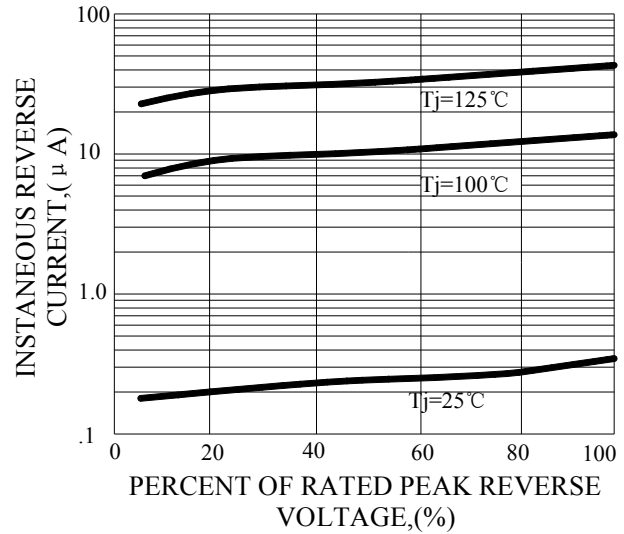
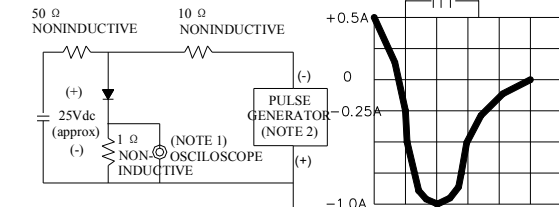
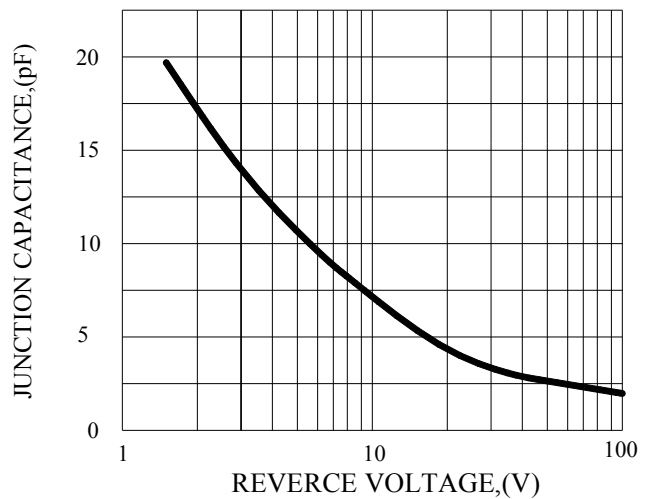


FIG.5-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



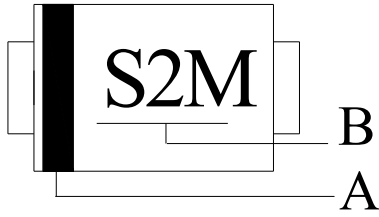
NOTES:1. Rise Time=7ns max, Input Impedance= 1 megohm, 22pF.
2. Rise Time=10ns max, Source Impedance= 50 ohms.

FIG.6-TYPICAL JUNCTION CAPAOTANCE



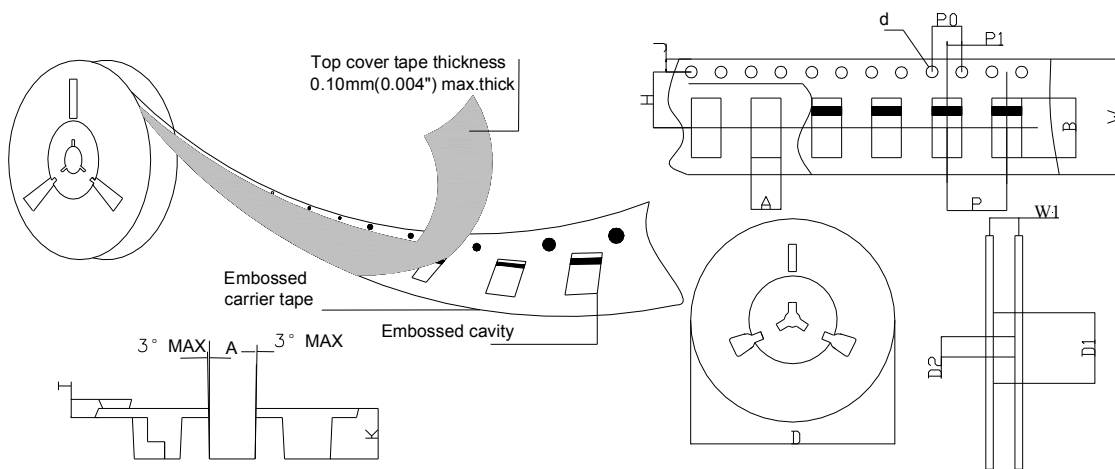
Marking and packaging illustration

1、Marking



SYMBOL	Explanation
A	Color Band Denotes Cathode
B	Product Name

2、Packaging



SPECIFICATIONS mm(inch)		PACKAGE	SPECIFICATIONS mm(inch)		PACKAGE
ITEM	SYM BOL	SOD-123FL	ITEM	SYM BOL	SOD-123FL
Carrier width	A	2.1(0.083)Max	Carrier depth	K	1.60(0.063)Typ
Carrier length	B	4.0(0.157)Max	Punch hole pitch	P	4.00(0.157)Typ
Sprocket hole	d	ø1.55(0.061)Typ	Sprocket hole pitch	P0	4.00(0.157)Typ
Reel outer diameter	D	177.8(7)Typ	Embossment center	P1	2.00(0.079)Typ
Reel inner diameter	D1	50.0(1.969)Min	Overall tape thickness	T	0.25(0.098)Typ
Feed hole diameter	D2	13.0(0.512)Typ	Tape width	W	8.15(0.321)Typ
Sprocket hole position	J	1.75(0.069)Typ	Reel width	W1	10.5(0.413)Min
Punch hole position	H	3.50(0.138)Typ			

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