



PLETRONICS SM44T Series 3.3V CMOS Clock Oscillator



SM44TV
3.2 x 2.5 x 1.05 mm
LCC Ceramic Package

Features

- Pletronics' SM44T Series is a quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 3.3V nominal Supply Voltage
- 0.80-180 MHz Frequency Range

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Base Stations/ Picocell
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range ²	0.80	-	180	MHz	Consult factory for other options
Frequency Stability ² ± 20 = 20 , ± 25 = 44 , ± 50 = 45	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures
Operating Temperature Range ²	-10 -20 -40	-	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage ^{1,2} V _{CC}	2.97	3.30	3.63	V	3.3V ± 10%
Output Waveform	CMOS				
Duty Cycle	45	-	55	%	At 50% point of V _{CC}
Output V _{HIGH}	90	-	-	%	of V _{CC}
Output V _{LOW}	-	-	10	%	of V _{CC}
Startup Time	-	-	3	ms	Time for output to reach specified frequency
V _{DISABLE}	-	-	30	%	Of V _{CC} applied to Pad 1
V _{ENABLE}	70	-	-		
Enable Time	-	-	100	ns	Time for output to reach a logic state
Disable Time	-	-	100	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	50	-	-	Kohm	To V _{CC}
Output Leakage	V _{OUT} = V _{CC} -10 V _{OUT} = 0V -10	-	+10 +10	µA	Pad 1 low, device disabled
Standby Current	-	-	3	µA	
Jitter	-	-	0.6	ps RMS	12kHz to 20MHz from specified frequency
	-	-	2.5	ps RMS	10Hz to 1MHz from specified frequency
	-	-	100	ps pk-pk	@25.0 MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



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Electrical Characteristics

Parameter	Typ	Max	Unit	Condition
Output T _{RISE} and T _{FALL}	2.5	5	nS	< 35 MHz
	1.5	3		≥ 35 MHz and < 70 MHz
	1	2		≥ 70 MHz
	4	8		< 35 MHz
	3	5		≥ 35 MHz and < 70 MHz
	2	3		≥ 70 MHz
				C _{LOAD} = 15 pF 10% to 90% of V _{CC} See Load Circuit
				C _{LOAD} = 30 pF 10% to 90% of V _{CC} See Load Circuit

Parameter	Typ	Max	Unit	Condition
V _{CC} Supply Current (I _{CC})	2	4	mA	< 8 MHz
	3	5		≥ 8 MHz and < 16 MHz
	4	6		≥ 16 MHz and < 35 MHz
	12	18		≥ 35 MHz and < 70 MHz
	23	36		≥ 70 MHz and < 110 MHz
	45	70		≥ 110 MHz
	3	5		< 8 MHz
	4	6		≥ 8 MHz and < 16 MHz
	6	8		≥ 16 MHz and < 35 MHz
	16	22		≥ 35 MHz and < 70 MHz
	30	43		≥ 70 MHz and < 120 MHz
			C _{LOAD} = 30 pF	

Specifications with Pad 1 E/D circuit open



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Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V _{CC}	Frequency in MHz	Optional T&R Packaging code
SM44	45	T	E	V	- 125.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20 = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ±10%	0.80 - 180 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

Device Marking

PFF.FFM • YMDxx	PFF.FFM • YMxxx
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PLE or P = Pletronics
FF.FF = Frequency in MHz
YMD or YM = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3	4	5	6	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2022	2023	2024	2025	2026	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:
SM4445TV-125.0M
Customer P/N:
12345678
Qty: D/C
1000 11M

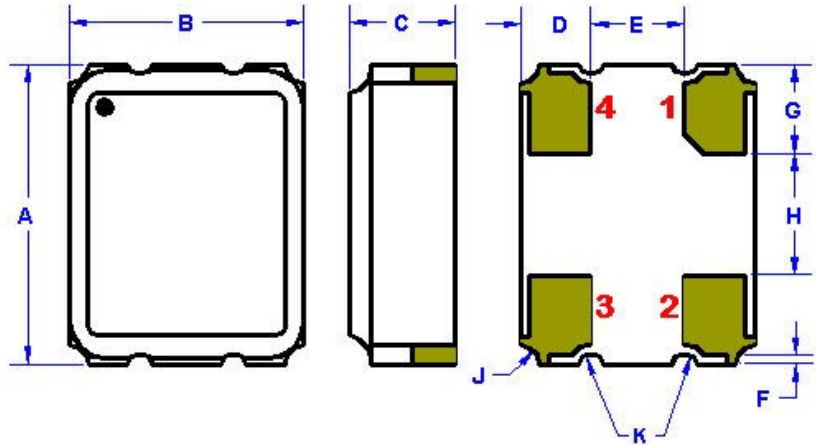
RoHS Compliant
2nd LvL Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.024 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

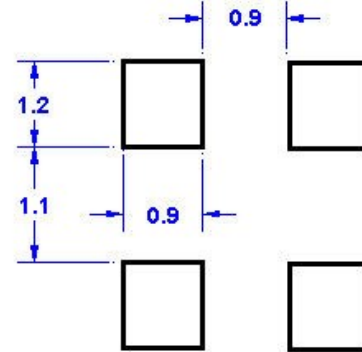
Mechanical Dimensions

	Inches	mm
A	0.125 ± 0.006	3.20 ± 0.15
B	0.098 ± 0.006	2.50 ± 0.15
C	0.041 ± 0.004	1.05 ± 0.10
D ¹	0.030	0.75
E ¹	0.039	1.00
F ¹	0.004	0.10
G ¹	0.043	1.10
H ¹	0.039	1.00
J ¹	0.008	0.20R
K	End Detents optional	



Pad Layout mm shown

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.



¹ Typical dimensions

(Not to Scale)

Contacts (pads): Gold 11.8 to 39.4 μmches (0.3 to 1.0 μm) over Nickel 50 to 350 μmches (1.27 to 8.89 μm)

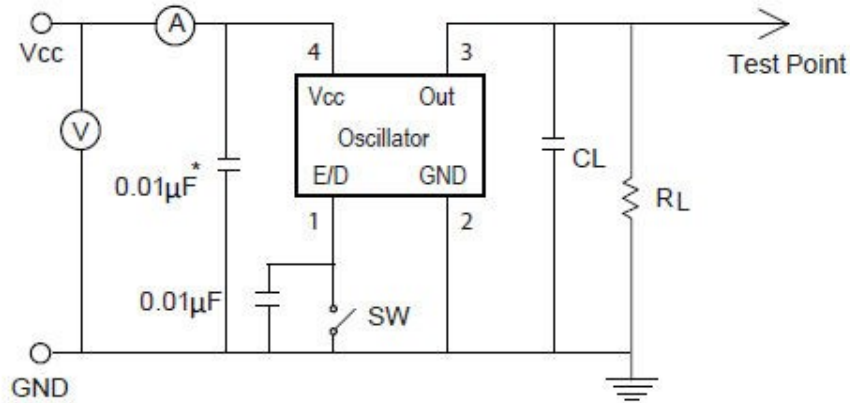
Layout

Pad	Function	Note
1	Output Enable/Disable	The oscillator shall operate when this pad is not connected. The output will be inhibited (high impedance state) when this pad is logic low. Recommend connecting this pad to V _{CC} if the oscillator is to be always on.
2	Ground (GND)	
3	Output	CMOS
4	V _{CC} Supply Voltage	Connect an appropriate power supply bypass capacitor as close as possible

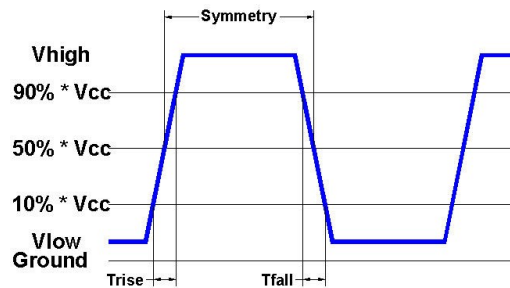
For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

Electrical Test / Load Circuit



Notes:
 RL: 5 Kohm minimum
 CL: Includes the input capacitance of oscilloscope
 * 0.01µF external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	MIL-STD-883 3015.7
Machine Model	200V	EIAJ ED-4701/304

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.3V to +4.0V
V _i Input Voltage	-0.3V to V _{CC} + 0.3V
V _o Output Voltage	-0.3V to V _{CC} + 0.3V

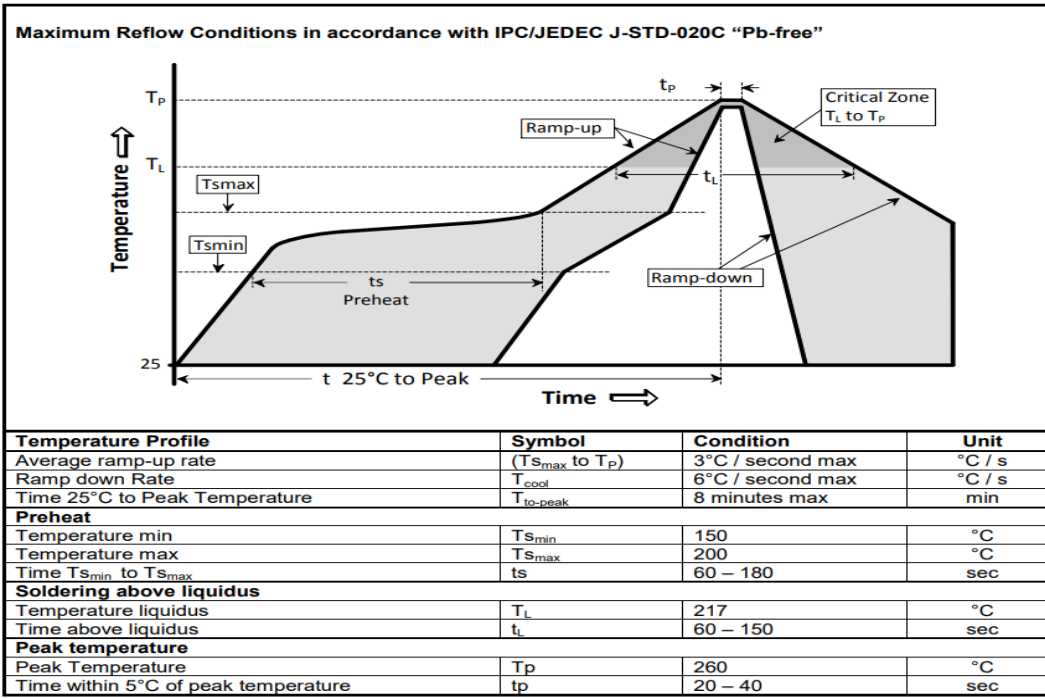
Thermal Characteristics:

The maximum die or junction temperature is 125°C



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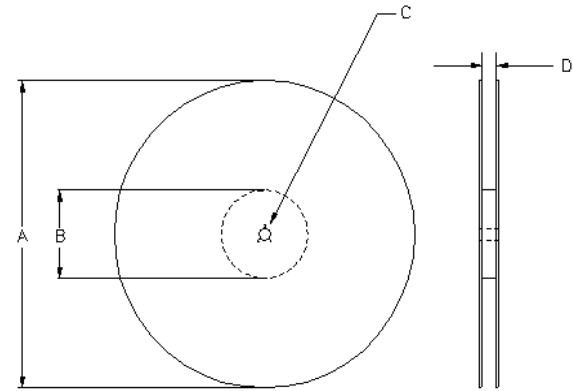
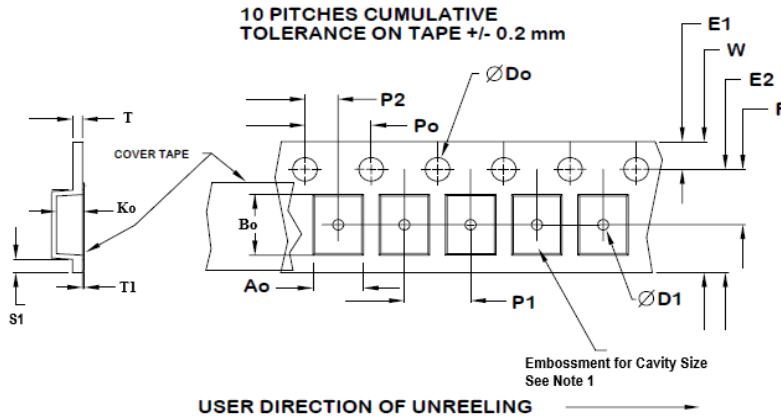
Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.



Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7±0.1	3.4±0.1	1.4±0.1

Dimensions in mm Drawing Not to scale
Note 1: Embossed cavity to conform to EIA-481-B

Tape Size	Do	D1 min	E1	Po	P2	S1 min	T max	T1 max
8mm	1.5	1.0	±0.1	±0.1	2.0	0.6	0.3	0.1
12mm		1.5			±0.05			
16mm	+0.1 -0.0	1.5	±0.1	±0.1	2.0	0.6	0.3	0.1
24mm		1.5			±0.1			

Reel Size	A		B		C	D
	Inches	mm	Inches	mm		
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		



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