





April 2017



- Pletronics' SM44 Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 0.8 to 180 MHz
- 2.5 x 3.2 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- · Low Jitter

## Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.041 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

#### **Absolute Maximum Ratings:**

Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V <sub>CC</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V
lo Output Current	+25 mA to -25 mA

#### **Thermal Characteristics**

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 40 to 60°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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#### **Part Number:**

SM44	45	Т	E	V	- 75.0M	-XX	
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
							Frequency in MHz
							Supply Voltage V <sub>cc</sub> V = 3.3V <u>+</u> 10%
							Temperature Range Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C
							Series Model
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm
							Series Model

### Part Marking:

PFF.FFM •YMD*xx* 

or

PFF.FFM •YM*xxx* 

### Marking legend:

P = Pletronics

FF.FFF = Frequency in MHz

YMD or YM = Date of Manufacture (year

and month, or year-month-

day)

All other markings are internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

#### Codes for Date Code YMD

Code	3	4	5	6	7	Code	Α	В	С	D	Е	F	G	Н	7	K	L	M
Year	2013	2014	2015	2016	2017	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
(	Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	П	F	G

Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	Н	J	K	L	М	N	Р	R	T	U	٧	W	X	Υ	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

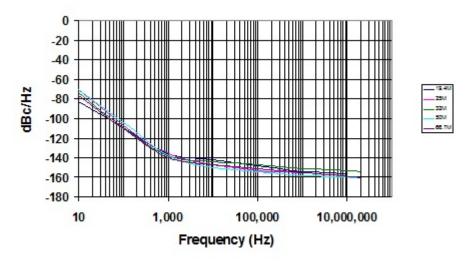


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### Electrical Specification for 3.30V ±10% over the specified temperature range

Item	Min	Max	Unit	Condition
Frequency Range	0.8	180	MHz	
Frequency Accuracy "45" "44" "20"	-50 -25 -20	+50 +25 +20	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures
Output Waveform		CMOS	S	
Output High Level	90	-	%	of V <sub>cc</sub> (See load circuit)
Output Low Level	-	10	%	
Output Symmetry	45	55	%	at 50% point of V <sub>cc</sub>
Jitter	-	0.6	pS RMS	12 KHz to 20 MHz from the output frequency
	-	2.5	pS RMS	10 Hz to 1 MHz from the output frequency
	-	100	pS pk-pk	@ 25.0 MHz
Enable/Disable Internal Pull-up	50	-	Kohm	to V <sub>cc</sub>
V disable	-	30	%	of V <sub>cc</sub> applied to pad 1
V enable	70	-	%	
Output leakage $V_{OUT} = V_{CC}$	-10	+10	uA	Pad 1 low, device disabled
$V_{OUT} = 0V$	-10	+10	uA	
Standby Current I <sub>cc</sub>	-	3	uA	
Enable time	-	100	nS	Time for output to reach a logic state
Disable time	-	100	nS	Time for output to reach a high Z state
Start up time	-	3	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
	-10	+70	°C	Extended Temperature Range "C" Option
	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	

Typical phase noise plot for 5 oscillators at different output frequencies.





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### Electrical Specification for 3.30V ±10% over the specified temperature range

Item	Тур	Max	Unit	Condition	
Output T <sub>RISE</sub> and T <sub>FALL</sub>	2.5	5	nS	< 35 MHz	C <sub>LOAD</sub> = 15 pF
	1.5	3	nS	≥ 35 MHz and < 70MHz	10% to 90% of V <sub>cc</sub> See Load Circuit
	1	2	nS	≥ 70 MHz	
	4	8	nS	< 35 MHz	C <sub>LOAD</sub> =30 pF 10% to 90% of V <sub>CC</sub>
	3	5	nS	≥ 35 MHz and < 70MHz	See Load Circuit
	2	3	nS	<u>&gt;</u> 70 MHz	
V <sub>cc</sub> Supply Current (I <sub>cc</sub> )	2	4	mA	< 8 MHz	C <sub>LOAD</sub> = 15 pF
	3	5	mA	≥ 8 MHz and < 16 MHz	
	4	6	mA	≥ 16 MHz and < 35 MHz	
	12	18	mA	≥ 35 MHz and < 70MHz	
	23	36	mA	≥ 70 MHz and < 110MHz	
	45	70	mA	≥ 110 MHz	
	3	5	mA	< 8 MHz	C <sub>LOAD</sub> = 30 pF
	4	6	mA	≥ 8 MHz and < 16 MHz	
	6	8	mA	≥ 16 MHz and < 35 MHz	
	16	22	mA	≥ 35 MHz and < 70MHz	
	30	43	mA	<u>&gt;</u> 70 MHz and < 120MHz	

Specifications with Pad 1 E/D open circuit

#### **Load Circuit and Test Waveform** Sym metry Vhigh 90% \* Vcc-E/D Voc 50% \* Vcc 0.01 uF UUT 10% \* Vcc Vlow Ground To oscilloscope and counter GND Out Total load: 5K ohm minimum C LOAD maximum per specification All leads 0.2 inches (5mm) or less



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### Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

#### **ESD Rating**

Model	Minimum Voltage	Conditions			
Human Body Model	1500	MIL-STD-883 Method 3115			
Charged Device Model	1000	JESD 22-C101			

### **Package Labeling**

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

P/N: SM4445TV-50.0M

Customer P/N: 12345678

Qty: D/C 7GY-MMo

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

**RoHS Compliant** 

2nd LvL Interconnect

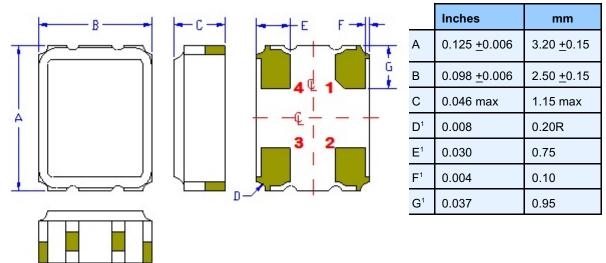
Category=e4

Max Safe Temp=260C for 10s 2X Max



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#### Mechanical:



Not to Scale

<sup>1</sup> Typical dimensions

#### Contacts:

Gold 11.8 to 39.4 μinches (0.3 to 1.0 μm) over Nickel 50 to 350 μinches (1.27 to 8.89 μm)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V <sub>cc</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.

### Layout and application information



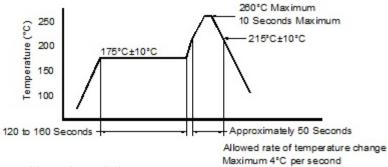
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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### Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

### Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

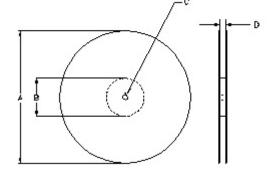
	Constant Dimensions Table 1												
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max					
8mm		1.0			2.0								
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05								
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1					
24mm		1.5			<u>+</u> 0.1								

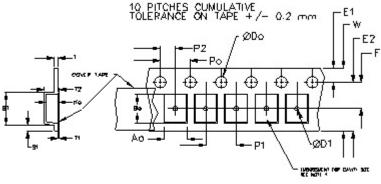
Variable Dimensions Table 2											
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko				
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1				

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm

Not to scale





		REE			
Α	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
В	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Width
С	mm	13	vviain		
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0

USER DIRECTION OF UNREELING -----

Reel dimensions may vary from the above

www.pletronics.com

425-776-1880



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