



November 2018



- Pletronics' SM55 Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- · Tape and Reel or cut tape packaging is available.
- 0.8 to 165 MHz
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- · Low Jitter
- Optimized for low power applications (battery)

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.064 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V
lo Output Current	+25 mA to -25 mA

Thermal Characteristics

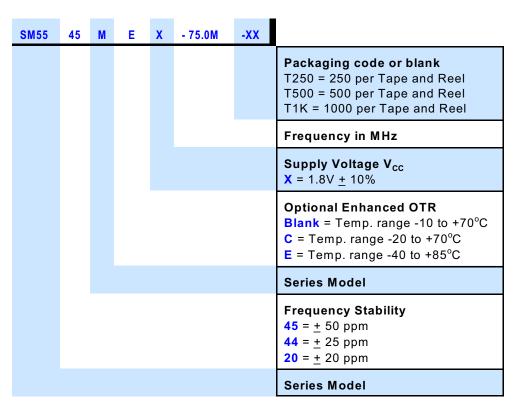
The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



November 2018

Part Number:



Part Marking and Legend:



PLE = Pletronics

ff.fff M or ff.ff M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	Code	Α	В	С	D	E	F	G	Н	J	K	L	M
Year	2016	2017	2018	2019	2020	Month	JAN	FEB	MAI	R AP	R MA	Y JU	N JUI	AUG	SEF	OCT	NOV	DEC DEC
С	ode	1	2	2 :	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
[Day	1	1 2	2 ;	3	4	5	6	7	8	9	10	11	12	13	14	15	16
С	ode	H	1 .	J	K	L	VI	N	Р	R	Т	U	٧	W	Х	Υ	Z	
	Day	1	7 1	8 1	9 :	20 2	21 :	22	23	24	25	26	27	28	29	30	31	



November 2018

Electrical Specification for 1.80V ±10% over the specified temperature range

Item	Min	Max	Unit	Condition
Frequency Range	0.8	165	MHz	
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for
"44"	-25	+25		1 year, shock, vibration and temperatures
" 20 "	-20 +20			
Output Waveform		CMOS		
Output High Level	90	-	%	of V _{cc} for I _{oH} = +2 mA <35 MHz
	70	-		of V_{CC} for I_{OH} = +8 mA \geq 35 MHz
Output Low Level	-	10	%	of V_{CC} for I_{OL} = -2 mA <35 MHz
	-	30		of V_{CC} for I_{OL} = -8 mA \geq 35 MHz
Output Symmetry	45	55	%	at 50% point of V _{cc} (See load circuit)
Jitter Output: 1 to 15 MHz	-	6.0	pS RMS	10 Hz to 1 MHz from the output frequency
Output: 15 to 35 MHz	-	5.0	pS RMS	
Output: 35 to 50 MHz	-	4.0	pS RMS	
Output: 50 to 70 MHz	-	3.0	pS RMS	
Output: > 70 MHz	-	2.5	pS RMS	
Output: 25 to 70 MHz	-	0.7	pS RMS	12 KHz to 20 MHz from the output frequency
Output: > 70 MHz	-	0.6	pS RMS	
E/D Internal Pull-up	50	500	Kohm	to V _{cc}
V disable	-	30	%	of V _{cc} applied to pin 1
V enable	70	1	%	
Output leakage V _{OUT} = V _{CC}	-10	+10	uA	Pin 1 low, device disabled
$V_{OUT} = 0V$	-10	+10	uA	
Standby Current I _{cc}	-	4	uA	< 35 MHz
	-	100	uA	≥ 35 MHz
Enable time	-	250	nS	Time for output to reach a logic state
Disable time	-	250	nS	Time for output to reach a high Z state
Start up time	-	10	mS	Time for output to reach specified frequency
Operating Temperature	-10	+70	°C	Standard Temperature Range
Range	-20	+70	°C	Extended Temperature Range "C" Option
	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	



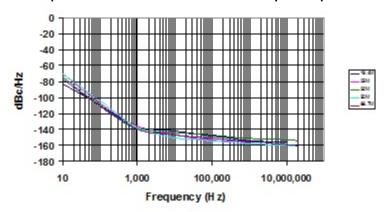
November 2018

Electrical Specification for 1.80V ±10% over the specified temperature range

Item	Тур	Max	Unit	Condition				
Output T _{RISE} and T _{FALL}	2	5	nS	< 35 MHz	C _{LOAD} = 15 pF			
	1	3.5	nS	≥ 35 MHz and < 70 MHz	20% to 80% of V _{cc} See Load Circuit			
	1.5	2.5	nS	> 70 MHz	000 2000 0			
	4	10	nS	< 35 MHz	C _{LOAD} = 30 pF			
	2	7	nS	≥ 35 MHz and < 70 MHz	20% to 80% of V _{cc} See Load Circuit			
	3	7	nS	> 70 MHz	000 2000 0			
V _{cc} Supply Current (I _{cc})	1	2	mA	< 8 MHz	C _{LOAD} = 15 pF			
	1.5	2.5	mA	≥ 8 MHz and < 16 MHz				
	2	3	mA	≥ 16 MHz and < 35 MHz				
	-	18	mA	≥ 35 MHz and < 70 MHz				
	17	27	mA	≥ 70 MHz and < 120 MHz				
	23	37	mA	<u>></u> 120 MHz				
	1.5	2.5	mA	< 8 MHz	C _{LOAD} = 30 pF			
	2	3	mA	≥ 8 MHz and < 16 MHz				
	3	4	mA	≥ 16 MHz and < 35 MHz				
	10	20	mA	≥ 35 MHz and < 70 MHz				

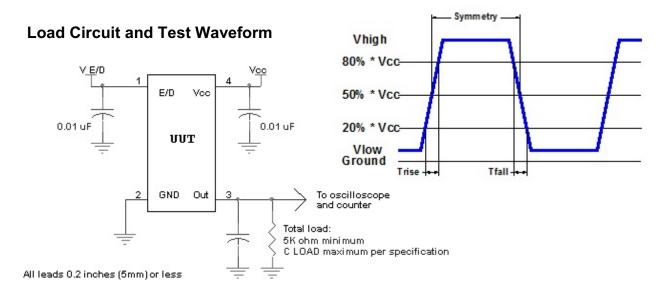
Specifications with Pad 1 E/D open circuit

Typical phase noise plot for 5 oscillators at different output frequencies.





November 2018



Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	1500	MIL-STD-883 Method 3115		
Charged Device Model	1000	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

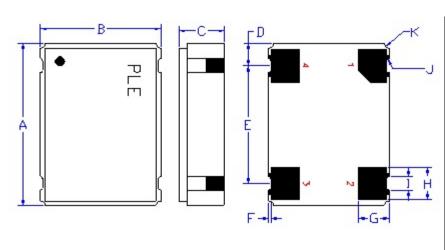
2nd LvL Interconnect Category=e4

Max Safe Temp=260C for 10s 2X Max



November 2018

Mechanical:



Inches mm Α 0.197 <u>+</u>0.006 5.00 ±0.15 0.126 +0.006 3.20 ±0.15 В С 0.045 ±0.004 1.15 ±0.10 D^1 0.048 1.23 E^1 0.100 2.54 F^1 0.004 0.10 G^1 0.050 1.27 H^1 0.055 1.40 I^1 0.024 0.60 J^1 0.004 0.10R K^1 0.008 0.020R

Not to Scale

¹ Typical dimensions

Contacts:

Gold 11.8 to 39.4 μ inches (0.3 to 1.0 μ m) over Nickel 50 to 350 μ inches (1.27 to 8.89 μ m)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information



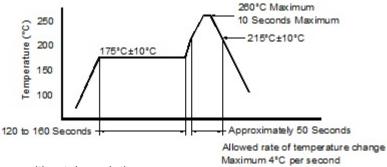
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- · do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



November 2018

Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

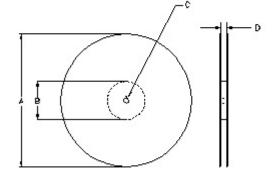
Constant Dimensions Table 1										
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max		
8mm		1.0			2.0					
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05					
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1		
24mm		1.5			<u>+</u> 0.1					

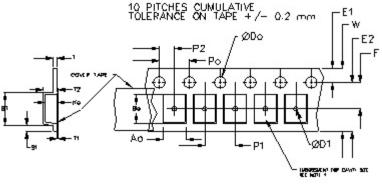
Variable Dimensions Table 2									
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko		
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1		

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm

Not to scale





		REE	REEL DIMENSIONS								
Α	inches	7.0	7.0 10.0								
	mm	177.8	254.0	330.2							
В	inches	2.50	4.00	3.75							
	mm	63.5	101.6	95.3	Tape Width						
С	mm	13	vviatri								
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0						

Reel dimensions may vary from the above

www.pletronics.com 425-776-1880



November 2018

IMPORTANT NOTICE

Pletronics Incorporated (PLE) reserves the right to make corrections, improvements, modifications and other changes to this product at anytime. PLE reserves the right to discontinue any product or service without notice. Customers are responsible for obtaining the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to PLE's terms and conditions of sale supplied at the time of order acknowledgment.

PLE warrants performance of this product to the specifications applicable at the time of sale in accordance with PLE's limited warranty. Testing and other quality control techniques are used to the extent PLE deems necessary to support this warranty. Except where mandated by specific contractual documents, testing of all parameters of each product is not necessarily performed.

PLE assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using PLE components. To minimize the risks associated with the customer products and applications, customers should provide adequate design and operating safeguards.

PLE products are not designed, intended, authorized or warranted to be suitable for use in life support applications, weapons, weapon systems or space applications, devices or systems or other critical applications that may involve potential risks of death, personal injury or severe property or environmental damage. Inclusion of PLE products in such applications is understood to be fully at the risk of the customer. Use of PLE products in such applications requires the written approval of an appropriate PLE officer. Questions concerning potential risk applications should be directed to PLE.

PLE does not warrant or represent that any license, either express or implied, is granted under any PLE patent right, copyright, artwork or other intellectual property right relating to any combination, machine or process which PLE product or services are used. Information published by PLE regarding third-party products or services does not constitute a license from PLE to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from PLE under the patents or other intellectual property of PLE.

Reproduction of information in PLE data sheets or web site is permissible only if the reproduction is without alteration and is accompanied by associated warranties, conditions, limitations and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. PLE is not responsible or liable for such altered documents.

Resale of PLE products or services with statements different from or beyond the parameters stated by PLE for that product or service voids all express and implied warranties for the associated PLE product or service and is an unfair or deceptive business practice. PLE is not responsible for any such statements.

Contacting Pletronics Inc.

Pletronics Inc. Tel: 425-776-1880 19013 36th Ave. West Fax: 425-776-2760

Lynnwood, WA 98036-5761 USA E-mail: ple-sales@pletronics.com

URL: www.pletronics.com

Copyright © 2018 Pletronics Inc.

www.pletronics.com 425-776-1880 8

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Standard Clock Oscillators category:

Click to view products by Pletronics manufacturer:

Other Similar products are found below:

601252 F335-12 F335-25 F535L-33.333 F535L-50 ASV-20.000MHZ-LR-T ECS-2018-160-BN-TR MXO45HS-2C-66.6666MHZ

SiT8209AI-32-33E-125.000000 SIT8918AA-11-33S-50.000000G SM4420TEV-40.0M-T1K F335-24 F335-40 F535L-10 F535L-12 F535L
16 F535L-24 F535L-27 F535L-48 PE7744DW-100.0M CSX-750FCC14745600T ASF1-3.686MHZ-N-K-S XO57CTECNA3M6864 ECS
2100A-147.4 601251 EP16E7E2H26.000MTR SIT8918AA-11-33S-16.000000G XO3003 9120AC-2D2-33E212.500000 9102AI
243N25E100.00000 8208AC-82-18E-25.00000 8008AI-72-XXE-24.545454E 8004AC-13-33E-133.33000X AS-4.9152-16-SMD-TR ASFL1
48.000MHZ-LC-T SIT8920AM-31-33E-25.0000 DSC1028DI2-019.2000 9121AC-2C3-25E100.00000 9102AI-233N33E100.00000X

9102AI-233N25E200.00000 9102AI-232H25S125.00000 9102AI-133N25E200.00000 9102AC-283N25E200.00000 9001AC-33-33E1-30.000

XLH536125.000JS4I 3921AI-2CF-33NZ125.000000 5730-1SF PXA000010 SIT1602BC-83-33E-10.0000000Y 8003AI-12-33S-40.000000Y