

Pletronics, Inc.

19013 36th Ave. W, Suite H · Lynnwood, WA 98036 USA Manufacturer of High Quality Frequency Control Products

SM7745H CMOS Series

- CMOS with Enable/ Disable or Optional Stand By Mode (3.3 V)
- Fundamental or 3rd Overtone Crystal Used
- 4 Pad 7 x 5mm Leadless Surface Mount Ceramic Clock Oscillator

1.500 MHz – 69.999 MHz

Standard Specifications

Overall Frequency Stability Operating Temperature Range Supply Voltage (Vcc) Symmetry (Duty Cycle) Logic Levels Output Load Enable/Disable Option (E/D)		SM7745H: ± 50 PPM, SM7744H: ± 25 PPM, SM7720H: ± 20 PPM over Operating Temp. Range 0 to +70°C is standard, but can be extended to - 40 to +85°C for certain frequencies 5.0 volts, 3.3 volts, 2.5 volts and 1.8 volts available, .01 µF bypass cap recommended 40/60 to 60/40% is standard, but 45/55% at 50% of Vcc is also available (see Waveform 1) Logic "1" 90% of Vcc MIN; Logic "0" 10% of Vcc MAX Standard load is 15 pF (typ. 1 ASIC) maximum, see Test Circuit 2 (consult factory for heavier loads) Output enabled when Pin #1 is open or at Logic "1"; Output disabled when Pin #1 is at Logic "0".				
Frequency Range (MHz)	Max. Supp Icc (mA) w/	ly Current 15pF load	Max. Rise and Fall Time Tr & Tf (nS) w/ 15pF load	_		
	2.5V, 3.3V	5.0V	2.5V to 5.0V	Frequency	Max. ICC (mA)	Max. Ir & If (nS)
1.500 - 10.999	7	10	5.0	Range (MHz) 1.500 - 39.999 40.000 - 69.999	w/ TSPF TOad	w/ 15pF load
11.000 - 23.999	15	15	5.0		1.8V	1.8V
24.000 - 29.999	15	20	5.0		10	5.0
30.000 - 45.999	20	30	5.0		25	3.0
46.000 - 69.999	25	45	4.5			

Part Numbering Guide



Consult factory for available frequencies and specs. Not all options available for all frequencies. A special part number may be assigned. Frequency Stability is inclusive of frequency shifts due to calibration, temperature, supply voltage, shock, vibration and load

Mechanical: inches (mm) not to scale Solder Pads

Due to part size and factory abilities, part marking may vary from lot to lot and may contain our part number or an internal code.



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