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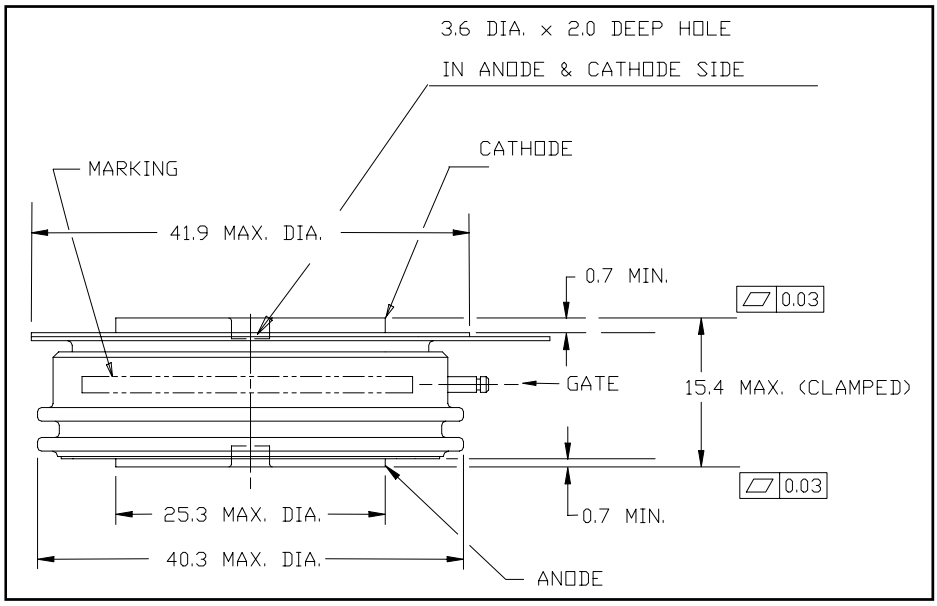
www.pwrx.com

Phase Control SCR
750 Amperes Average
1600 Volts



T7H8 750A (Outline Drawing)

T7H8 750A Phase Control SCR
750 Amperes Average, 1600 Volts

Description:
Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak, hermetic Pow-R-Disc devices employing the field proven amplifying gate.

Features:
- ☐ Low On-State Voltage
- ☐ High di/dt Capability
- ☐ High dv/dt Capability
- ☐ Hermetic Packaging
- ☐ Excellent Surge and I^2t Ratings

Applications:
- ☐ Power Supplies
- ☐ Motor Control

Ordering Information:
Select the complete 12 digit module part number from the table below.
Example: T7H8167504DN is a 1600V 750A Phase Control SCR.

Type	Voltage V_{RRM} (Volts)	Current $I_{T(av)}$ (A)	Turn-off Time t_q (µsec)	Gate Current I_{GT} (mA)	Lead Code
T7H8	02 through 16 200V through 1600V	75 750A	0 150 µsec typical	4 150 mA	DN 8”

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Absolute Maximum Ratings

Characteristics	Symbol		Units
Non-Repetitive Transient Peak Reverse Blocking Voltage	V_{RSM}	V_{RRM} + 100V	Volts
RMS On-State Current, T_C = 62°C	$I_{T(RMS)}$	1180	Amperes
Average Current 180° Sine Wave, T_C = 62°C	$I_{T(AV)}$	750	Amperes
RMS On-State Current, T_C = 55°C	$I_{T(RMS)}$	1260	Amperes
Average Current 180° Sine Wave, T_C = 55°C	$I_{T(AV)}$	800	Amperes
Peak One Cycle Surge On-State Current (Non-Repetitive) 60 Hz	I_{TSM}	10500	Amperes
Peak One Cycle Surge On-State Current (Non-Repetitive) 50 Hz	I_{TSM}	9600	Amperes
Critical Rate-of-rise of On-State Current (Non-Repetitive)	di/dt	600	A/µsec
Critical Rate-of-rise of On-State Current (Repetitive)	di/dt	150	A/µsec
I^2t (for Fusing) for One Cycle, 60 Hz	I^2t	460,000	A^2 sec
Peak Gate Power Dissipation	P_{GM}	16	Watts
Average Gate Power Dissipation	$P_{G(av)}$	3	Watts
Operating Temperature	T_J	-40 to +125	°C
Storage Temperature	T_{stg}	-40 to +150	°C
Approximate Weight		4	oz.
		113	g
Mounting Force		2000 to 2400	lb.
		900 to 1090	kg.

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Electrical Characteristics, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Repetitive Peak Reverse Leakage Current	I_{RRM}	T_J=125°C, $V_R = V_{RRM}$			30	mA
Repetitive Peak Forward Leakage Current	I_{DRM}	T_J=125°C, $V_D = V_{DRM}$			30	mA
Peak On-State Voltage	V_{TM}	I_{FM}=625A peak, Duty Cycle < 0.1 %			1.40	V
Threshold Voltage, Low-level Slope Resistance, Low-level	$V_{(TO)1}$ r_{T1}	T_J = 125°C, I = 15%$I_{T(AV)}$ to π$I_{T(AV)}$			0.97192 0.4818	V mΩ
Threshold Voltage, High-level Slope Resistance, High-level	$V_{(TO)2}$ r_{T2}	T_J = 125°C, I = π$I_{T(AV)}$ to I_{TSM}			1.4824 0.2845	V mΩ
V_{TM} Coefficients, Low-level		T_J = 125°C, I = 15%$I_{T(AV)}$ to π$I_{T(AV)}$ V_{TM} = A+ B Ln(I) +C(I) + D Sqrt(I)		A = B = C = D =	1.221 -0.1259 8.857 E-04 0.03348	
Typical Turn-On Time	t_{on}	I_T = 100A, V_D = 100 V		7		µs
Typical Turn-Off Time	t_q	T_j = 125°C, I_T = 250A, di_R/dt = 25A/µs Reapplied dv/dt = 20 V/µs Linear to 80% V_{DRM}		150		µs
Minimum Critical dv/dt – Exponential to V_{DRM}	dv/dt	T_J = 125°C	300			V/µs
Gate Trigger Current	I_{GT}	T_J = 25°C, V_D = 12 V			150	mA
Gate Trigger Voltage	V_{GT}	T_J = 25°C, V_D = 12 V			3.0	V
Non-Triggering Gate Voltage	V_{GDM}	T_J = 125°C, $V_D = V_{DRM}$			0.15	V
Peak Forward Gate Current	I_{GTM}				4	A
Peak Reverse Gate Voltage	V_{GRM}				5	V

Thermal Characteristics

Maximum Thermal Resistance, Double Sided Cooling		Max.	Units
Junction-to-Case	$R_{Θ(J-C)}$	0.04	°C/W
Case-to-Sink	$R_{Θ(C-S)}$	0.02	°C/W

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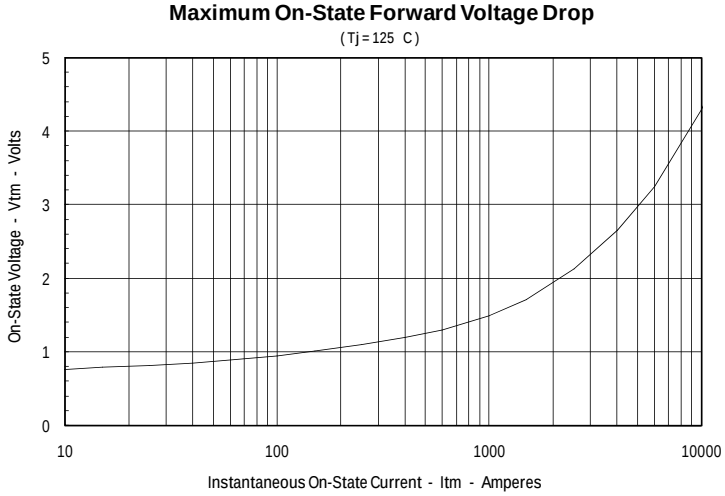
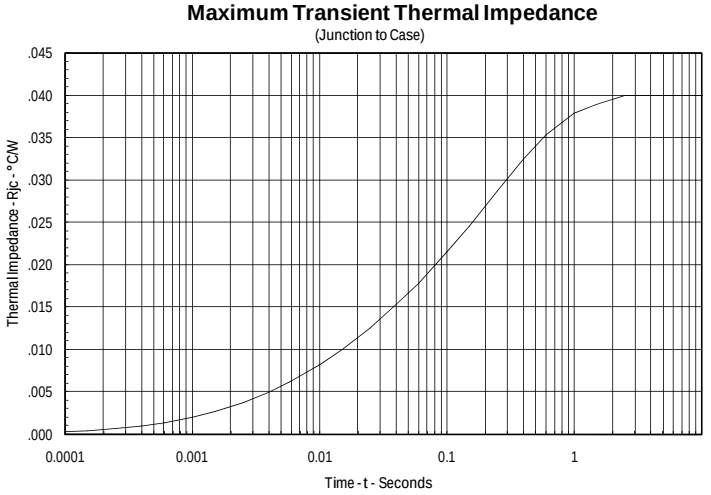
Maximum On-State Forward Voltage Drop

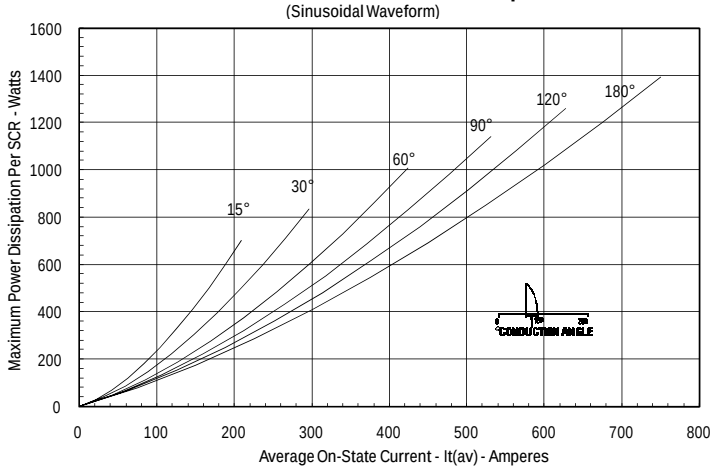


Maximum Transient Thermal Impedance



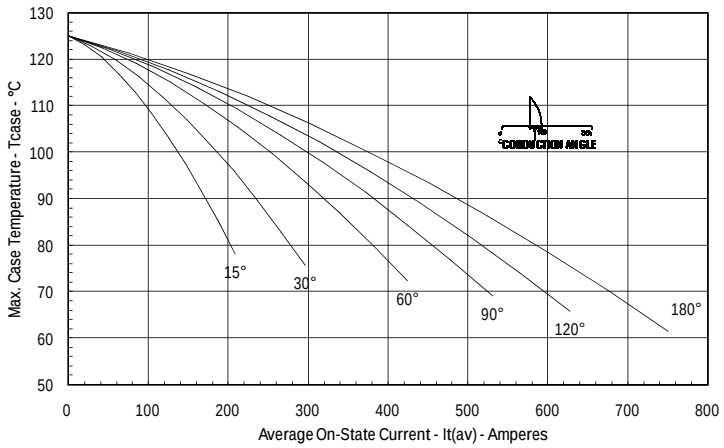
Maximum On-State Power Dissipation

(Sinusoidal Waveform)



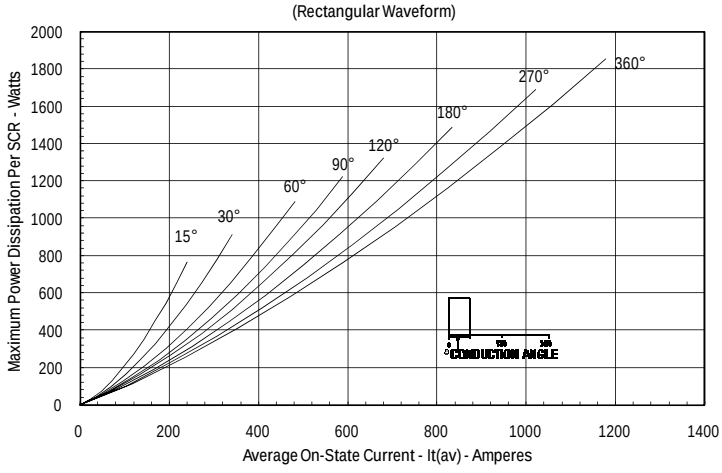
Maximum Allowable Case Temperature

(Sinusoidal Waveform)



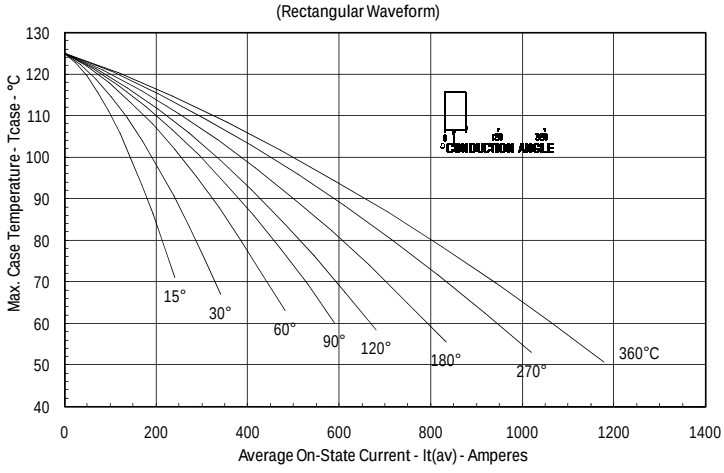
Maximum On-State Power Dissipation

(Rectangular Waveform)



Maximum Allowable Case Temperature

(Rectangular Waveform)



Revision Date: 11/13/2007

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