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DATA SHEET

DTC123YCA **DIGITAL TRANSISTORS (NPN)** VOLTAGE 50 Volts CURRENT 100 mA **FEATURES** • Built-in bias resistor enable the configuration of an inverter circuit without connecting external input resistors • The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects • Only the on/off conditions need to be set for operation, making device design easy. • Lead free and halogen-free. **MECHANICAL DATA** (1) IN (2) GND • NPN digital transistor (3) OUT

• Built-in resistor types



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTIC

MAXIMUM RATINGES(@ T_A=25°C UNLESS OTHERWISE NOTED)

PARAMETER		SYMBOL	VALUE			UNITS	
Supply Voltage		V _{CC}	50			V	
Input Voltage		V _{IN}	-10~30			V	
Output Current		Io	100		mA		
		I _{C(MAX)}	100			mA	
Power Dissipation		P _D	200			mW	
Junction Temperature		TJ	150			°C	
Storage Temperature Range		T _{STG}	- 55 to + 150			°C	
ELECTRICAL CHARACTERISTICS (AT T _A =25°C UNLESS OTHERWISE NOTED)							
PARAMETER	CONDITIONS	SYMBOL	MIN.	ТҮР	MAX.	UNIT	
Input Voltage	V _{CC} =5V, I ₀ =100uA	V _{I(off)}	-	-	0.3	V	
	V ₀ =0.3V, I ₀ =20mA	V _{I(on)}	3	-	-	V	
Output Voltage	$I_O/I_I = 10 \text{mA}/0.5 \text{mA}$	V _{O(on)}	-	-	0.3	V	
Input Current	V _I =5V	II	-	-	3.8	mA	
Output Current	$V_{CC}=50V, V_{I}=0V$	I _{O(off)}	-	-	0.5	μA	
DC Current Again	V ₀ =5V, I ₀ =5mA	GI	33	-	-	-	
Resistance Ratio	-	R_2/R_1	3.6	4.5	5.5	-	
Transition Frequency	V ₀ =10V, I ₀ =5mA, f=100MHz	f_{T}	-	250	-	MHz	
Input Resistance	-	R ₁	1.54	2.2	2.86	KΩ	



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Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
L	2.80	3.10	0.110	0.122	
L1	0.30	0.50	0.012	0.020	
W	2.25	2.54	0.089	0.100	
W1	1.20	1.40	0.047	0.055	
E	1.80	2.00	0.071	0.079	
Н	0.90	1.15	0.035	0.045	
H1	0.40	0.80	0.016	0.031	
D	0.30	0.50	0.012	0.020	
Т	0.08	0.15	0.003	0.006	
θ	0°	8°	0 °	8°	

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