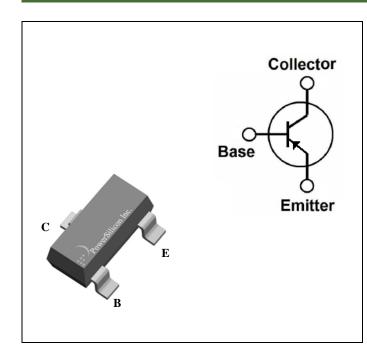


PLASTIC-ENCAPSULATE TRANSISTORS PNP Silicon



FEATURES

■ Collector Current : $I_C = -0.5A$

MECHANICAL DATA

Available in SOT-23 Package

■ Solderability: MIL-STD-202, Method 208

Full RoHS Compliance

ORDERING INFORMATION

PART NUMBER	PACKAGE	SHIPPING	MARKING CODE
S8550⊡-∆-T3	SOT-23	Tape Reel	2TY

Notes:

- none is for Lead Free package;
 "G" is for Halogen Free package.
- 2. \triangle : Rank Of h_{FE}; See Classification Of h_{FE}
- 3. Marking Code: yww: y: Year code; ww: Week code.

THERMAL DATA

PARAMETER	SYMBOL	VALUES	UNIT
Thermal Resistance, Junction-to-Ambient	$R_{ heta_JA}$	357	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta_{JC}}$	90	°C/W

Notes:

 $4.R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design. The value of $R_{\theta JA}$ is measured with device mounted on 1 in² FR-4 board with 2 oz copper.



ABSOLUTE MAXIMUM RATINGS

 $T_A = 25$ °C, unless otherwise noted.

PARAMETER	SYMBOL	VALUES	UNIT
Collector-Emitter Voltage	V_{CEO}	-25	V
Collector-Base Voltage	V _{CBO}	-40	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current-Continuous	Ic	-0.5	Α
Power Dissipation	P _C	0.3	W
Junction Temperature	TJ	150	°C
Storage Temperature	T_{stg}	-55 ~ +150	°C

ELECTRICAL CHARACTERISTICS

 $T_A = 25$ °C, unless otherwise noted.

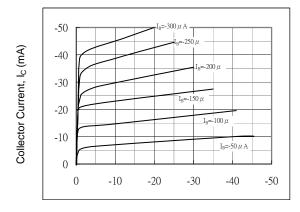
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{(BR)CEO}	$I_{C} = -1 \text{mA}, I_{B} = 0$	-25			V
V _{(BR)CBO}	$I_C = -100 \mu$ A, $I_E = 0$	-40			V
$V_{(BR)EBO}$	$I_E = -100 \mu$ A, $I_C = 0$	-5			V
I _{EBO}	$V_{EB} = -3V, I_{C} = 0$			-0.1	μ A
I _{CBO}	$V_{CB} = -40V, I_{E} = 0$			-0.1	μ A
I _{CEO}	$V_{CE} = -20V, I_B = 0$			-0.1	μ A
h _{FE(1)}	$V_{CE} = -1V, I_{C} = -50mA$	120		350	
h _{FE(2)}	$V_{CE} = -1V, I_{C} = -500 \text{mA}$	50			
V _{CE(sat)}	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$			-0.6	V
V _{BE(sat)}	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$			-1.2	V
f _T	$I_C = -20$ mA, $V_{CE} = -6$ V, $f = 30$ MHz	150			MHz
	V _{(BR)CEO} V _{(BR)CBO} V _{(BR)EBO} I _{EBO} I _{CEO} I _{CEO} h _{FE(1)} h _{FE(2)} V _{CE(sat)} V _{BE(sat)}	$\begin{array}{c c} V_{(BR)CEO} & I_C = -1 mA, \ I_B = 0 \\ \\ V_{(BR)CBO} & I_C = -100 \ \mu \ A, \ I_E = 0 \\ \\ V_{(BR)EBO} & I_E = -100 \ \mu \ A, \ I_C = 0 \\ \\ I_{EBO} & V_{EB} = -3 V, \ I_C = 0 \\ \\ I_{CBO} & V_{CB} = -40 V, \ I_E = 0 \\ \\ I_{CEO} & V_{CE} = -20 V, \ I_B = 0 \\ \\ \\ h_{FE(1)} & V_{CE} = -1 V, \ I_C = -50 mA \\ \\ h_{FE(2)} & V_{CE} = -1 V, \ I_C = -500 mA \\ \\ V_{CE(sat)} & I_C = -500 mA, \ I_B = -50 mA \\ \\ V_{BE(sat)} & I_C = -500 mA, \ I_B = -50 mA \\ \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

CLASSIFICATION OF h_{FE(1)}

RANK	L	Н	
h _{FE(1)} RANGE	120~200	200~350	



TYPICAL PERFORMANCE CHARACTERISTICS



Collector-Emitter Voltage, V_{CE} (V)

Fig.- Static Characteristic

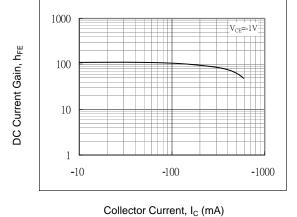


Fig.2 - DC Current Gain

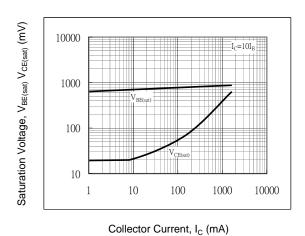
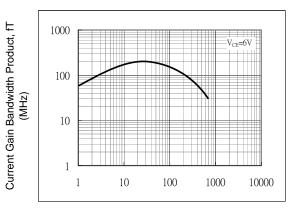


Fig.3 - Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage



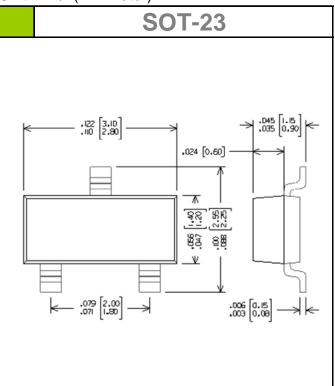
Collector Current, I_C (mA)

Fig.4 - Current Gain Bandwidth Product



PHYSICAL DIMENSION

Unit: Inch(Millimeter)



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NS 2N5769 2SC2412KT146S CPH6501-TL-E MCH4021-TL-E MJE340 Jantx2N5416 US6T6TR NJL0281DG 732314D CPH3121-TL-E

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