

SPECIFICATIONS

CUSTOMER	:	PTC
SAMPLE CODE	:	NSC1602LRU-FWT-H
MASS PRODUCTION CODE	:	NPC1602LRU-FWT-H
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	JLMD- NPC1602LRU-FWT-H _001
PACKAGING NO. (Ver.)	:	JPKG- NPC1602LRU-FWT-H _001

Customer Approved

Date:

Approved	Checked	Designer
閻偉	劉進	徐明菲

- ☐ Preliminary specification for design input
☒ Specification for sample approval

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History of Version

[illegible]

Total : 31 Pages

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Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0T

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective , Extended Temp.
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	40g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U-0T
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web site : http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	84.0 (L) * 44.0 (W) *12.7 (H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95(L)* 5.55 (W)	mm
Character Pitch	3.55(L)* 5.95 (W)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	-	V _{DD} -10.0	V _{DD} +0.3	
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C
Storage Humidity	H _D	Ta < 60 °C	-	90	%RH

1.4 DC Electrical Characteristics

$V_{DD}=5.0\pm0.5V$, $V_{SS}=0V$, $T_a = 25^{\circ}C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
“H” Input Voltage	V_{IH}	-	$0.7 V_{DD}$	-	V_{DD}	V
“L” Input Voltage	V_{IL}	-	-0.3	-	0.6	V
“H” Output Voltage	V_{OH}	$I_{OH}=-0.1mA$	3.9	-	V_{DD}	V
“L” Output Voltage	V_{OL}	$I_{OL}=0.1mA$	-	-	0.4	V
Supply Current	I_{DD}	$V_{DD}= 5.0 V$, $V_{op}= 4.5 V$	-	2	3	mA
LCM Driver Voltage	V_{OP*1}	$-20^{\circ}C$	4.4	4.6	4.8	V
		$25^{\circ}C$	4.3	4.5	4.7	
		$70^{\circ}C$	4.1	4.3	4.5	

NOTE: *1 The V_{OP} test point is ($V_{DD} - V_0$)

1.5 Optical Characteristics

LCD Panel : 1/16 Duty , 1/5 Bias , $V_{LCD} = 4.5\text{ V}$, $T_a = 25^\circ\text{C}$

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Reference
Response Time	Rise	tr	-	-	80	125	ms	Note 2
	Fall	tf		-	220	330		
Viewing angle range	Top	$\theta+$	$C \geq 2.0$	-	40	-	Deg.	Note 1
	Bottom	$\theta-$		-	40	-		
	Left	θ_L		-	45	-		
	Right	θ_R		-	45	-		
Contrast Ratio		C	$\theta = 0^\circ$	-	10	-	-	Note 3
Average Brightness (with LCD) *2		IV	IF=100 mA	30	35	-	cd/m ²	Note 4
Wavelength (with LCD) *2		λ_p		568	571	574	nm	
Uniformity *1		ΔB	IF=100 mA	70	-	-	%	

Note 4:

1 : $\Delta B = B(\text{min}) / B(\text{max}) * 100\%$

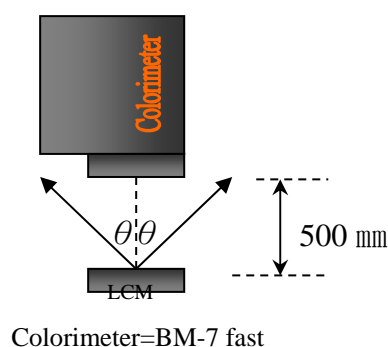
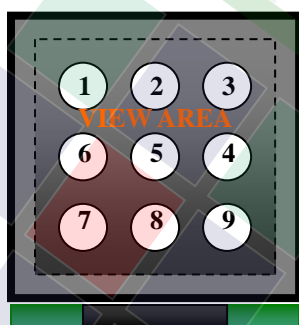
2 : Measurement Condition for Optical Characteristics:

a : Environment: $25^\circ\text{C} \pm 5^\circ\text{C}$ / $60 \pm 20\%$ R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm , ($\theta = 0^\circ$)

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

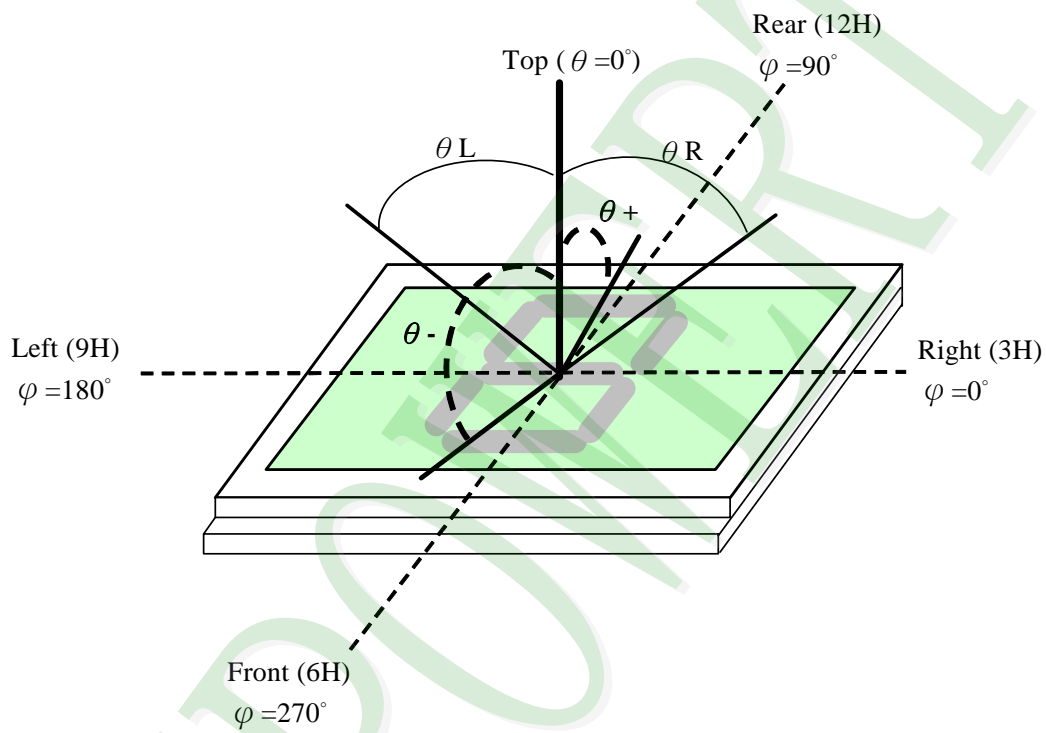
d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



Note 1.

Optical characteristics-2

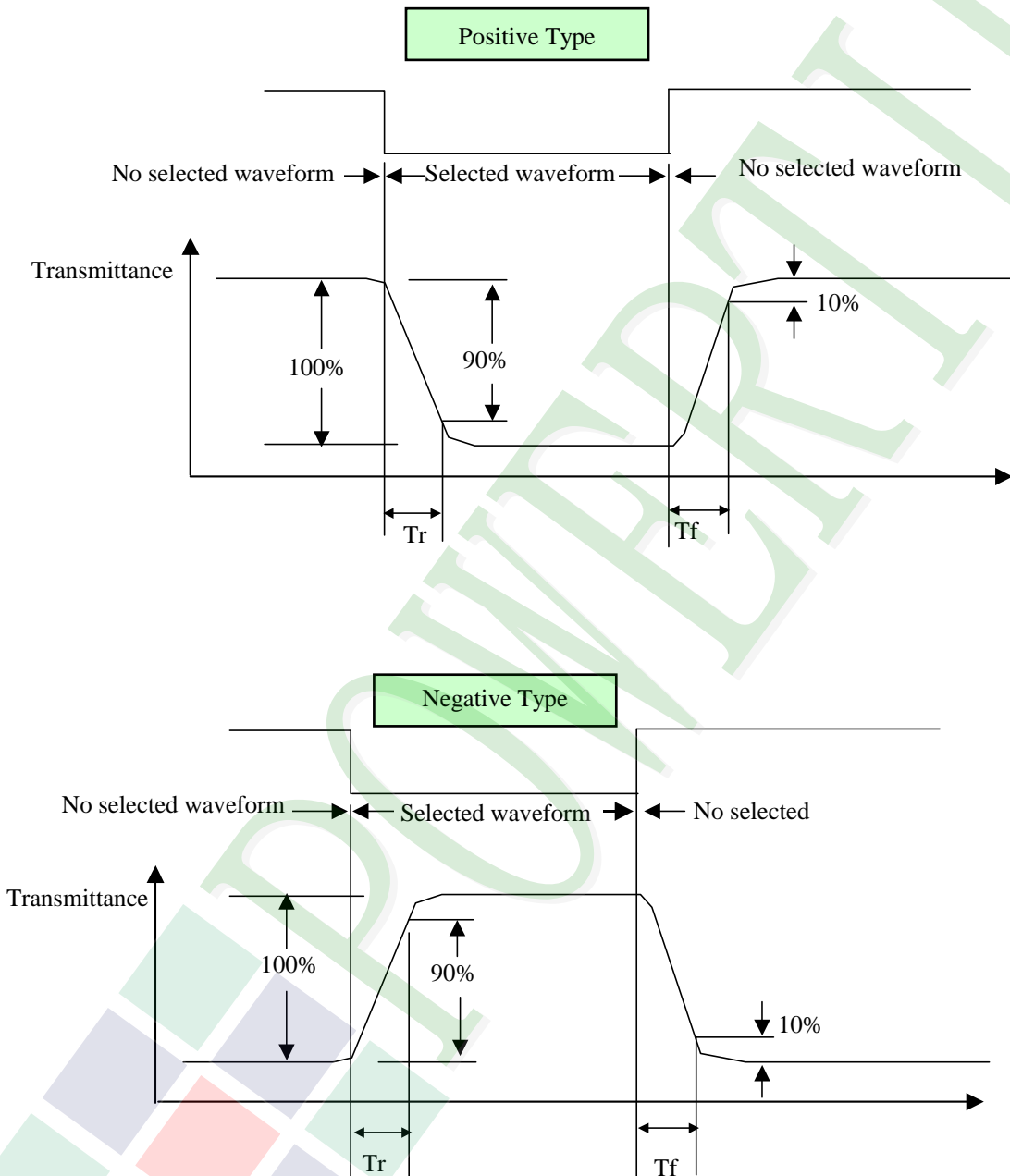
Viewing angle



Note 2.

Optical characteristics-3

Fig.2 Definition of response time



Electrical characteristics-2

※2 Drive waveform

V_{op} : Drive voltage

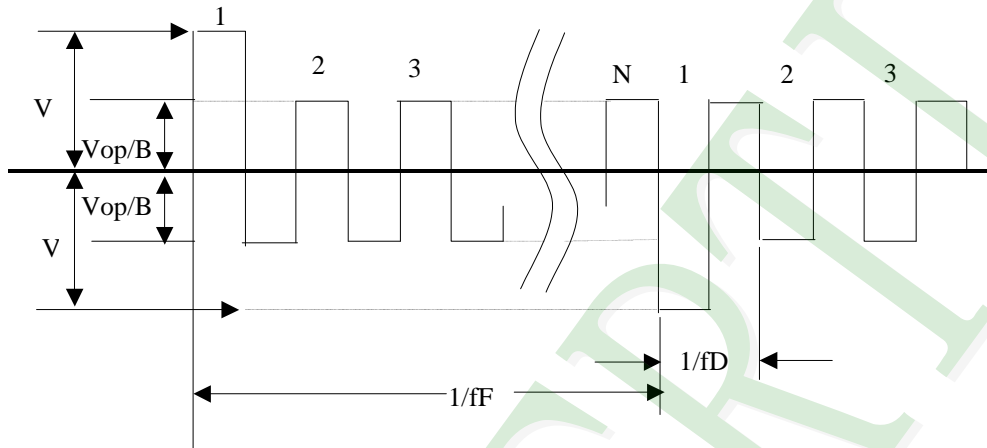
$1/B$: Bias

N : Duty

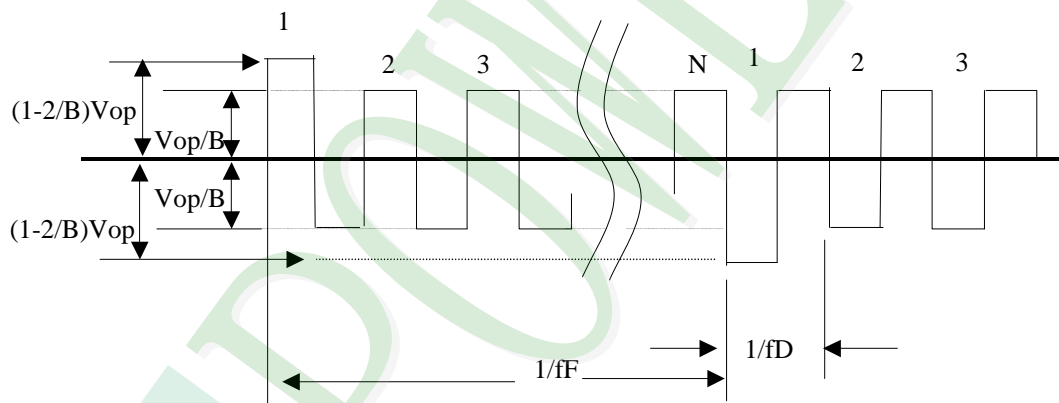
f_F : Frame frequency

f_D : Drive frequency

(1) Selected waveform



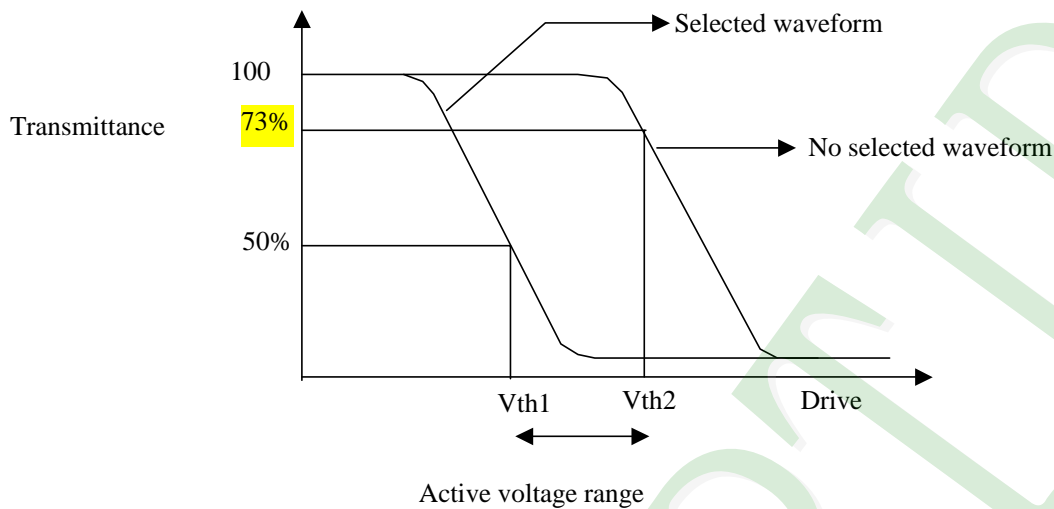
(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak / 2 = 1 period

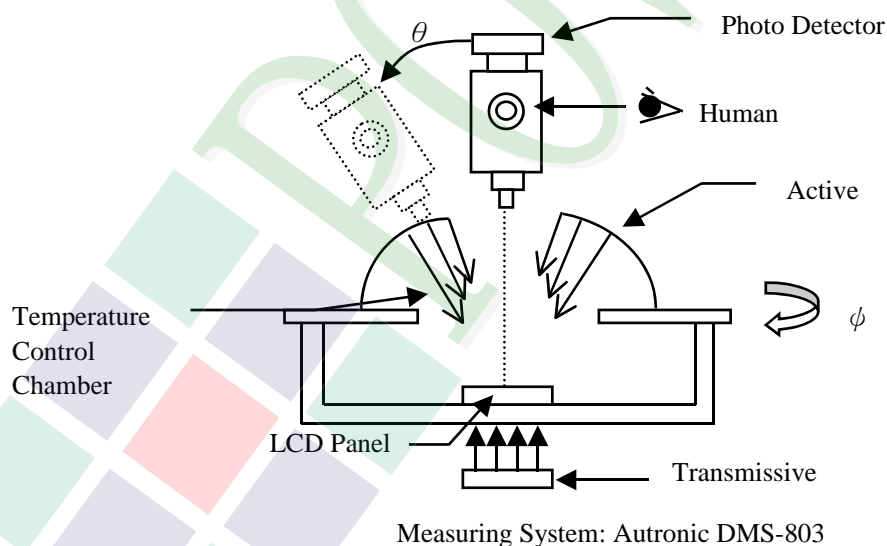
Note 3. : Definition of Vth



	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



1.6 Backlight Characteristics

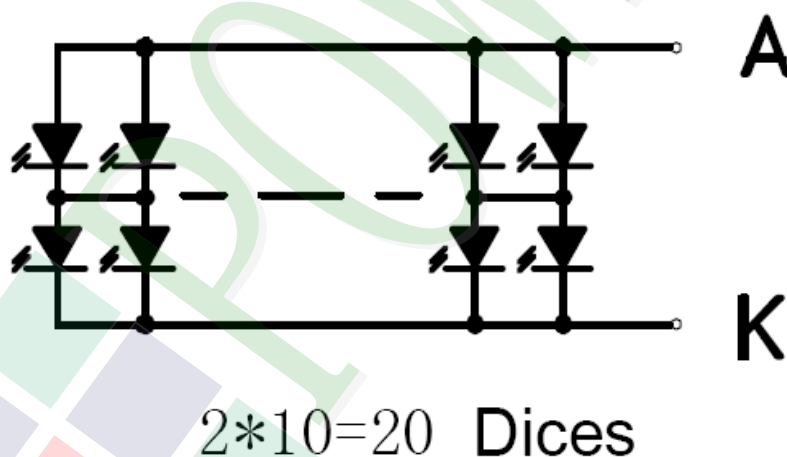
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25℃	-	150	mA
Reverse Voltage	VR	Ta =25℃	-	8	V
Power Dissipation	PD	Ta =25℃	-	660	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 100 mA	4.0	4.2	4.4	V
Reverse Current	IR	VR=8V	-	-	100	uA
Average Brightness	IV	IF= 100 mA	160	190	-	cd/m ²
Wavelength	λ p	IF= 100 mA	569	572	575	nm
Color	Yellow/Green					

Internal Circuit Diagram:



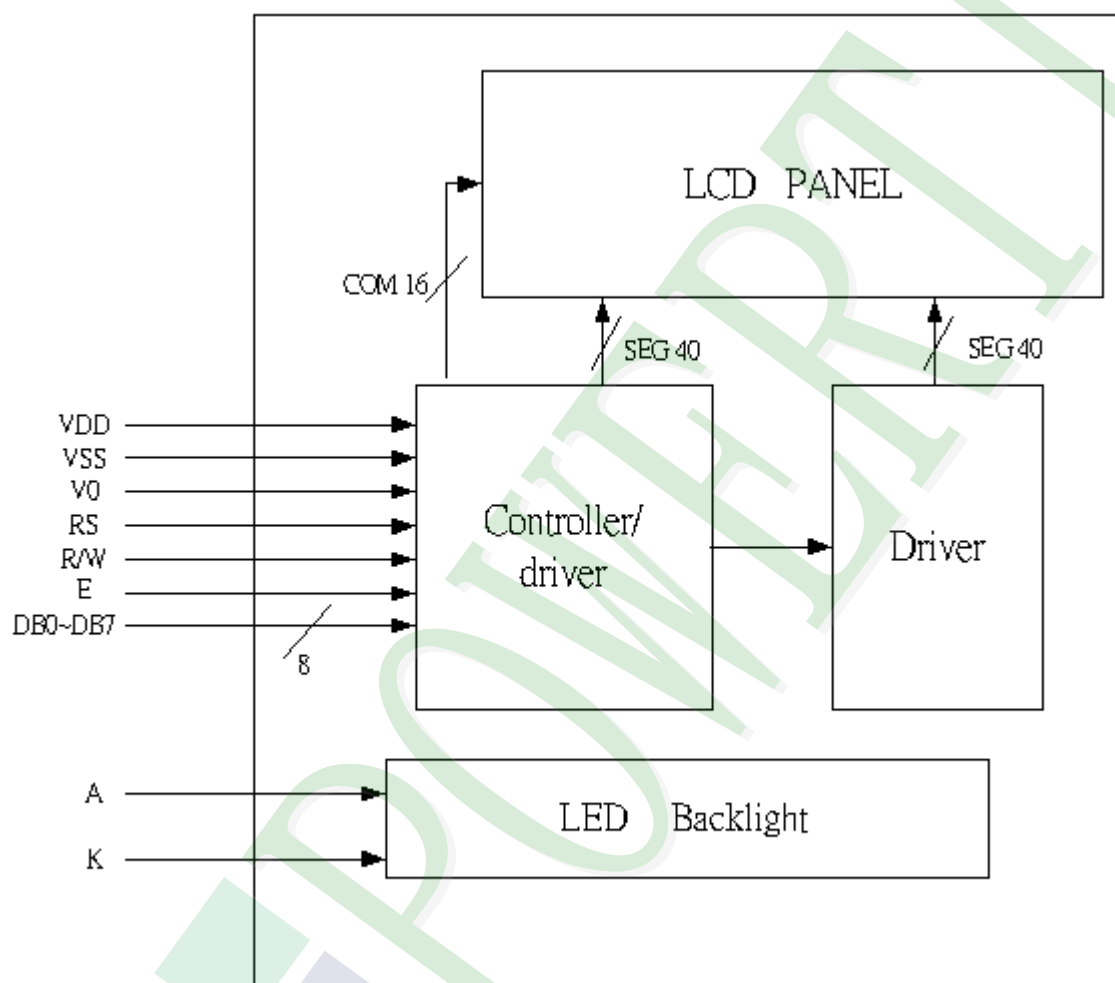
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

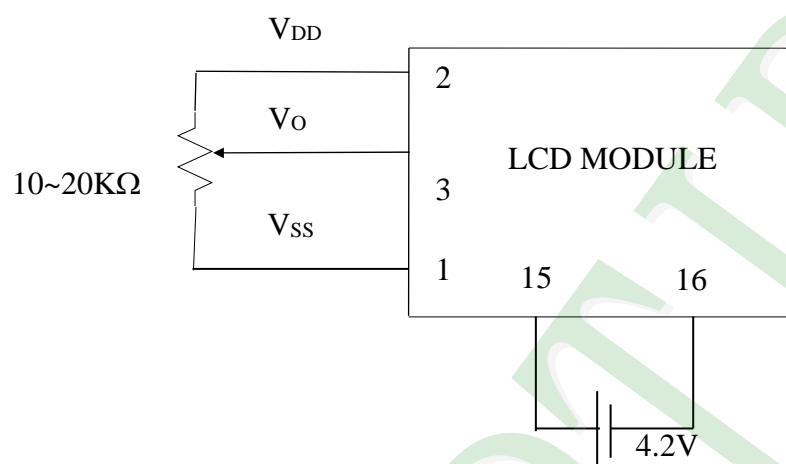


2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V _{SS}	Power Supply (V _{SS} =0)
2	V _{DD}	Power Supply (5V)
3	V _O	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	Four low order bi-directional three-state data bus lines. Use for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
8	DB1	
9	DB2	
10	DB3	
11	DB4	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.
12	DB5	
13	DB6	
14	DB7	
15	A	LED+
16	K	LED-

2.2.1 Application Notes

Contrast Adjust



2.2.2 Refer Initial code

```
void initial()
```

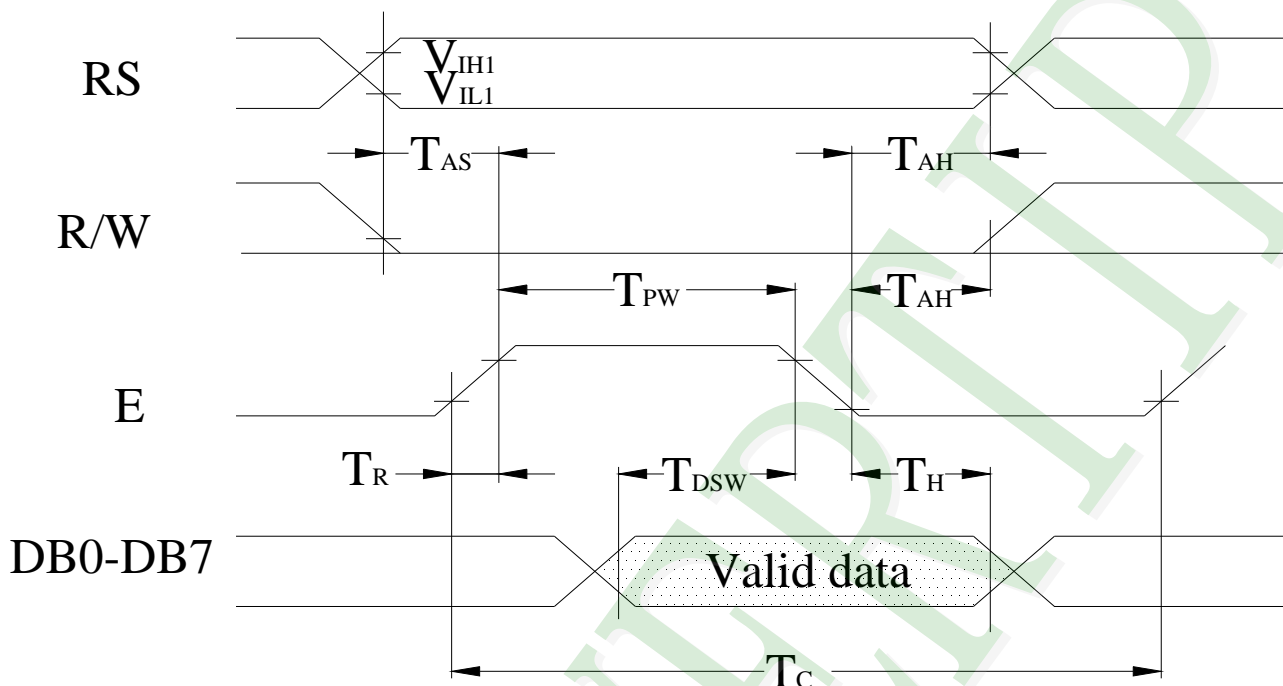
```
{
```

```
    delay(40);
    write_com(0x01);
    delay(5);
    write_com(0x38);
    delay(5);
    write_com(0x0c);
    delay(5);
    write_com(0x06);
    delay(5);
```

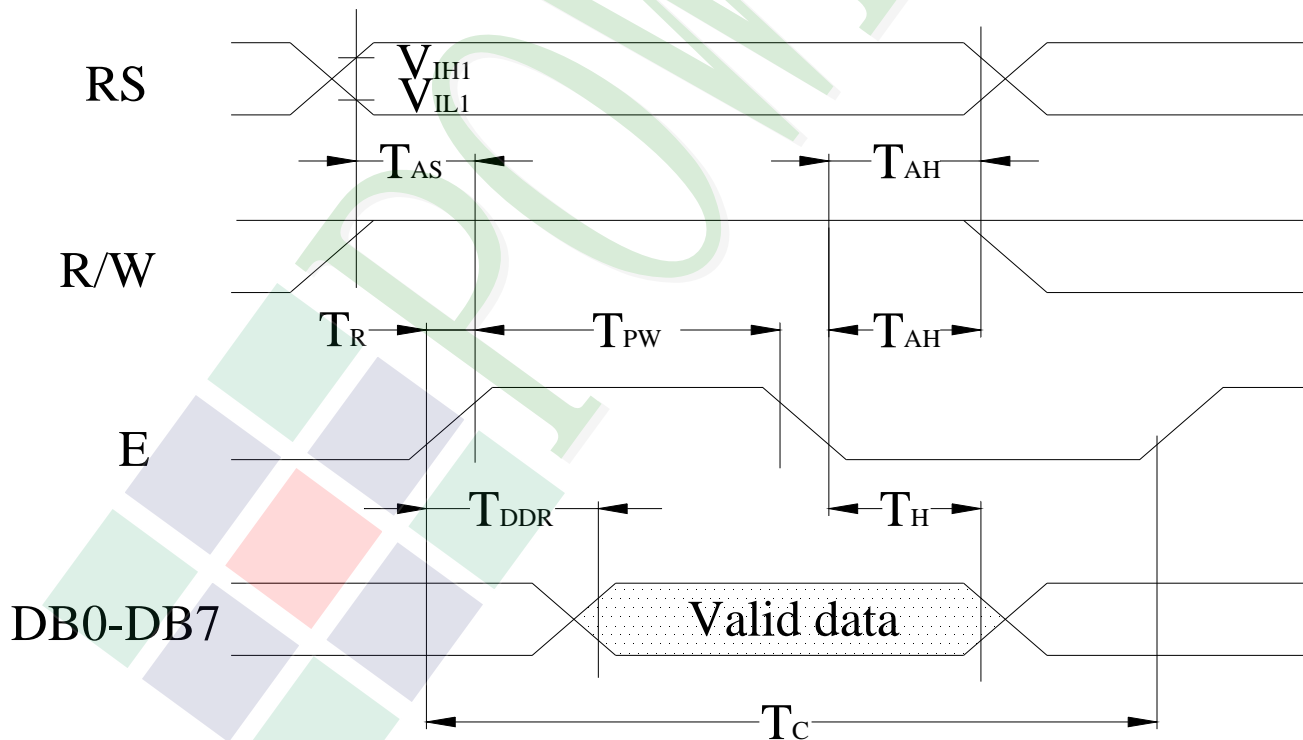
```
}
```

2.3 Timing Characteristics

- Writing data from MPU to ST7066U



- Reading data from ST7066U to MPU



• Write Mode (Writing data from MPU to ST7066U)

(VDD = +5V, Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

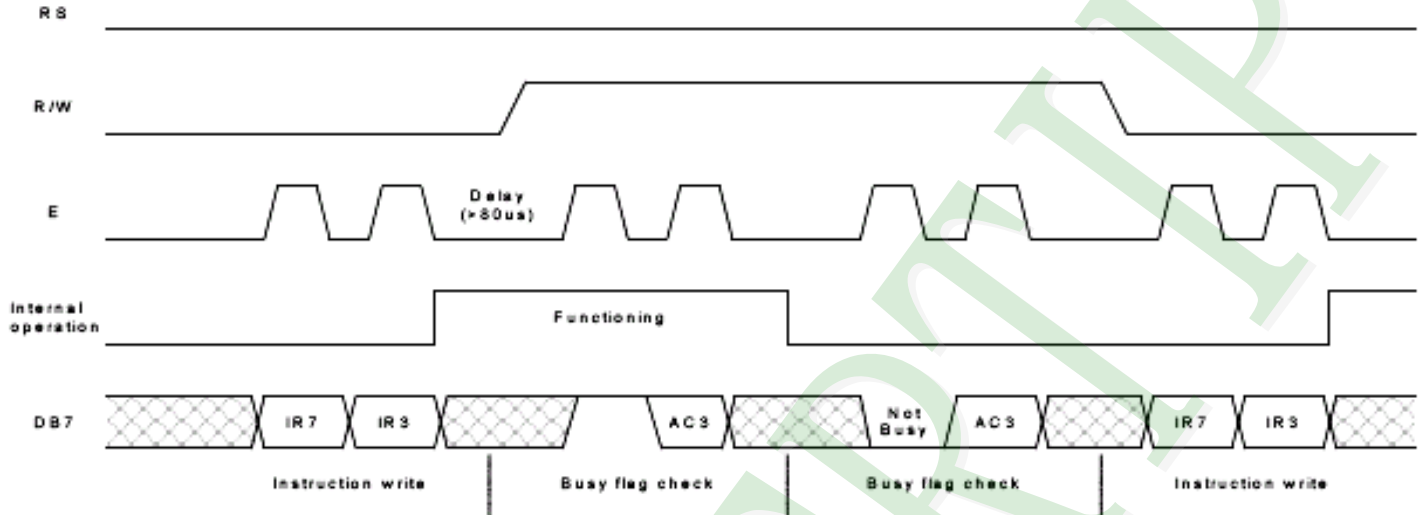
• Read Mode (Reading data from ST7066U to MPU)

(VDD = +5V, Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

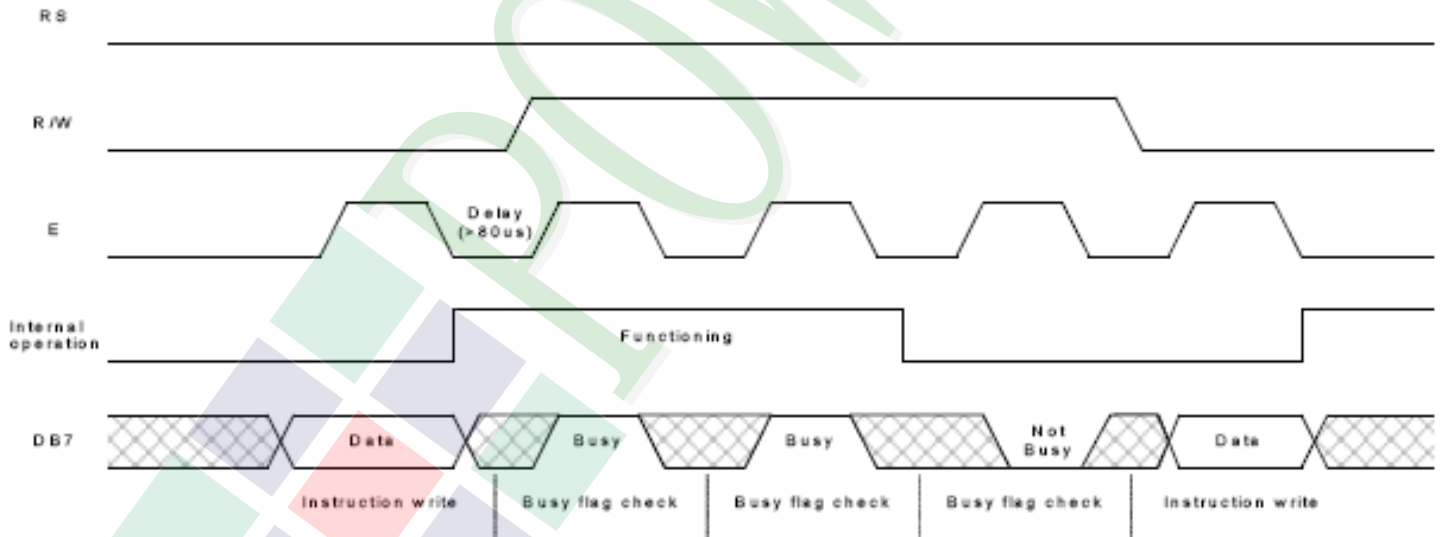
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Example of busy flag check timing sequence



2.4 Display Command

Instructions	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read .	37μs
Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the the direction, without changing of DDRAM data.	37μs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37μs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37μs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37μs
Read Busy Flag and Address	0	1	B F	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs

Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37 μ s
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37 μ s

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag , the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80 μ s.. Do not keep “E” always “High” for checking BF
Refer to Instruction Table for the list of each instruction execution time .

2.5 Character Pattern

ST7066-0T

b7-b4 b3-b0	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG FAM (1)			0	0	P		P			B	R	4	.	2	8
0001	(2)		!	1	A	Q	a	q			7	9	u	!	U	3
0010	(3)		"	2	B	R	b	r			E	6	%	U	W	9
0011	(4)		#	3	O	S	o	s			H	B	W	U	a	%
0100	(5)		\$	4	D	T	d	t			3	r	%	%	o	W
0101	(6)		%	5	E	U	e	u			H	e	a	%	U	%
0110	(7)		&	6	F	V	f	v			R	8	R	%	W	9
0111	(8)		*	7	G	W	g	w			J	3	9	!	!	!
1000	(1)		C	8	H	X	h	x			M	H	o	!	!	!
1001	(2)		>	9	I	Y	i	y			V	Q	%	!	!	!
1010	(3)		*	*	J	Z	j	z			Q	K	a	!	!	!
1011	(4)		+	*	K	L	k	l			Y	a	%	!	!	!
1100	(5)		.	%	L	Q	l	q			W	H	R	9	o	!
1101	(6)		-	=	M	J	m	j			b	H	C	!	!	!
1110	(7)		.	%	N	K	n	k			W	n	J	%	o	!
1111	(8)		/	%	O	L	o	e			G	T	E	.	o	!

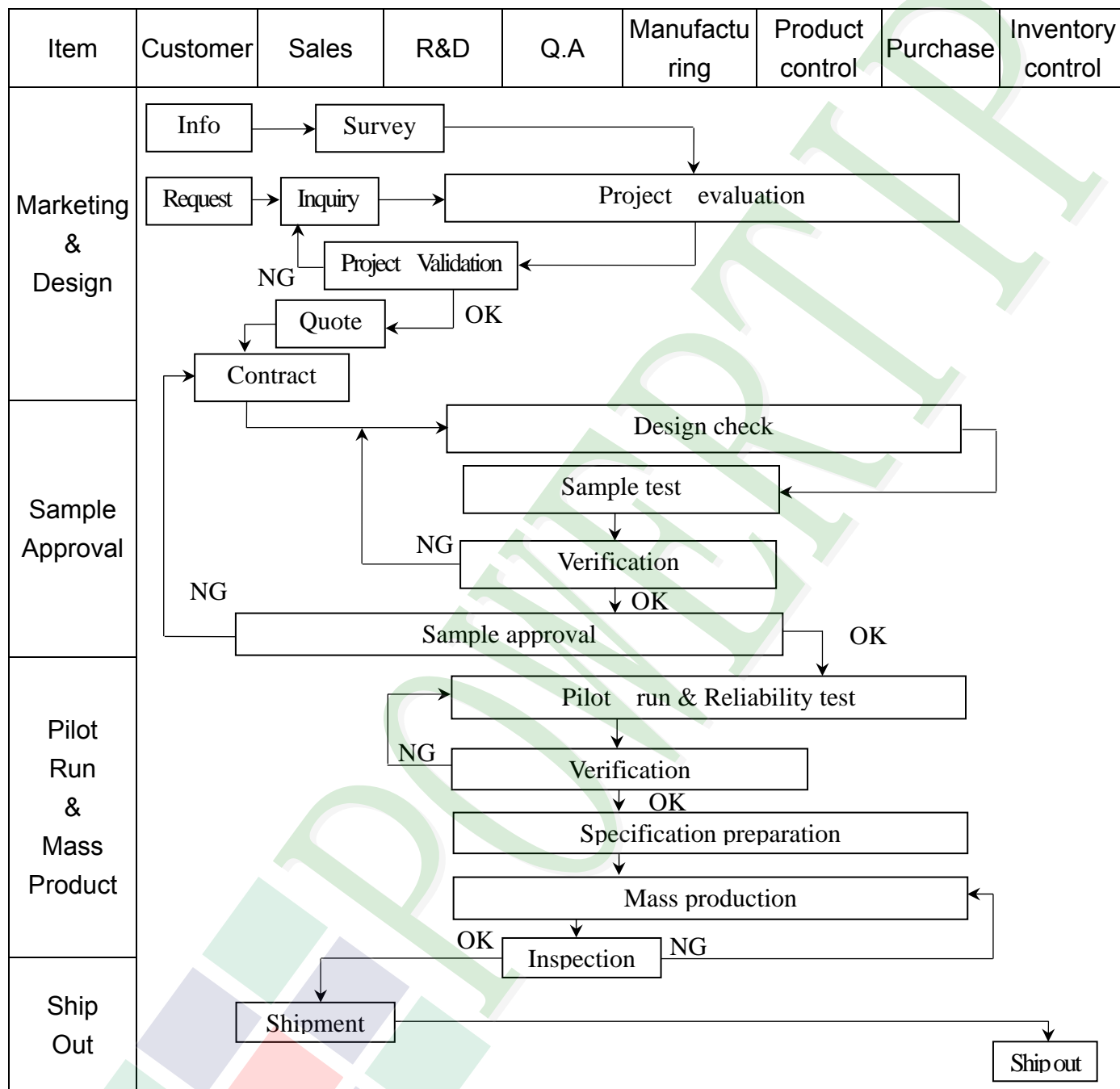
2.6 JUMPER(Setting different use)

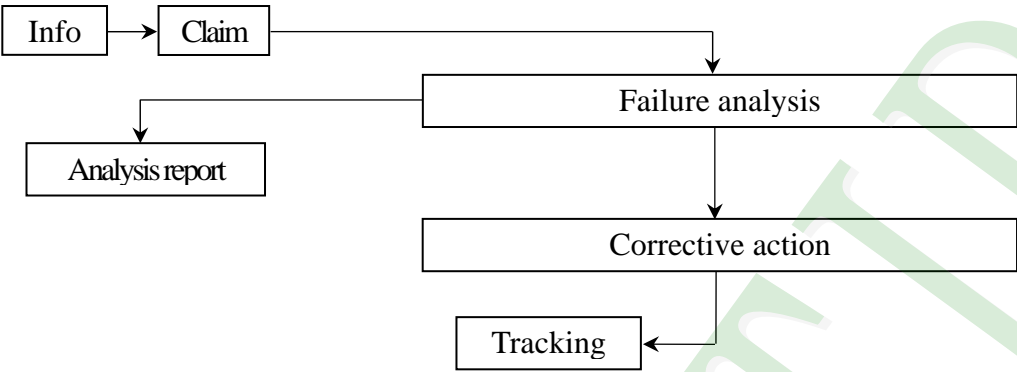
J1/J4:SHORT;

Others :open

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> FA[Failure analysis] Claim --> AR[Analysis report] FA --> CA[Corrective action] CA --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2 Inspection Specification

◆Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II .

◆Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample

◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect : AQL : 1.5 .

◆OUT Going Defect Level : Sampling .

◆Manner of appearance test :

(1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.

(2). Standard of inspection : (Unit : mm)

(3). The test direction is base on about around 45° of vertical line. (Fig. 1)

(4). Definition of area . (Fig. 2)

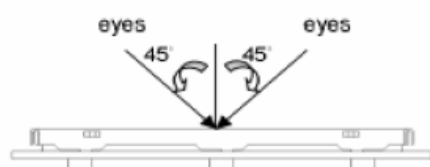


Fig.1

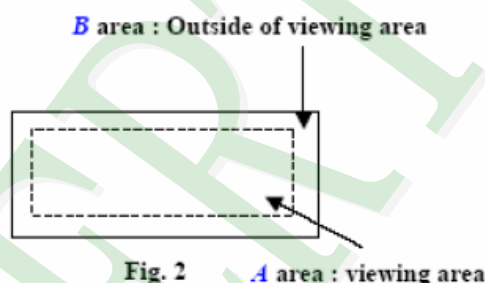


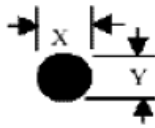
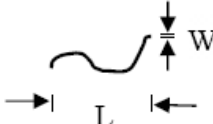
Fig. 2

◆ Specification:

NO	Item	Criterion	Level
01	Product condition	1. 1 The part number is inconsistent with work order of Production.	Major
		1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
04	Electrical Testing	4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
		4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major

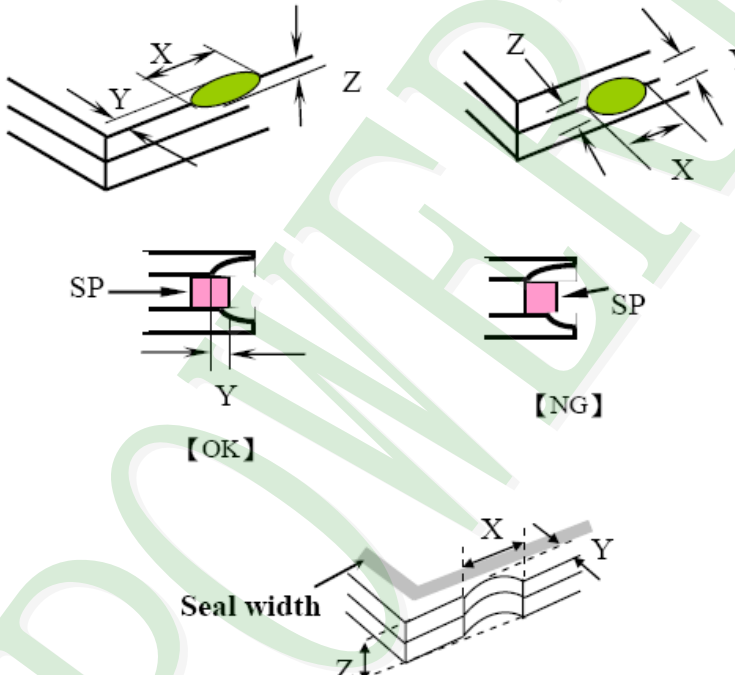
◆Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level																																				
05	<p>Black or white dot 、 scratch 、 contamination</p> <p>Round type</p>  <p>$\Phi=(x+y)/2$</p> <p>Line type</p> 	<p>5. 1 Round type:</p> <p>5. 1. 1 display only :</p> <ul style="list-style-type: none">• White and black spots on display ≤ 0.30 mm , no more than 4 white or black spots present.• Densely spaced : NO more than two spots or lines within 3 mm. <p>5. 1. 2 Non-display :</p> <table><thead><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr></thead><tbody><tr><td>$\Phi \leq 0.10$</td><td>Accept no dense</td><td rowspan="3">Ignore</td></tr><tr><td>$0.10 < \Phi \leq 0.20$</td><td>3</td></tr><tr><td>$0.20 < \Phi \leq 0.30$</td><td>2</td></tr><tr><td>Total quantity</td><td>4</td><td></td></tr></tbody></table> <p>5. 1. 3 Line type:</p> <table><thead><tr><th colspan="2">Dimension</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>Length (L)</th><th>Width (W)</th><th>A area</th><th>B area</th></tr></thead><tbody><tr><td>---</td><td>$W \leq 0.03$</td><td>Accept no dense</td><td rowspan="3">Ignore</td></tr><tr><td>$L \leq 3.0$</td><td>$0.03 < W \leq 0.05$</td><td rowspan="2">4</td></tr><tr><td>$L \leq 2.5$</td><td>$0.05 < W \leq 0.075$</td></tr><tr><td>---</td><td>$W > 0.075$</td><td colspan="2">As round type</td></tr></tbody></table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.10$	Accept no dense	Ignore	$0.10 < \Phi \leq 0.20$	3	$0.20 < \Phi \leq 0.30$	2	Total quantity	4		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Accept no dense	Ignore	$L \leq 3.0$	$0.03 < W \leq 0.05$	4	$L \leq 2.5$	$0.05 < W \leq 0.075$	---	$W > 0.075$	As round type		Minor
Dimension (diameter : Φ)	Acceptance (Q'ty)																																						
	A area	B area																																					
$\Phi \leq 0.10$	Accept no dense	Ignore																																					
$0.10 < \Phi \leq 0.20$	3																																						
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$L \leq 3.0$	$0.03 < W \leq 0.05$	4																																					
$L \leq 2.5$	$0.05 < W \leq 0.075$																																						
---	$W > 0.075$	As round type																																					
06	<p>Polarizer Bubble</p>	<table><thead><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr></thead><tbody><tr><td>$\Phi \leq 0.20$</td><td>Accept no dense</td><td rowspan="4">Ignore</td></tr><tr><td>$0.20 < \Phi \leq 0.50$</td><td>3</td></tr><tr><td>$0.50 < \Phi \leq 1.00$</td><td>2</td></tr><tr><td>$\Phi > 1.00$</td><td>0</td></tr><tr><td>Total quantity</td><td>4</td><td></td></tr></tbody></table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.20$	Accept no dense	Ignore	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$\Phi > 1.00$	0	Total quantity	4		Minor																			
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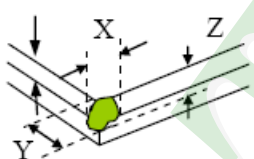
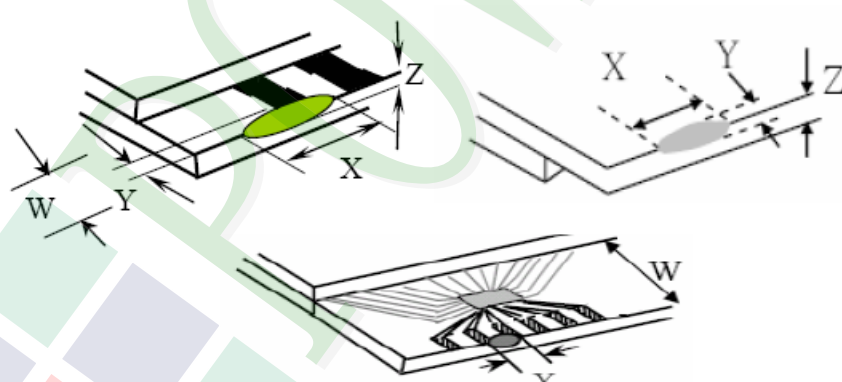
◆ Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level						
07	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p> <hr/>	Minor						
		<p>7.1 General glass chip :</p> <p>7.1.1 Chip on panel surface and crack between panels:</p> <div></div> <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq a$</td><td>Crack can't enter viewing area</td><td>$\leq 1/2 t$</td></tr><tr><td>$\leq a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
X	Y	Z							
$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$							
$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$							

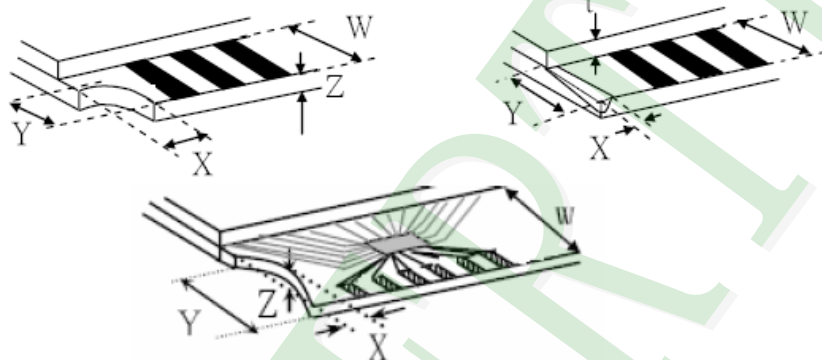
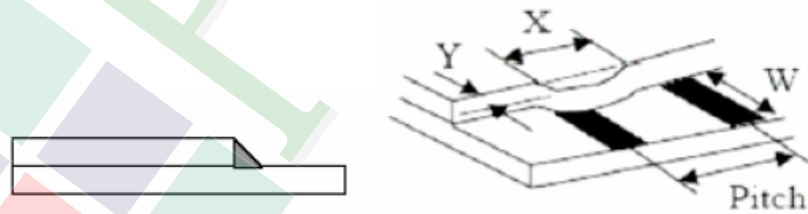
◆ Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level									
07	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p> <hr/> <p>7.1.2 Corner crack :</p>  <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't enter viewing area</td><td>$Z \leq 1/2 t$</td></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>	X	Y	Z	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
		X	Y	Z								
$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$										
$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$										
<p>7.2 Protrusion over terminal :</p> <p>7.2.1 Chip on electrode pad :</p>  <table><tr><th></th><th>X</th><th>Y</th><th>Z</th></tr><tr><td>Front</td><td>$\leq a$</td><td>$\leq 1/2 W$</td><td>$\leq t$</td></tr><tr><td>Back</td><td colspan="3">Neglect</td></tr></table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	Neglect		
	X	Y	Z									
Front	$\leq a$	$\leq 1/2 W$	$\leq t$									
Back	Neglect											

◆ Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level												
07	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p> <p>7.2.2 Non-conductive portion :</p> <div></div> <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq 1/3 a$</td><td>$\leq W$</td><td>$\leq t$</td></tr></table> <p>☉ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>7.2.3 Glass remain :</p> <div></div> <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq a$</td><td>$\leq 1/3 W$</td><td>$\leq t$</td></tr></table>	X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z	$\leq a$	$\leq 1/3 W$	$\leq t$	Minor
		X	Y	Z											
$\leq 1/3 a$	$\leq W$	$\leq t$													
X	Y	Z													
$\leq a$	$\leq 1/3 W$	$\leq t$													

◆ Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level
08	Backlight elements	8. 1 Backlight can't work normally.	Major
		8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
09	General appearance	9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security

systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.001	LCM包裝規格書 LCM Packaging Specifications		Approve	Check	Contact
Documents NO. JPKG-NPC1602LRU-FWT-H			Ryan	Terry	Air

1.包裝材料規格表 (Packaging Material) : (per carton)

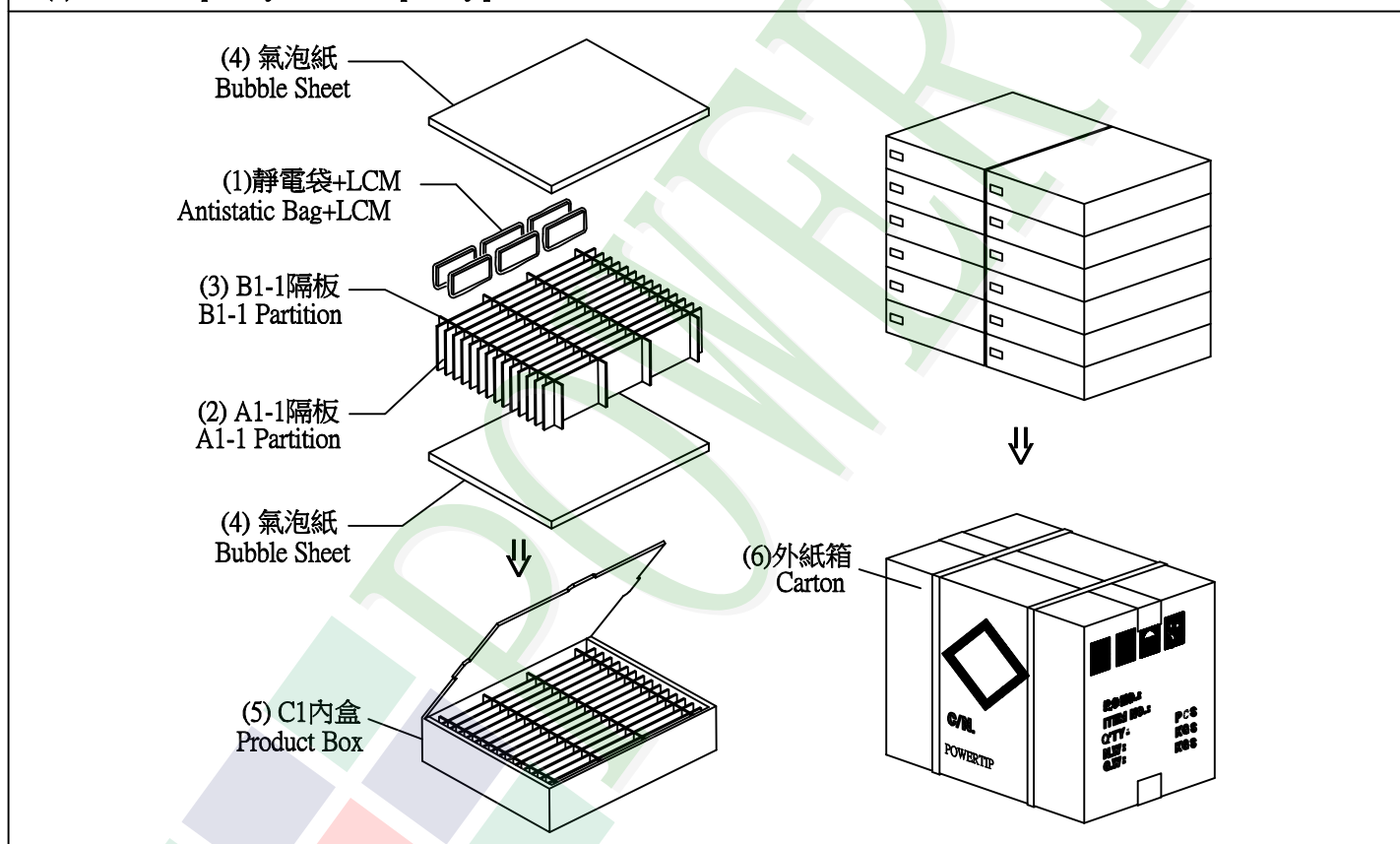
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	NPC1602LRU-FWT-H	84.0 X 44.0 X 12.7	0.034	468	15.912
2	靜電袋(1)Antistatic Bag	BAG100100ARABA	100 X 100	0.0011	468	0.5148
3	A1-1隔板(2)A1-1 Partition	BX29500047BZBA	295 X 47 X 3	0.0078	168	1.3104
4	B1-1隔板(3)B1-1 Partition	BX24500047BZBA	245 X 47 X 3	0.0065	48	0.312
5	氣泡紙(4)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	24	0.144
6	C1內盒(5)Product Box	BX31025555AABA	310 X 255 X 55	0.13	12	1.56
7	外紙箱(6)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

2.一 整箱總重量 (Total LCD Weight in carton) : 20.58 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)Quantity Of Spacer : A1-1隔板 X 14 , B1-1隔板 X 4

(2)Total LCM quantity in carton : quantity per box 39 x no of boxes 12 = 468



特 記 事 項 (REMARK)

4. Label Specifications : 標籤依廠內標準作業	5. LCM排放示意圖(前後間隔不放置): 5. LCM placed as figure showing: (First and last slot should be empty)	
	<p>模組(LCM) X 1pcs.</p>	

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