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WRF-KCN-H-Q (Rev.0)
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Date:
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RECORDS OF REVISION

Date	Rev.	Description	Note	Page
2006/11/30	0	PG12864WRF-KCN-H-Q is the ROHS compliant part number based on Powertip's standard PG12864WRF-KCN-H		

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Note : For detailed information please refer to IC data sheet : <u>NT7107,NT7108</u>



1. SPECIFICATIONS

1.1 Features

Item	Standard Value		
Display Type	128 * 64 dots		
LCD Type	FSTN, White, Transflective, Positive, Extended Temp.		
Driver Condition	LCD Module :1/64 Duty , 1/9 Bias		
Viewing Direction	6 O'clock		
Backlight	LED White B/L		
Weight	_		
Interface	_		
Other	_		

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	93.0(L) * 70.0(w) * 14.0(H)(Max)	mm
Viewing Area	72.0(L) * 40.0(w)	mm
Active Area	66.52(L) * 33.24(w)	mm
Dot Size	0.48(L) * 0.48(w)	mm
Dot Pitch	0.52(L) * 0.52(w)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	_	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	_	VDD-19.0	V _{DD} +0.3	V
Input Voltage	V_{IN}	_	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	Excluded B/L	-20	70	°C
Storage Temperature	T _{ST}	Excluded B/L	-30	80	°C
Storage Humidity	H _D	Ta<40 °C	-	90	%RH



1.4 DC Electrical Characteristics

$V_{DD} = 5.0 V \pm 0.5 V$, $V_{SS} = 0V$, $Ta = 25^{\circ}C$						
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V _{DD}	—	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	—	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V _{IL}	—	Vss	-	0.3 Vdd	V
"H" Output Voltage	V _{OH}	—	Vdd-0.4	-	-	V
"L" Output Voltage	V _{OL}	_	-	-	0.4	V
Supply Current	I _{DD}	$V_{DD} = 5.0 V$	-	4.5	6.5	mA
		-20°C	-	-	-	
LCM Driver Voltage	V _{OP}	25°C*1	12.3	12.5	12.7	V
		70°C		-	-	

Note: *1. THE V_{OP} TEST POINT IS V_{DD} - V_{O}

1.5 Optical Characteristics

LCD Panel : 1/64D	uty, 1/9 Bias	$V_{LCD} = 14.0V$,	Ta = 25°C
-------------------	---------------	---------------------	-----------

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C \geq 2.0, $\emptyset = 0^{\circ}$	0°	-	45°	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	5	8	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	150ms	-	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	280 ms	-	Note 4



Note 1: Definition of angles θ and \emptyset



Light (when transmitted) $Y(\emptyset=0^{\circ})$ $(\theta=90^{\circ})$ Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$



Note 3: Definition of contrast C



Note 4: Definition of response time





 V_{LCD} : Operating voltage f_{FRM} : Frame frequency t_r : Response time (rise) t_f : Response time (fall)



1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25℃	-	72	mA
Reverse Voltage	VR	Ta =25℃	-	5	V
Power Dissipation	РО	Ta =25℃	-	0.29	W

Electrical / Optical Characteristics

					la =	25 °C
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 40 mA	3	3.3	4	V
Reverse Current	IR	VR=5 V	-	-	0.15	mA
Average Brightness (with LCD)	IV	IF= 40 mA	120	190	-	cd/m ²
CIE Color Coordinate	Х	IE- 40 mA	0.260	0.290	0.320	
(Without LCD)	Y	IF= 40 IIIA	0.270	0.230	0.330	
Color			White			



2. MODULE STRUCTURE

2.1 Counter Drawing



POWERTIP

2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VSS	Signal ground (GND)
2	VDD	Power supply for logic (VDD> VSS)
3	Vo	Operating voltage for LCD (variable)
		Register selection input
4	D/\overline{I}	High =Data register
4	D/ 1	Low =Instruction register (for write)
		Busy flag address counter (for read)
5	R/W	R/\overline{W} signal input is used to select the read/write mode
		High =Read mode, Low =Write mode
6	Е	Start enable signal to read or write the data
7-14	DB0-DB7	Data bus
15	CS1	Chip enable for D2 (segment 1 to segment 64)
16	CS2	Chip enable for D3 (segment 65 to segment 128)
17	RST	Reset signal
18	Vout	Negative voltage power supply
19	А	Power supply for LED B/L(+)
20	K	Power supply for LED B/L(-)

Contrast Adjust





2.3 Timing Characteristics



MPU write timing







Characteristic	Symbol	Min.	Тур	Max	Unit
E Cycle	tc	1000	-	-	ns
E High Level Width	twn	450	-	-	ns
E Low Level Width	twL	450	-	-	ns
E Rise Time	tr	-	-	25	ns
E Fall Time	tF	-	-	25	ns
Address Set-Up time	tasu	140	-	-	ns
Address Hold Time	tан	10	-	-	ns
Data Set-Up Time	tdsu	200		-	ns
Data Delay Time	tD	-	-	320	ns
Data Hold Time (Write)	tohw	10	-	-	ns
Data Hold Time (Read)	tdhr	20	-	-	ns



2.4 Display command

Instructions	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Functions
											Controls the display on or
Display on/off	0	0	0	0	1	1	1	1	1	0/1	Off. Internal status and
											display RAM data is not
											affected.
											0: OFF , 1: ON
Set address	0	0	0	1			Y ad	dress			Sets the Y address in the Y
(Y address)							(0~	63)			address counter.
Set Page	0	0	1	0	1	1	1		Page		Sets the X address at the
(X address)									(0-7)		X register.
Display Start Line	0	0	1	1		Di	splay	start li	ne		Indicates the display data
(Z address)							(0~	63)			RAM displayed at the top
						-		-	-	-	of the screen.
	0	1	В	0	0	R	0	0	0	0	Reads status.
			U		Ν	Е					BUSY 1 : In operation
			S		/	S					0 : Ready
Status Read			Y		0	Е					ON/OFF 1 : Display OFF
					F	Т					0 : Display ON
					F						RESET 1 : Reset
											0 : Normal
											Writes data (DB0:7) into
											display data RAM. After
Write Display Data	1	0				Write	Data				writing instruction, Y
											address is increased by 1
											automatically.
											Reads data (DB0:7) from
Read Display Data	1	1				Read	Data				display data RAM to the
											data bus.
Detailed Exp	lanati	on									
Display C	n/Off	2									

(C	0	16	<u>,</u>

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.



Display Start Line (Z Address)

	RS	R/W	DB	7						.DB0
Code	0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address(AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, form the line specified by display start line instruction, is displayed. See figure 1.

Set page (X address)

	RS	R/V	V DE	3 7							DB	0	
Code	0	0	1	0	1 1	1	A	C2	AC	1	A	AC0]
		•				•	•				•		
X address	(ACO	-AC2) of the	e disp	lay data	RAM	is set in	the X a	ddress re	egister	r.Writing	g or readir	ng to o
from MP	U is e	xecute	ed in th	is spe	cified p	age ur	ntil the n	ext page	e is set. S	See fig	gure 2.		
Set Adress	(YA	ddress	5)										
	RS	R/V	V DE	3 7							Dl	B0	
Code	0	0	0	1	AC5	AC4	· AC	3	AC2	AC	C1	AC0	
Y address(AC	0-AC5)	of the	display	data R	AM is set	in the `	Y address	Counter.	An addres	s is set	by instru	ction and in	creased
by 1 automatically	by read	d or wr	ite opera	ations o	of display	data.							
<u>Status Rea</u>	<u>ıd</u>												
	RS	R/W	DB7									DB0	
Code	0	1	BUSY	Υ 0	ON/O	FF	REST	0		0	0	0	7
													_
• Busy													
When bus	When busy is 1, the Chip is executing internal operation and no instructions are accepted												
When bus	When busy is 0, the Chip is ready to accept any instructions.												



POWERTIP

• ON/OFF

When on/off is 1, the display is OFF.

When on/off is 0, the display is ON.

• RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in the usual operation condition.

Write Display Data

	RS	R/W	/ DB7							DB0
Code	0	1	D7	D6	D5	D4	D3	D2	D1	D0

Write data(D0-D7) into the display data RAM.After writing instruction, Y address is increased by 1 automatically.

Read Display Data

	R/W	D/I	DB7.				•••••			.DB0
Code	1	1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data(D0-D7) from the display data RAM. After reading instruction, Y address is increased by 1 automatically









Note: "128*64" consist of 2 "64*64"

 $CS1 \Rightarrow$ Chip enable for left 64*64 (segment1 to segment 64)

 $CS2 \Rightarrow$ Chip enable for right 64*64 (segment 65 to segment 128)



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II • Equipment : Gauge • MIL-STD • Powertip Tester • Sample •

IQC Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 •

FQC Defect Level : 100% Inspection •

OUT Going Defect Level : Sampling •

Specification :

NO	Item	Specification	Judge	Level
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major
2	Quantity	The quantity is inconsistent with work order of production	N.G.	Major
	Flectronic	The display lacks of some patterns.	N.G.	Major
	characteristics of	Missing line.	N.G.	Major
3	LCM	The size of missing dot, A is $> 1/2$ Dot size	N.G.	Major
	$A=(L+W)\div 2$	There is no function.	N.G.	Major
		Output data is error	N.G.	Major
		Material is different with work order of production	N.G.	Major
		LCD is assembled in inverse direction	N.G.	Major
		Bezel is assembled in inverse direction	N.G.	Major
		Shadow is within LCD viewing area + 0.5 mm	N.G.	Major
	Appearance of	The diameter of dirty particle, A is $>$ 0.4 mm	N.G.	Minor
	$\begin{array}{c} \text{LCD} \\ \text{A=(} \text{L} + \text{W} \text{)} \div 2 \end{array}$	LCD $=(L+W)\div 2$ Dirty particle length is >3.0mm, and 0.01mm < width $\leq 0.05mm$		Minor
4	D ¹	Display is without protective film	N.G.	Minor
	Dirty particle	Conductive rubber is over bezel 1mm	N.G.	Minor
	(Including	Polarizer exceeds over viewing area of LCD	N.G.	Minor
	scratch v bubble)	Area of bubble in polarizer, $A > 1.0$ mm, the number of		
		bubble is >1 piece.	N.G.	Minor
		0.4 mm $<$ Area of bubble in polarizer, A < 1.0 mm, the number of bubble is >4 pieces	N.G.	Minor
		Burned area or wrong part number is on PCB	NG	Major
		The symbol character and mark of PCB are	IN.U.	Iviajoi
		unidentifiable	N.G	Minor
		The stripped solder mask , A is>1.0mm	N.G.	Minor
		0.3 mm $<$ stripped solder mask or visible circuit, A $<$	NG	NC
_	Appearance of	1.0mm, and the number is ≥ 4 pieces	N.G.	Minor
5	PCB	There is particle between the circuits in solder mask	N.G	Minor
	A = (L + W) = 2	The circuit is peeled off or cracked	N.G	Minor
		There is any circuits risen or exposed.	N.G	Minor
		0.2 mm < Area of solder ball, A is ≤ 0.4 mm	NG	
		The number of solder ball is ≥ 3 pieces	N.G	Minor
		The magnitude of solder ball, A is >0.4 mm.	N.G	Minor



NO	Item	Specification	Judge	Level
		The shape of modeling is deformed by touching.	N.G.	Major
	Appearance of	Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
6	molding A= $(L+W)\div 2$	Excessive epoxy: Diameter of modeling is >20 mm or height is >2.5 mm	N.G.	Minor
		The diameter of pinhole in modeling, A is >0.2 mm.	N.G.	Minor
		The folding angle of frame must be $>45^{\circ} +10^{\circ}$	N.G.	Minor
7	Appearance of frame	The area of stripped electroplate in top-view of frame, A is > 1.0 mm.	N.G.	Minor
/	$A=(L+W)\div 2$	Rust or crack is (Top view only)	N.G.	Minor
		The scratched width of frame is >0.06 mm. (Top view only)	N.G.	Minor
		The color of backlight is nonconforming	N.G.	Major
	Electrical	Backlight can't work normally.	N.G.	Major
0	backlight	The LED lamp can't work normally	N.G.	Major
0		The unsoldering area of pin for backlight, A is $> 1/2$ solder joint area.	N.G.	Minor
	M(L+W):Z	The height of solder pin for backlight is >2.0 mm	N.G.	Minor
		The mark or polarity of component is unidentifiable.	N.G.	Minor
		The height between bottom of component and surface of the PCB is floating >0.7 mm	N.G.	Minor
10	Assembly parts A=(L+W)÷2	D > 1/4W W \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow	N.G.	Minor
	$M(\mathbf{L} + \mathbf{W})$	End solder joint width, D' is $>50\%$ width of component termination or width of pad	N.G.	Minor
		Side overhang, D is $>25\%$ width of component termination.	N.G.	Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.	N.G.	Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is <0.5 mm.	N.G.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	Item	Test Condition					
	Lich Tommentum	Storage at 80 $\pm 2^{\circ}$ C 96~100 hrs					
1	Storage	Surrounding temperature, then storage at normal condition					
	Storage	4hrs					
	I ow Temperature	Storage at -30 $\pm 2^{\circ}$ C 96~100 hrs					
2	Storage	Surrounding temperature, then stor	rage at normal condition				
	Storage	4hrs					
		1.Storage 96~100 hrs 60±2°C, 90~	95%RH surrounding				
		temperature, then storage at nor	mal condition 4hrs.				
3	High Temperature	(Excluding the polarizer).					
5	/Humidity Storage	or					
		2.Storage 96~100 hrs 40±2°C, 90~	95%RH surrounding				
		temperature, then storage at nor	mal condition 4 hrs.				
		$-20^{\circ}\text{C} \rightarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}\text{C} \rightarrow 25^{\circ}\text{C}$					
4	Temperature Cycling	(30mins) (5mins) (30mins) (5mins)					
		10 Cycle					
		10~55Hz (1 n	ninute) 1.5mm				
5	Vibration	X,Y and Z direction * (each 2hrs)					
			· · · · ·				
		Air Discharge:	Contact Discharge:				
		Apply 6 KV with 5 times	Apply 250V with 5 times				
6	ESD Test	discharge for each polarity +/-	discharge for each polarity +/-				
0	ESD Test	Testing le setient	Testing location:				
		A manual the free of LCD	1.Apply to bezel.				
		Around the face of LCD	2.Apply to Vdd, Vss.				
		Packing Weight (Kg)	Drop Height (cm)				
		0~45.4	122				
7	Drop Test	45.4 ~ 90.8	76				
		90.8 ~ 454	61				
		Over 454	46				

POWERTIP

5. PRECAUTION RELATING PRODUCT HANDLING 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

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