

## PDTC114TE

# **Digital Transistor(built-in resistors)**

### Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

#### **Applications**

- Inverter
- Interface
- Driver

### **Mechanical Characteristics**

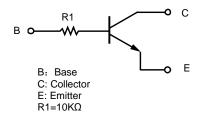
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

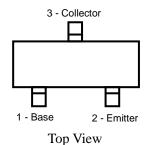
#### Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

### Electrical characteristics per line@25°C( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Collector-base breakdown voltage	ВV <sub>сво</sub>	Ι <sub>C</sub> =50μΑ	50			V
Collector-emitter breakdown	BV <sub>CEO</sub>	I <sub>C</sub> =1mA	50			V
Emitter-base breakdown voltage	BV <sub>EBO</sub>	Ι <sub>Ε</sub> =50μΑ	5			V
Collector cutoff current	I <sub>CBO</sub>	V <sub>CB</sub> =50V			0.5	μA
Emitter cutoff current	I <sub>EBO</sub>	V <sub>EB</sub> =4V			0.5	μA
Collector-emitter saturation	V <sub>CE(sat)</sub>	I <sub>C</sub> /I <sub>B</sub> =5mA/0.25mA			0.3	V
DC current transfer ratio	h <sub>FE</sub>	I <sub>C</sub> =1mA, V <sub>CE</sub> =5V	100	250	600	-
Input resistance	R <sub>1</sub>	-	7	10	13	kΩ
Transition frequency	f <sub>T</sub>	$V_{CE}$ =10V, I <sub>E</sub> = -5mA,		250		MHz





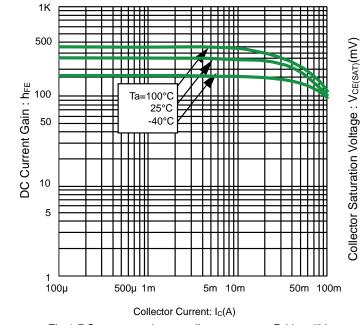
# Digital Transistor(built-in resistors)

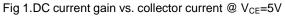
### PDTC114TE

## Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	Ι <sub>C</sub>	100	mA
Collector power dissipation	Pc	150	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

## **Typical Characteristics**





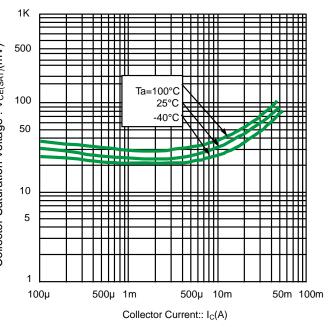


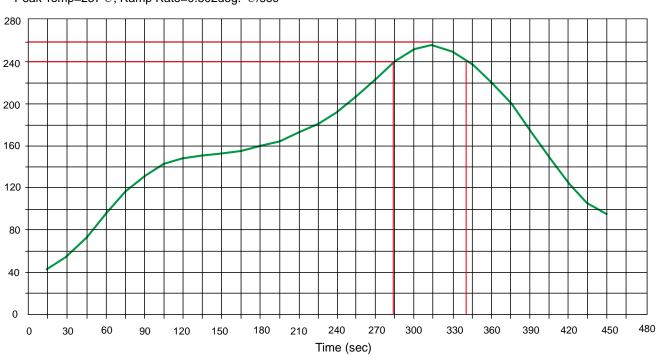
Fig 2.Collector-emitter saturation voltage vs. collector current

 $@I_C/I_B=10$ 

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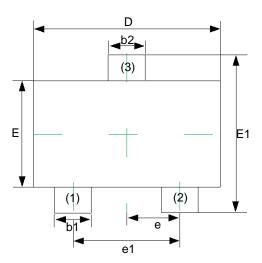
### PDTC114TE

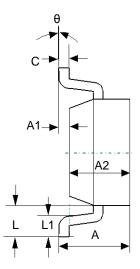
## **Solder Reflow Recommendation**



### Peak Temp=257 $^\circ\!\mathrm{C}$ , Ramp Rate=0.802deg. $^\circ\!\mathrm{C}/\!\text{sec}$

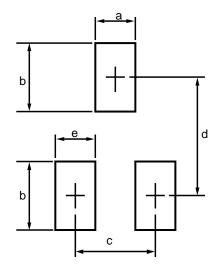
### Product dimension (SOT-523)





# Digital Transistor(built-in resistors)

Dim	Millimeters		Inches		
Diin	MIN	МАХ	MIN	МАХ	
А	0.700	0.900	0.028	0.035	
A1	0.000	0.100	0.000	0.004	
A2	0.700	0.800	0.028	0.031	
b1	0.150	0.250	0.006	0.010	
b2	0.250	0.350	0.010	0.014	
с	0.100	0.200	0.004	0.008	
D	1.500	1.700	0.059	0.067	
E	0.700	0.900	0.028	0.035	
E1	1.450	1.750	0.057	0.069	
е	0.500TYP		0.020TYP		
e1	0.900	1.100	0.035	0.043	
L	0.400REF		0.016REF		
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	



Dim	Millimeters			
	MIN	МАХ		
а		0.5		
b		0.6		
с		1.0		
d		1.24		
е		0.4		

## Ordering information

Device	Package	Shipping
PDTC114TE	SOT-523 (Pb-Free)	3000 / Tape & Reel

## PDTC114TE

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