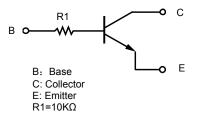


Digital Transistor(built-in resistors)

Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.



Applications

- Inverter
- Interface
- Driver

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260 °C
- Device meets MSL 1 requirements
- ➤ Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Collector-base breakdown voltage	BV _{CBO}	I _C =50μA	50			V
Collector-emitter breakdown	BV _{CEO}	I _C =1mA	50			V
Emitter-base breakdown voltage	BV _{EBO}	I _E =50μA	5			V
Collector cutoff current	I _{CBO}	V _{CB} =50V			0.5	μA
Emitter cutoff current	I _{EBO}	V _{EB} =4V			0.5	μA
Collector-emitter saturation	V _{CE(sat)}	I_{C}/I_{B} =5mA/0.25mA			0.3	V
DC current transfer ratio	h _{FE}	I _C =1mA, V _{CE} =5V	100	250	600	-
Input resistance	R ₁	-	7	10	13	kΩ
Transition frequency	f⊤	V _{CE} =10V, I _E = −5mA,		250		MHz

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	Ic	100	mA
Collector power dissipation	P _C	150	mW
Junction temperature	Tj	150	$^{\circ}\! \mathbb{C}$
Storage temperature	T _{stg}	-55 to +150	$^{\circ}$ C

Typical Characteristics

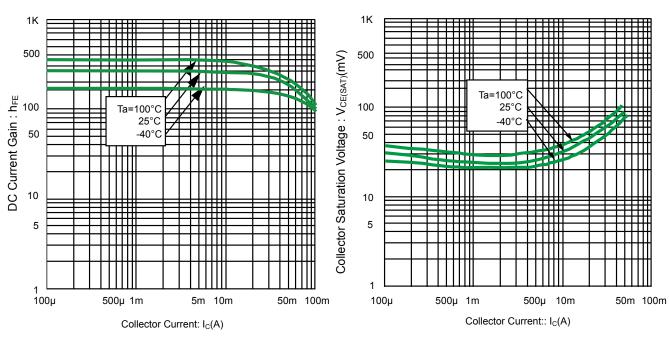
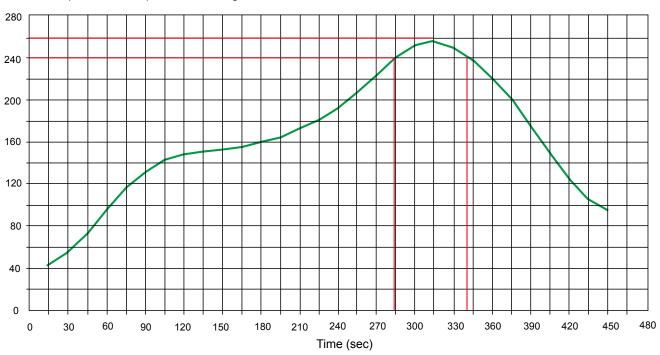


Fig 1.DC current gain vs. collector current @ V_{CE} =5V

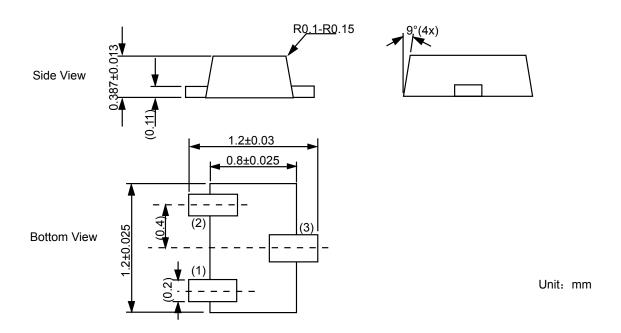
Fig 2.Collector-emitter saturation voltage vs. collector current $@I_C/I_B=10$

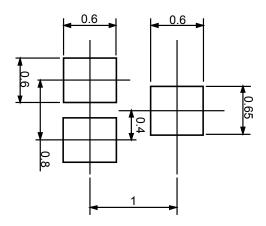
Solder Reflow Recommendation

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec



Product dimension (SOT-723)





Unit: mm

Ordering information

Device	Package	Shipping	
PDTC114TM	SOT-723 (Pb-Free)	8000 / Tape & Reel	

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NSVDTC143ZM3T5G SMUN5335DW1T2G SMUN5216DW1T1G NSVMUN5316DW1T1G NSVMUN5215DW1T1G

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