

PESDHC2FD4V5BH

Bi-directional 4.5V High Capacitance ESD Protector

Description

The PESDHC2FD4V5BH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L(Bottom View)

Feature

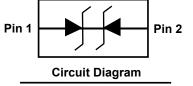
- 500W peak pulse power per line (t_P = 8/20µs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC61000-4-2(ESD) ±30KV(air), ±30KV(contact); IEC61000-4-4 (EFT) 40A (5/50ns)
 IEC61000-4-5 (Surge) 36A (8/20us)

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil



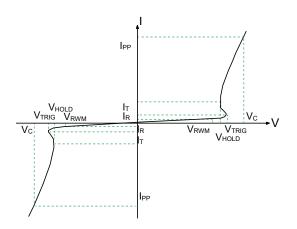


Marking (Top View)

PESDHC2FD4V5BH

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
VTRIG	Reverse trigger Current		
V _{HOLD}	Reverse holding voltage		
Iт	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
IF	Forward Current		
VF	Forward Voltage @ I⊧		



Electrical characteristics per line@25°C (unless otherwise specified)

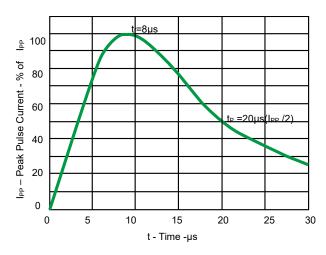
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				4.5	V
Reverse trigger voltage	V _{TRIG}	I _{TRIG} =2uA	4.7			V
Reverse holding voltage	V _{HOLD}	I _{HOLD} =50mA	4.6			
Reverse Leakage Current	I _R	V _{RWM} = 4.5V Т=25℃			1.0	μA
Maximum Reverse Peak Pulse Current	I _{PP}			36		А
Clamping Voltage	V _{CL}	I _{PP} =16A t _p =100ns		6		V
Clamping Voltage	Vc	I _{PP} =1A t _P = 8/20µs		4.8	5.8	V
Clamping Voltage	Vc	I _{PP} =20Α t _P = 8/20μs		10	13	V
Clamping Voltage	Vc	I _{PP} =36A t _P = 8/20µs		14	18	V
Junction Capacitance	Cj	V _R =0V f = 1MHz	70	80	90	pF

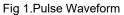
Absolute maximum rating@25°C

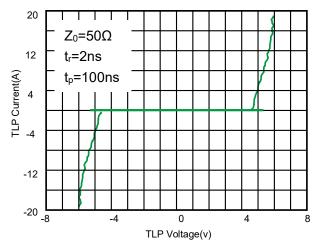
Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	500	W
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	Тѕтс	-55 to 150	°C

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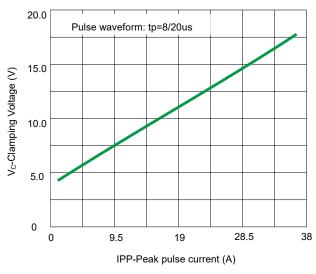
Typical Characteristics

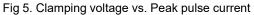


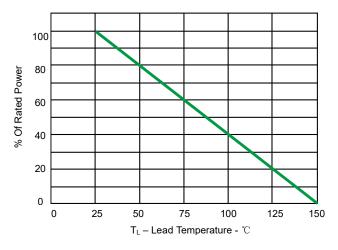


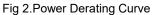












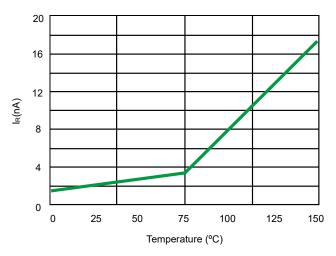
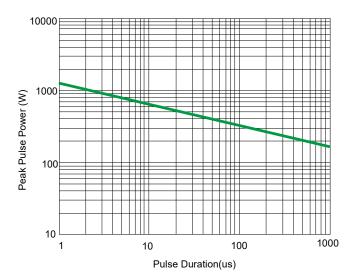


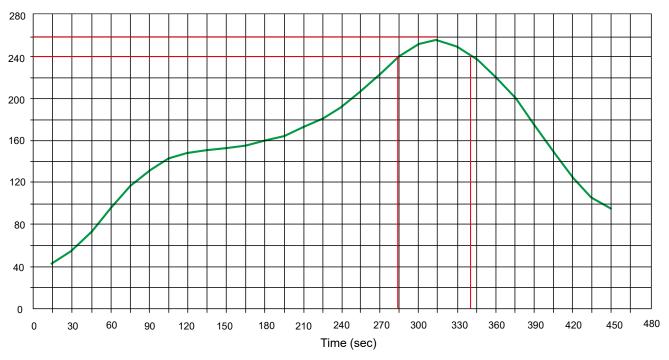
Fig 4.Typical Leakage Current vs. Temperature





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Solder Reflow Recommendation



Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

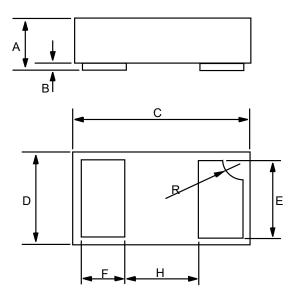
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

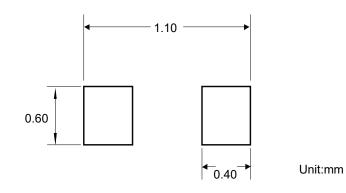
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

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Product dimension (DFN1006-2L)



Dim	Inches		Millimeters		
Dim	MIN	MAX	MIN	MAX	
А	0.013	0.020	0.34	0.50	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
н	0.015Typ.		0.40	Тур.	
R	0.001	0.005	0.05	0.15	



Suggested PCB Layout

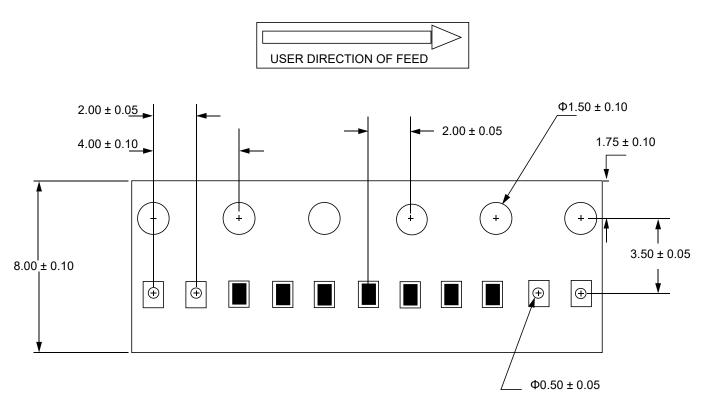
Ordering information

Device	Package	Reel	Shipping
PESDHC2FD4V5BH	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

PESDHC2FD4V5BH

ESD Protector

Load with information



Unit: mm

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