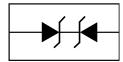


PESDWC2FD5VB

ESD Protector

Description

The PESDWC2FD5VB protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



Feature

- 80W peak pulse power per line (t_P = 8/20µs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- High ESD protection
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC61000-4-2(ESD) ±15KV(air), ±8KV(contact)

Mechanical Characteristics

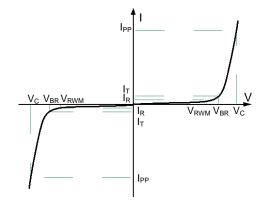
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter	
V _{RWM}	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V _{BR}	Breakdown Voltage @ I_T	
Ι _Τ	Test Current	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power Junction Capacitance Forward Current Forward Voltage @ I _F	
CJ		
I _F		
V _F		

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies



ESD Protector

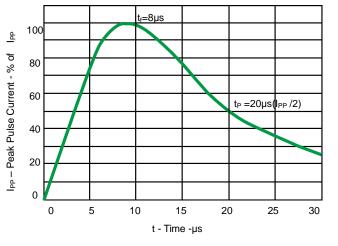
PESDWC2FD5VB

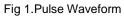
Electrical characteristics per line@25°C (unless otherwise specified) **Parameter Symbol Conditions** Min. Тур. Max. Units Peak Reverse Working Voltage V_{RWM} 5 V V Breakdown Voltage V_{BR} $I_t = 1mA$ 5.6 6.7 7.8 Reverse Leakage Current V_{RWM} = 5V T=25℃ 1.0 I_R μΑ **Clamping Voltage** V_{C} I_{PP}=1A 9 V I_{PP}=3A V **Clamping Voltage** V_{C} 12 V **Clamping Voltage** V_{C} IPP=5A 15 $V_R=0V$ f = 1MHz Junction Capacitance C_j 3 pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	80	W
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Typical Characteristics





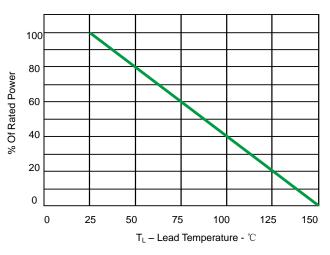


Fig 2. Power Derating Curve

ESD Protector

PESDWC2FD5VB

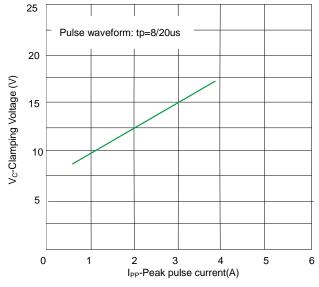
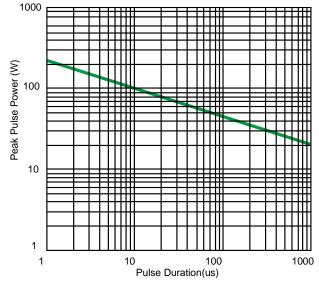
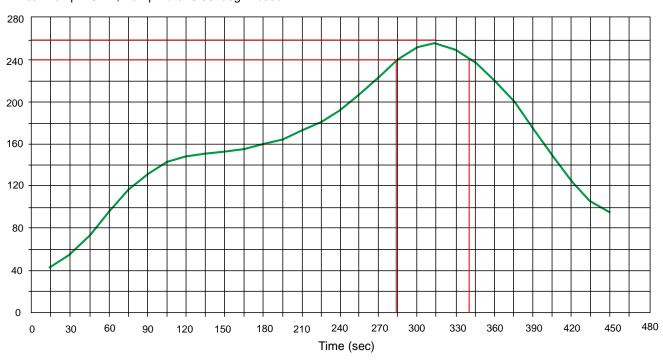


Fig 3. Clamping voltage vs. Peak pulse current





Solder Reflow Recommendation



Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

PCB Design

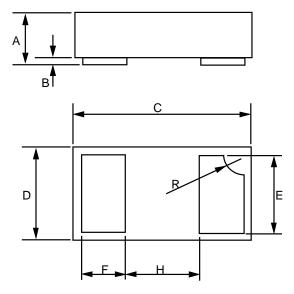
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- ≻ Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection. \triangleright
- Place via holes to ground as close as possible to the anode of the TVS diode. ≻
- Use as many via holes as possible for the ground connection. ⊳
- Keep the length of via holes in mind! The longer the more inductance they will have.

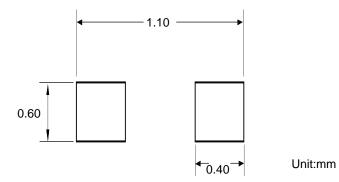
ESD Protector

PESDWC2FD5VB

Product dimension (DFN1006-2L)



Dim	Inches		Millimeters	
	MIN	МАХ	MIN	МАХ
А	0.013	0.020	0.34	0.50
В	0.000	0.002	0.00	0.05
С	0.037	0.042	0.95	1.075
D	0.021	0.026	0.55	0.675
E	0.017	0.021	0.45	0.55
F	0.007	0.011	0.20	0.30
н	0.015Typ.		0.40	Тур.
R	0.001	0.005	0.05	0.15



Ordering information

Device	Package	Shipping
PESDWC2FD5VB	DFN1006-2L (Pb-Free)	10000 / Tape & Reel

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