

#### PSBD2ED40V1H

# Schottky Barrier Diode

#### **Feature**

- Small mold type. (DFN1608-2L)
- Low I<sub>R</sub>
- > High reliability.



#### **Applications**

Low current rectification

### Construction

Silicon epitaxial planar

#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260 °C
- Device meets MSL 2 requirements
- ➤ Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

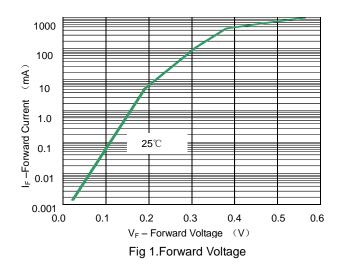
#### **Electrical characteristics per line@25℃**

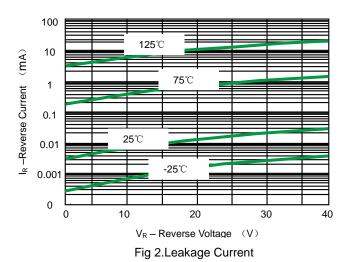
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V <sub>F</sub>	-	0.57	0.63	V	I <sub>F</sub> =1A
Forward voltage	V <sub>F</sub>	-	0.45	0. 50	V	I <sub>F</sub> =0.5A
Reverse current	I <sub>R</sub>	-	-	0.1	mA	V <sub>R</sub> =40V
Junction Capacitance	C <sub>j</sub>	-	90	-	pF	V <sub>R</sub> =0V f =1MHz

### Absolute maximum rating@25℃

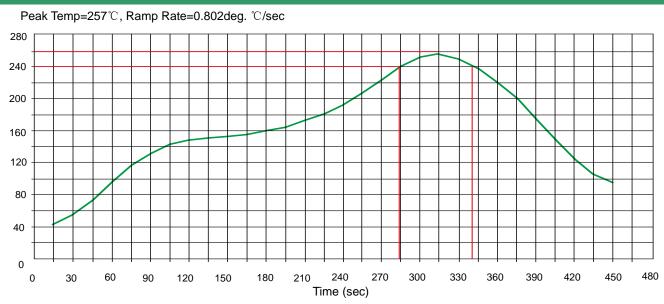
Parameter	Symbol	limits	Unit
Reverse voltage (DC)	$V_{RM}$	40	V
Average rectified forward current	I <sub>o</sub>	1	А
Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	7	А
Operating Junction temperature Range	Tj	-55 to +125	°C
Storage temperature	T <sub>stg</sub>	-40 to +125	°C

### Typical Characteristics

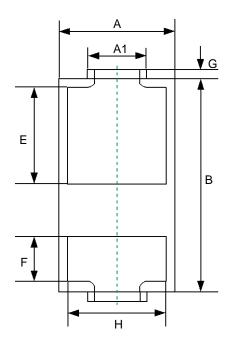


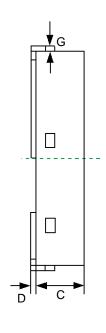


### **Solder Reflow Recommendation**

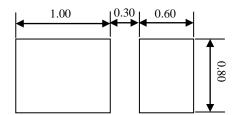


### Product dimension (DFN1608-2L)





Dim	Millimeters			
Dilli	MIN	MAX		
А	0.75	0.85		
A1	0.00	0.25		
В	1.55	1.65		
С	0.46	0.55		
D	0.00	0.04		
E	0.72	0.80		
F	0.32	0.40		
G	0.00	0.10		
Н	0.67	0.75		



Notes: This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

### **Ordering information**

Device Package		Shipping	
PSBD2ED40V1H	DFN1608-2L(Pb-free)	5000 / Tape & Reel	

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