

## PSBD2ED40V2H

## Schottky Barrier Diode

#### Feature

- Small mold type. (DFN1608-2L)
- Low IR
- High reliability.

#### **Applications**

Low current rectification

#### **Mechanical Characteristics**

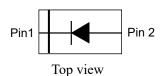
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- > Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

## Electrical characteristics per line@25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	-	0.53	0.56	V	I⊧=1A
Forward voltage	VF	-	0.60	0.65	V	IF=1.5A
Reverse current	IR	-	-	50	uA	V <sub>R</sub> =40V
Junction Capacitance	Cj	-	150	-	pF	V <sub>R</sub> =0V,f=1MHz
Reverse Recovery Time	trr		25		ns	IF=0.5A, IR=1.0A, Irr=0.25A

#### Absolute maximum rating@25℃

Parameter	Symbol	limits	Unit
Reverse voltage (DC)	V <sub>RM</sub>	40	V
Average rectified forward current	lo	2	A
Typical Thermal Resistance Junction to Ambient	R <sub>thja</sub>	80	°C/W
Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	Ігѕм	12	A
Repetitive Peak Forward Current (Pulse Wave=10ms, Duty Cycle = 25%)	IFRM	5	A
Operating and Storage Temperature Range	Tj ,Tstg	-55 to 125	°C



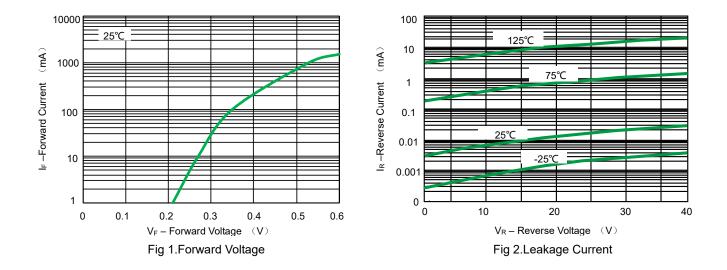
### Construction

Silicon epitaxial planar

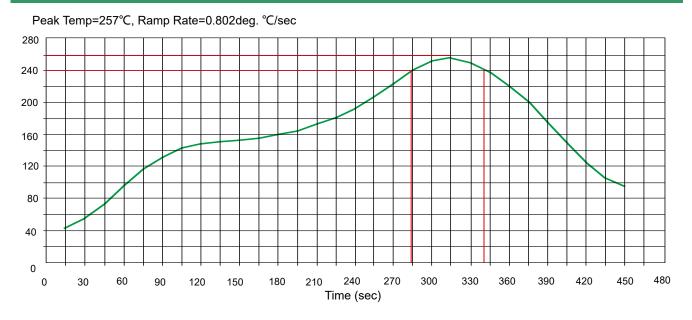
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### **Typical Characteristics**



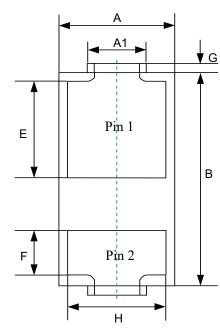
### **Solder Reflow Recommendation**

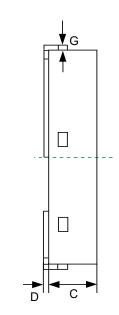


# Schottky Barrier Diode

## PSBD2ED40V2H

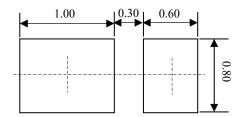
## Product dimension (DFN1608-2L)





Dim	Millimeters			
	MIN	MAX		
А	0.75	0.85		
A1	0.00	0.25		
В	1.55	1.65		
С	0.46	0.55		
D	0.00	0.04		
E	0.72	0.80		
F	0.32	0.40		
G	0.00	0.10		
н	0.67	0.75		

Bottom view



Notes: This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

Ordering information					
Device	Package	Shipping			
PSBD2ED40V2H	DFN1608-2L(Pb-free)	5000 / Tape & Reel			

#### Marking

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