

Feature

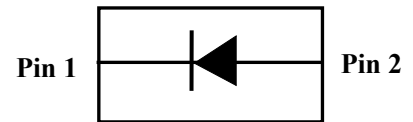
- Ultra Small mold type. DFN1006-2L
- Low I_R
- High reliability.



DFN1006-2L(Bottom View)

Applications

- Low current rectification



Circuit Diagram

Construction

- Silicon epitaxial planar



Marking (Top View)

Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- DFN1006-2L without plating

Electrical characteristics per line@25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_F	-	0.31	0.34	V	$I_F=10\text{mA}$
Forward voltage	V_F	-	0.42	0.48	V	$I_F=100\text{mA}$
Forward voltage	V_F	-	0.50	0.58	V	$I_F=200\text{mA}$
Reverse current	I_R	-	-	0.50	μA	$V_R=10\text{V}$
Reverse current	I_R	-	-	2.00	μA	$V_R=40\text{V}$

Absolute maximum rating@25°C

Parameter	Symbol	limits	Unit
Reverse voltage (DC)	V_{RM}	40	V
Average rectified forward current	I_o	200	mA
Forward current surge peak (60Hz 1cyc)	I_{FSM}	800	mA
Operating Junction temperature Range	T_j	-55 to 125	°C
Storage temperature	T_{stg}	-55 to 125	°C

Typical Characteristics

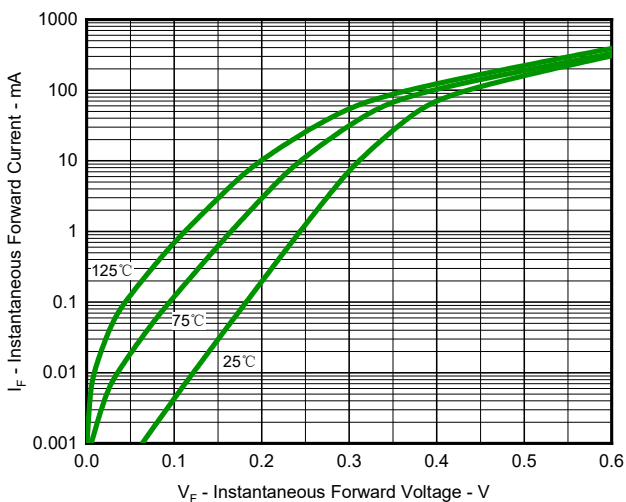


Fig.1 Typical Forward Characteristics

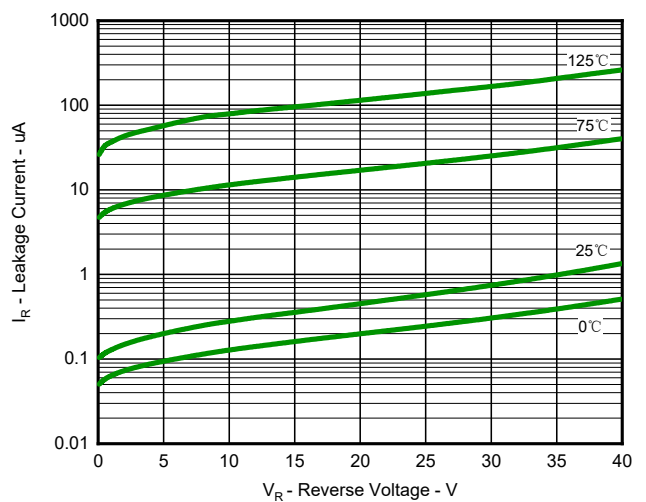


Fig 2.Leakage Current

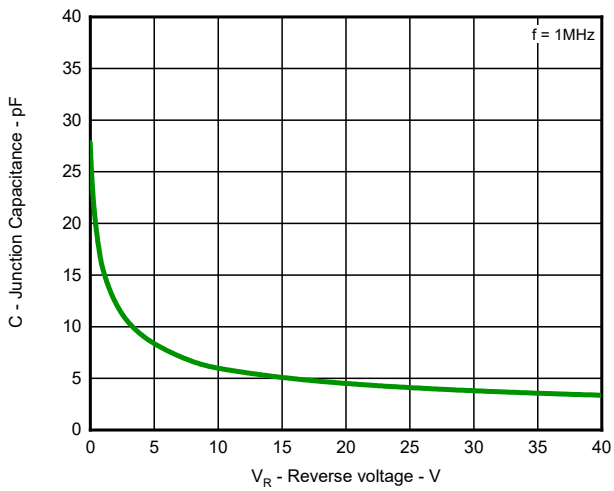
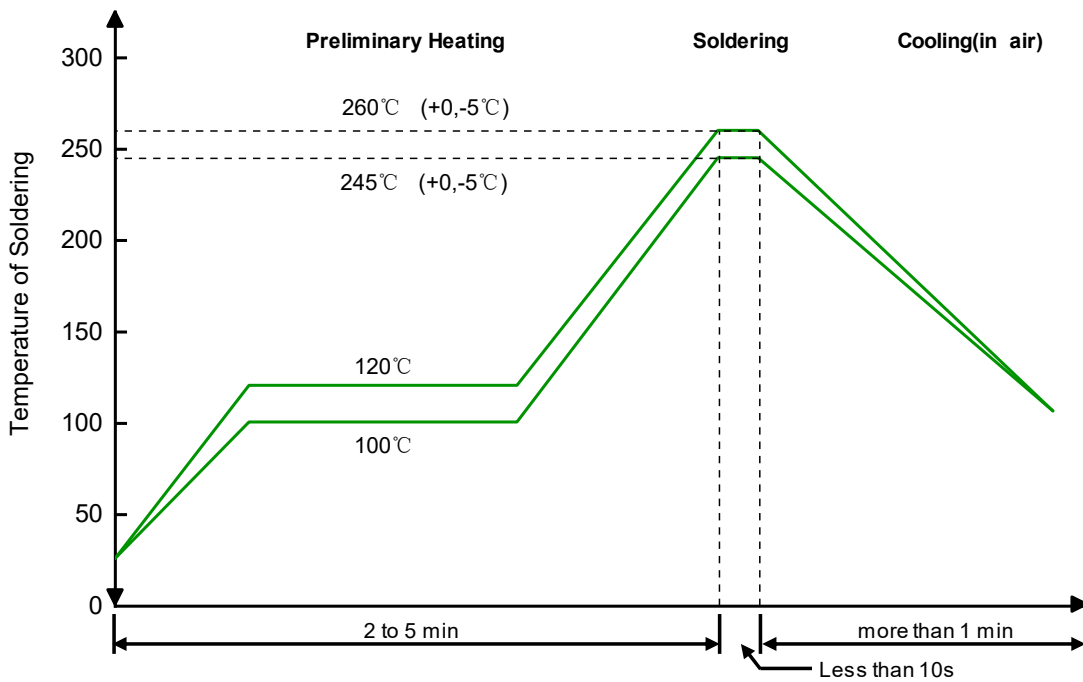


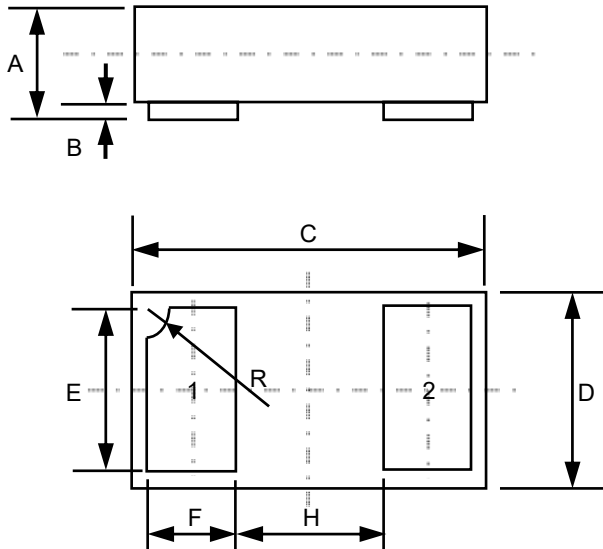
Fig 3.Capacitance vs. Revers voltage

Solder Reflow Recommendation

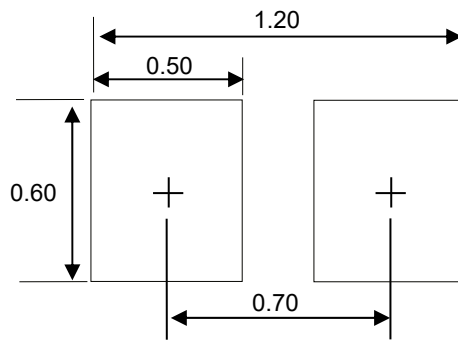


Remark: Pb free for 260°C; Pb for 245°C.

Product dimension (DFN1006-2L)



Dim	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.013	0.020	0.34	0.498
B	0.000	0.002	0.00	0.05
C	0.037	0.043	0.95	1.080
D	0.022	0.027	0.55	0.680
E	0.016	0.024	0.40	0.60
F	0.008	0.012	0.20	0.30
H	0.015Typ.		0.40Typ.	
R	0.001	0.005	0.05	0.15




Unit:mm

Suggested PCB Layout

Ordering information

Device	Package	Reel	Shipping
PSBD2FD40V02	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel


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