

Bi-directional 24V High Capacitance TVS Protector

Description

The PTVSHC1DF24VBH transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC1DF24VBH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC1DF24VBH is available in a SOD-123FL package with working voltages of 24 volt.



Feature

- \gt 5700W Peak pulse power per line (t_P = 8/20µs)
- SOD-123FL package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 80A (5/50ns)

Pin 1 Pin 2 Circuit Diagram

Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

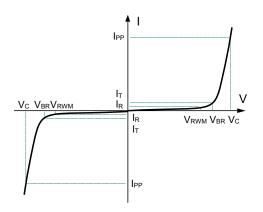


Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260 °C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V _{BR}	Breakdown Voltage @ I⊤		
I _T	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ I _{PP}		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		



Electrical characteristics per line@25℃(unless otherwise specified)

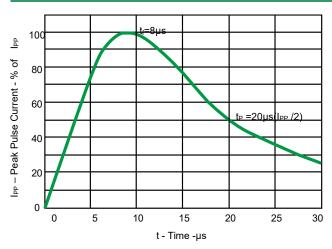
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				24	V
Breakdown Voltage	V _{BR}	I _t =1mA	25			V
Reverse Leakage Current	I _R	V _{RWM} =24V			1	μA
Clamping Voltage	Vc	I_{PP} =90A t_P = 8/20 μ s		27	29	V
Clamping Voltage	Vc	I _{PP} =165A t _P = 8/20μs		29	31	V
Junction Capacitance	C _j	V _R =0V f = 1MHz		280	350	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _P = 8/20µS)	P _{pp}	5700	W
Lead Soldering Temperature	T∟	260 (10 sec)	°C
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

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Typical Characteristics





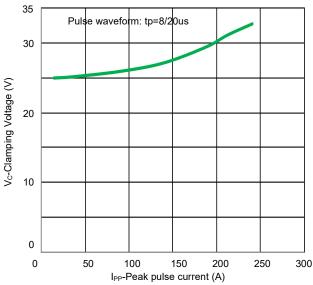


Fig 3. Clamping voltage vs. Peak pulse current

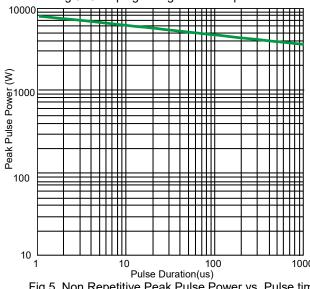


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

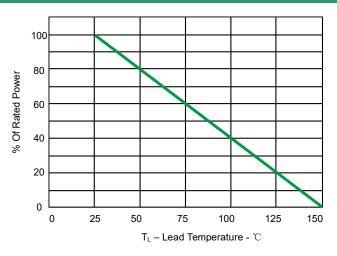


Fig 2.Power Derating Curve

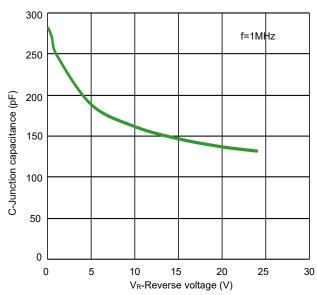
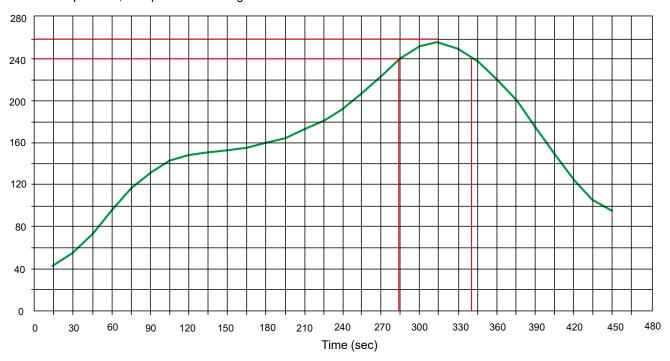


Fig 4. Capacitance vs. Reveres voltage

Solder Reflow Recommendation

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

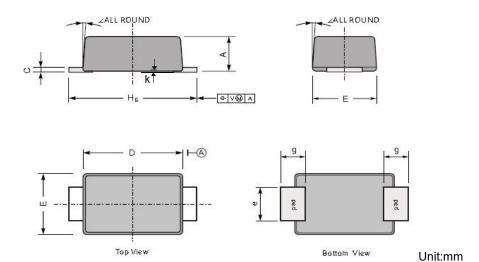


PCB Design

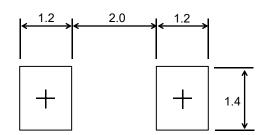
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (SOD-123FL)



Millimeters Inches Dim MIN MAX MIN MAX 0.80 Α 0.031 0.047 1.20 С 0.002 0.010 0.05 0.25 0.138 0.154 3.50 3.90 H_{E} 0.077 1.55 0.061 1.95 2.50 D 0.098 0.114 2.90 g 0.020 0.043 0.50 1.10 0.024 0.039 0.60 1.00 е 0.004 0.10 _ 7°



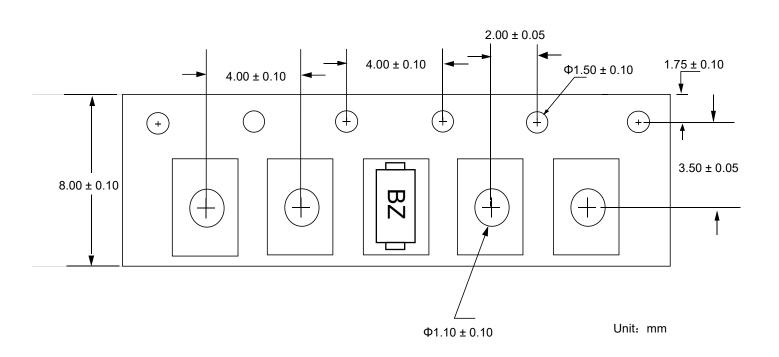
Suggested PCB Layout Unit:mm

Ordering information

Device	Package	Reel	Shipping
PTVSHC1DF24VBH	SOD-123FL (Pb-Free)	7"	3000 / Tape & Reel

Load with information





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