

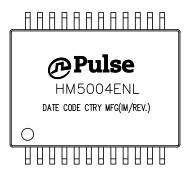


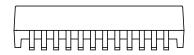


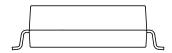
NOTES:

- ROHS COMPLIANT
- 2. STORAGE TEMPERATURE: -50°C TO +125°C
- 3. COMPLIANCE TO J-STD:
 - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
 - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
 - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
- 4. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX

 TO THE PART NUMBER(i.e HXXXXXXNL BECOMES HXXXXXXNLT).







FINAL OUTLINE

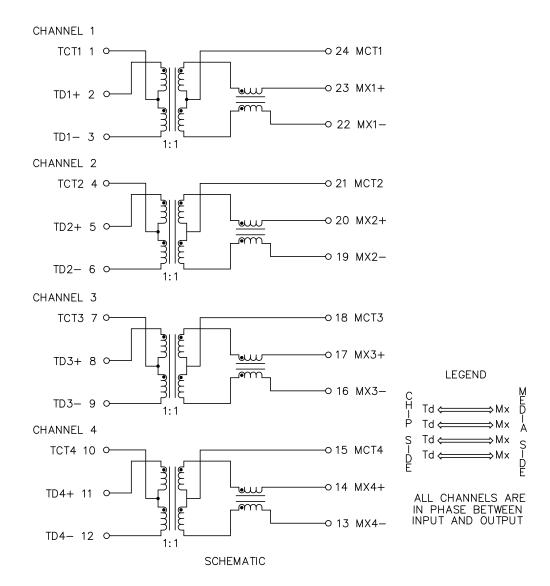
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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,1GD,1:1,SM,TU	HM5004ENL-P2	PS-2228.001-C	1 OF 3	HM5004ENL	В









ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS				
QUALIFICATION	AEC-Q200 (PENDING)				
OPERATING TEMP	-40°C TO +105°C				
TURNS RATIO	1 : 1 ±2%				
POLARITY	PER SCHEMATIC				
✓ INSERTION LOSS	100 KHz	1-125 MHz			
V INSERTION LOSS	-1.2 dB MAX	-0.2-0.002*f ^{^1.4} dB MAX			
RETURN LOSS	.1-40 MHz	40-100 MHz			
$(Z OUT = 100 OHM \pm 15\%)$	-16 dB MIN	-10+20*LOG ₁₀ (f/80 MHz) dB MIN			
V INDUCTANCE (OCL) (MEDIA SIDE, -40°C~+105°C)	350 uH MIN	(MEASURED AT 100 KHz, 100 mVRMS) AND WITH 8 MA DC BIAS			
	1 MHz	10-100 MHz			
CROSSTALK, ADJACENT CHANNELS	-50 dB MIN	-55+22*LOG ₁₀ (f/10) dB MIN			
COMMON MODE	2 MHz	30-200 MHz			
REJECTION RATIO	-50 dB MIN	-15+20*LOG ₁₀ (f/200) dB MIN			
DC RESISTANCE, 1/2 WINDING	.65 OHMS MAX				
DC RESISTANCE IMBALANCE	±.065 OHMS MAX (CENTER TAP SYMMETRY)				
V INPUT − OUTPUT ISOLATION	1500	VRMS MIN @ 60 SECONDS			

NOTE: 1. f IS FREQUENCY IN MHz.

2. "\textsize" IS SPECIAL CHARACTERISTIC.

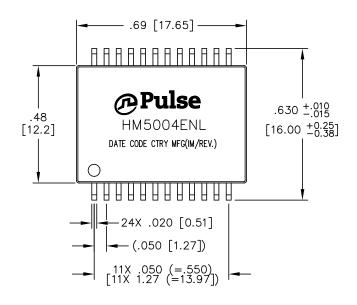
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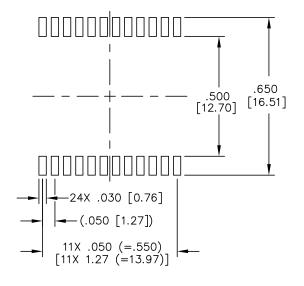
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MDL,SIN,1GD,1:1,SM,TU	HM5004ENL-P2	PS-2228.001-C	2 OF 3	HM5004ENL	В



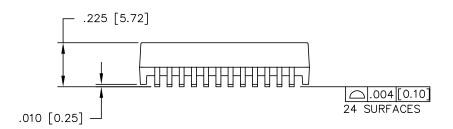


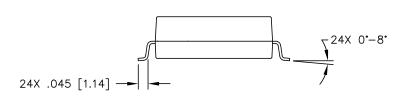






SUGGESTED LAND PATTERN





DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY. .XX= $\pm .02$ [± 0.51] .XXX= $\pm .010$ [± 0.25]

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MDL,SIN,1GD,1:1,SM,TU	HM5004ENL-P2	PS-2228.001-C	3 OF 3	HM5004ENL	В

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