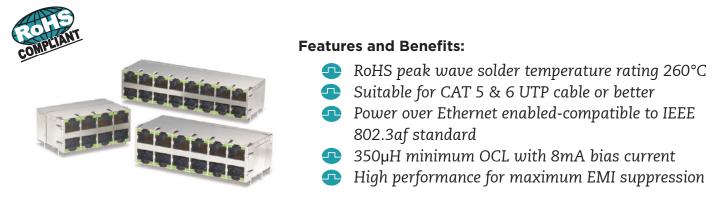
U.S. Patent#7241181

Description:



10/100/1000Base-T RJ45 2xN PoE Integrated Connectors with Magnetics, with the addition of power feeding over signal pairs.



Electrical Specifications @ 25°C – Operating Temperature 0°C to +70°C											
RoHS Compliant Part Number (Number of Ports)	LEDs (L/R)	Insertion TX/RX (dB TYP)	Return Loss (dB TYP) 100Ω (±15Ω)			Crosstalk (dB TYP)			Common Mode Rejection (dB TYP)		Hipot (Vdc MIN)
		1-125 MHz	1-40 MHz	60 MHz	100 MHz	1-10 MHz	30-60 MHz	60-100 MHz	1-10 MHz	10-150 MHz	1 MIN
JOB-0364NL (2X4)	G-Y/G-Y	-0.8	-20	-17	-14	-40	-35	-30	-50	-40	2250
JOB-0366NL (2X6)	G-Y/G-Y	-0.8	-20	-17	-14	-40	-35	-30	-50	-40	2250
JOB-0368NL (2X8)	G-Y/G-Y	-0.8	-20	-17	-14	-40	-35	-30	-50	-40	2250

Notes: 1. Bi-color LEDs: G-Y = green/yellow. Contact Pulse for LED options

RJ45 Durability Test Rating							
Part Number	Mating Force (MAX)	Unmating Force (MAX)	Durability				
JOB Series	5 lbs./2.268 kgs.	5 lbs./2.268 kgs.	750 Insertions				

RJ45 Material Specification										
Part		Shield		Contact	Housing		MSL Rating			
Number	Material Finish		Material Plating Area		Solder Area	Material		Specification		
		20-40 μ inches thick nickel	5 lbs./2.268	50 μ inches gold over	75-300µ inches tin-lead	Thermoplastic	UL 94 V-0			
JOB Series	Brass	over brass	kgs.	$30-50\mu$ inches nickel	over 50-100 μ inches nickel			1		

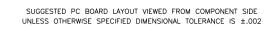
Notes:

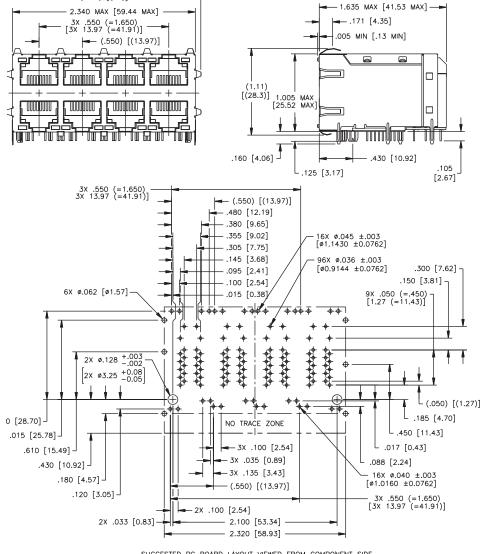
1. Connector dimensions comply with FCC dimension requirements.

J429.D (03/19)

Networking.PulseElectronics.com

2





JOB-0364NL

PulseJack® 2xN Gigabit PoE U.S. Patent#7241181

HΗ

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Electronics J0B-0364NL DATE CODE CTRY OF MFG

(2.47) [(63)] -

US PATENT 7241181 & 6962511 **Pulse**

2X4 Mechanicals

1.030 ±.005 [26.20 ±0.13]

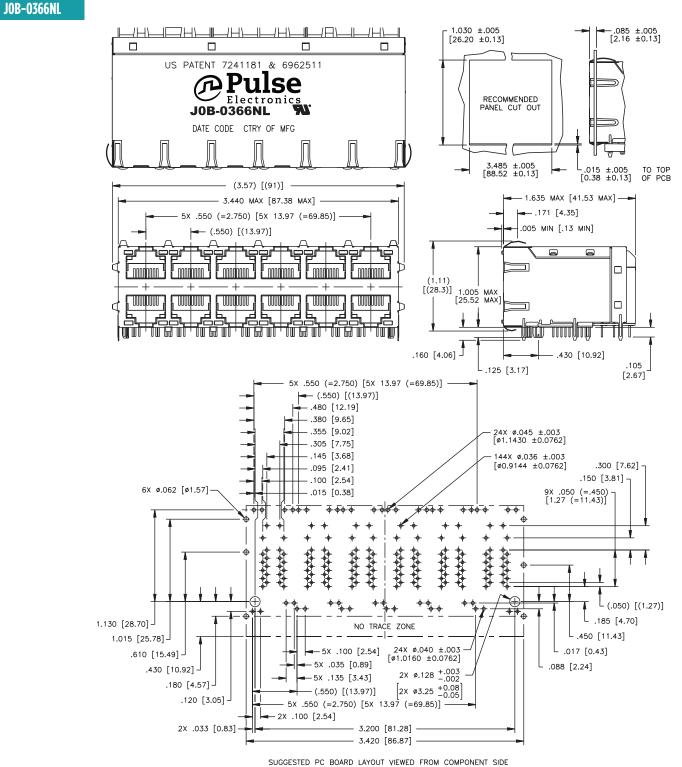
RECOMMENDED PANEL CUT OUT

7 2.385 ±.005 [60.58 ±0.13] -.085 ±.005 [2.16 ±0.13]

015 ±.005 TO TOP [0.38 ±0.13] OF PCB

U.S. Patent#7241181

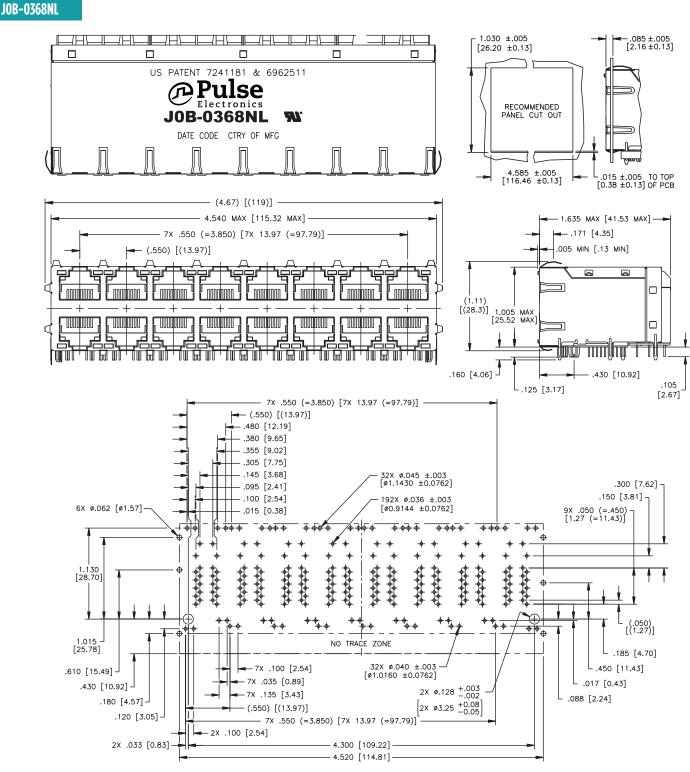
2X6 Mechanicals



SUGGESTED PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE UNLESS OTHERWISE SPECIFIED DIMENSIONAL TOLERANCE IS ±.002

U.S. Patent#7241181

2X8 Mechanicals



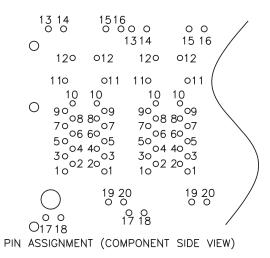
SUGGESTED PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE UNLESS OTHERWISE SPECIFIED DIMENSIONAL TOLERANCE IS $\pm.002$

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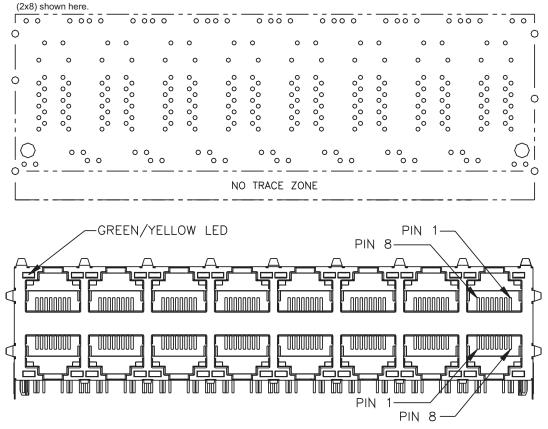
U.S. Patent#7241181

Pin-Out Assignment

JOB-0364NL/66NL/68NL

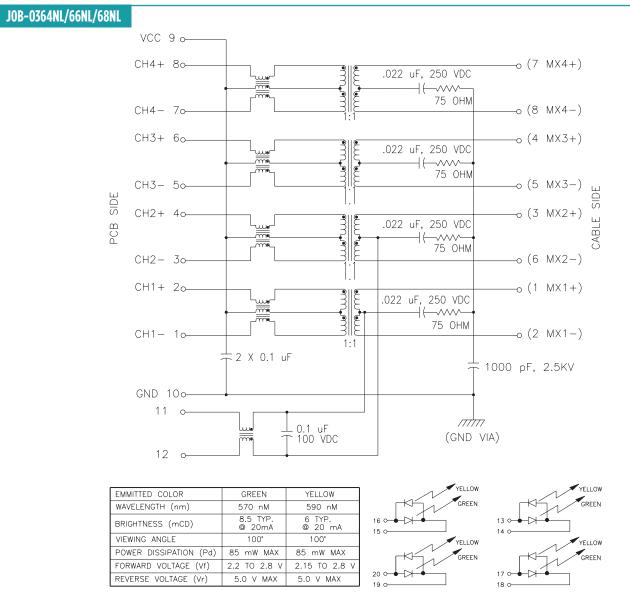


NOTE: All parts use the same port indentification system. J0B-0368NL



U.S. Patent#7241181

Schematic



For More Information:

Americas - prodinfonetworkamericas@pulseelectronics.com | Europe - comms-Apps-Europe@pulseelectronics.com | Asia - prodinfonetworkapac@pulseelectronics.com

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 GDCX-PN-64

 PN-66
 GDCX-PN-66-50
 GDLX-A-66
 GDLX-A-88
 GDLX-N-66
 GDLX-S-88K
 GDTX-S-88-50
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 GLX-S-88M-BLK
 GMX-N-1010
 GMX-S-1010
 GMX-S-66
 GMX-SMT2-N-1010
 GMX-SMT2-N-64-50
 GMX-SMT2-N-88

 GMX-SMT2-S6-88
 GMX-SMT4-N-88
 GPX-2-64
 GRT1-BT1-5
 GSGX-N-2-88
 GSX-NS2-88-3.05
 GSX-NS2-88-3.05-50
 GSX-NS-88-3.05-50
 GSX-NS-88-3.05-50
 GSX-NS-88-3.05

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 GSX-NS-88-3.05-50
 GSX-NS-88-3.68
 1-1775629-2
 GWLX-S-88-GR
 GWLX-S9-88-YG
 1300500326
 1300500227
 1300530003

 1300570002
 1324640-4
 RJ11FTVC2G
 RJ11FTVC2N
 RJFTVX2SA1G
 132764-001
 1400000
 1413235