

LAN Product Guide - Servers

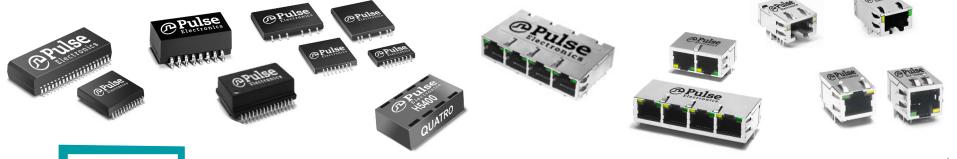
• Ideal for single and multi-port server applications

Available for PoE, PoE+ and PCI - Express cards.

Custom design options available (LED, pinout, extended temperature, etc...)

 \mathbf{T}^{X} = extended temp, P = PoE, PP = PoE+, OS = Offset

MAGNETICS			CONNECTORS WITH INTEGRATED MAGNETICS		
10/100	Gigabit	10GBASE-T	10/100	Gigabit	10GBASE-T
SINGLE:	SINGLE:	SINGLE:	1X1:	1X1:	1X1: 1X1:
H1263NL H2019NLP HX1188NL ^X HX2326NL ^{X, PP}	H5062NL H5610NL H6062NL ^P HX5004EN ^X HX5062NL ^X HX5610NL ^X	(4 channel) (5 channel) H7028FNL H7137FNLP H7029FNLP	JD1-0003NL JD1-0004NL JKM-0009NL JKM-0010NL JKO-0125NLP JKO-0144NLP	J1L-0103NL ^{OS} JD1-0001NL JD1-0002NL JKM-0003NL JKM-0004NL JKO-0161NLP JKO-0177NLP JXKO-0190NL	(4 channel) (5 channel) JT4-1120HL JTH-0024NL JT3-1125HL JT4-1108HL JT4-1109HL JT4-1160HL
DUAL: H1270FNL H2006ANL ^P HX1294NL ^X	DUAL: H5014NL HX5014NL ^X H5200NL ^P HX5200NL ^X H5201NL HX5201NL ^X	For Dual, please contact Pulse	1X2: JX80-0019NL ^X	1X2: JG0-0129NL JGL-0001NL ^{OS} JGL-0004NL ^{OS} JXG0-0129NL ^X	1X2: Available for sampling, please contact Pulse
QUAD: H1164NL HX1234NLX H1259NLP HX1259NLP,X H5200NLP HX5201NLX H5201NL HX5200NLX	QUAD: H5200NLP HX5400NL ^{P,X} H5400NLP HX5401NL ^X H6400NL ^{PP}	For Quad, please contact Pulse	1X4: J1N-0013NL J80-0004NL JX80-0033NL ^X	1X4: J1N-0002NL JG0-0098NL JXG0-0098NL ^X	1X4: (4 channel) JT6-1473NL JT6-1480NLPP



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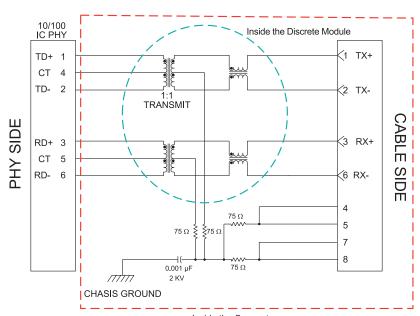
Application Notes

- 📭 Layering ground planes is advisable. Route connector/discrete module ground pins to chassis/analog ground if possible.
- Keep signal traces from PHY to connector/discrete module as short as possible. If traces exceed 3-4 inches, pay close attention to line impedance imbalance.

- Using BST (75 Ω resistors and high voltage cap to chassis ground) to terminate Cable side CTs is advisable for best EMI performance (included in most connector solutions).
- Follow PHY manufacturer's application notes for further layout

TYPICAL 10GBASE-T /GIGABIT CIRCUIT

10/100 APPLICATION CIRCUIT



Inside the Connector

Pulse Worldwide Headquarters 12220 World Trade Drive San Diego, CA 92128

For More Information

U.S.A.

Tel: 858 674 8100 Fax: 858 674 8262

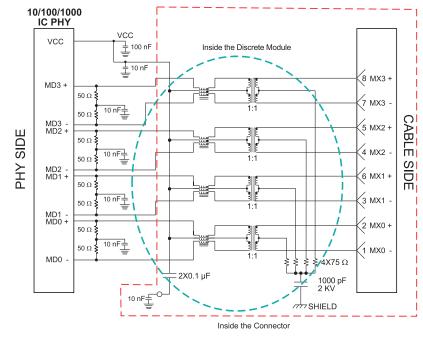
Pulse Europe

Zeppelinstrasse 15 71083 Herrenberg Germany

Tel: 49 7032 78060 Fax: 49 7032 7806 135

Pulse China Headquarters

B402, Shenzhen Academy of Aerospace Technology Bldg. 10th Keiinan Road High-Tech Zone Nanshan District Shenzen, PR China 518057 Tel: 86 755 33966678 Fax: 86 755 33966700



Pulse North China

Room 2704/2705 Super Ocean Finance Ctr. 2067 Yan An Road West Shanghai 200336 (hina

Tel: 86 21 62787060 Fax: 86 2162786973

Pulse South Asia

135 Ioo Seng Road #03-02 PM Industrial Bldg. Singapore 368363

Tel: 65 6287 8998 Fax: 65 6287 8998

Pulse North Asia

3F. No. 198 Zhongyuan Road Zhonali City Taoyuan County 320 Taiwan R. O. C. Tel: 886 3 4356768 Fax: 886 3 4356823 (Pulse)

Fax: 886 3 4356820 (FRE)

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