

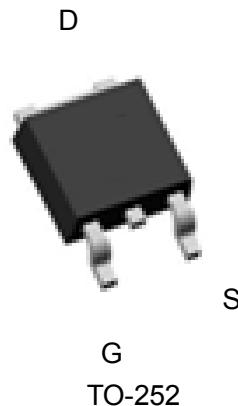
Features

- ◆ Low On-Resistance
- ◆ Fast Switching
- ◆ 100% Avalanche Tested
- ◆ Repetitive Avalanche Allowed up to Tjmax
- ◆ Lead-Free, RoHS Compliant

V_{DS}	30	V
$R_{DS(on),Typ}$	4.5	mΩ
I_D	80	A

Description

PTD3006 designed by the trench process techniques to achieve extremely low on-resistance. Additional features of this design can operate at high junction temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Motor applications and a wide variety of other applications.

**Absolute Maximum Ratings**

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (Ta) is 25°C, unless otherwise specified.

Symbol	Parameter	Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	±20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	30	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
I_S	Diode Continuous Forward Current	$T_c = 25^\circ\text{C}$	A
Mounted on Large Heat Sink			
I_{DM}	Pulse Drain Current Tested (Silicon Limit)	$T_c = 25^\circ\text{C}$	A
I_D	Continuous Drain current@ $V_{GS}=10\text{V}$ (See Fig2)	$T_c = 25^\circ\text{C}$	A
P_D	Maximum Power Dissipation	$T_c = 25^\circ\text{C}$	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.98	°C/W
Drain-Source Avalanche Ratings			
EAS	Avalanche Energy, Single Pulsed ②	225	mJ

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_D=250\mu\text{A}$	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^①	$V_{\text{GS}}=10\text{V}$, $I_D=40\text{A}$	--	4.5	6.0	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^①	$V_{\text{GS}}=4.5\text{V}$, $I_D=20\text{A}$	--	5.5	7.5	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	1350	--	pF
C_{oss}	Output Capacitance		--	190	--	pF
C_{rss}	Reverse Transfer Capacitance		--	115	--	pF
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$	--	38	--	nC
		$V_{\text{GS}}=4.5\text{V}$	--	15	--	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=15\text{V}$, $I_D=18\text{A}$, $V_{\text{GS}}=10\text{V}$	--	8	--	nC
Q_{gd}	Gate-Drain Charge		--	7	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=15\text{V}$, $I_D=10\text{A}$, $R_G=4.7\Omega$, $V_{\text{GS}}=10\text{V}$	--	13	--	nS
t_r	Turn-on Rise Time		--	12	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	19	--	nS
t_f	Turn-Off Fall Time		--	12	--	nS
Source- Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
I_{SD}	Source-drain current(Body Diode)	$T_c=25^\circ\text{C}$	--	--	80	A
V_{SD}	Forward on voltage	$I_S=40\text{A}$, $V_{\text{GS}}=0\text{V}$	--	--	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}$, $I_{\text{sd}}=40\text{A}$, $V_{\text{GS}}=0\text{V}$ $di/dt=100\text{A}/\mu\text{s}$	--	22	--	nS
Q_{rr}	Reverse Recovery Charge		--	11	--	nC

Note:

① Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.② Limited by $T_{j\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 30\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value

③ Repetitive rating; pulse width limited by max. junction temperature.

Typical Characteristics

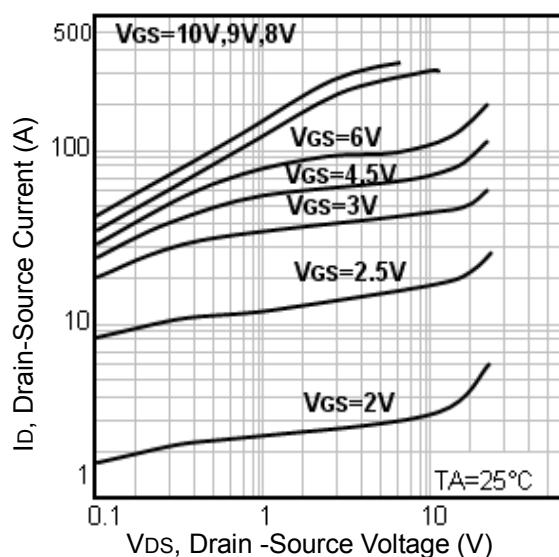


Fig1. Typical Output Characteristics

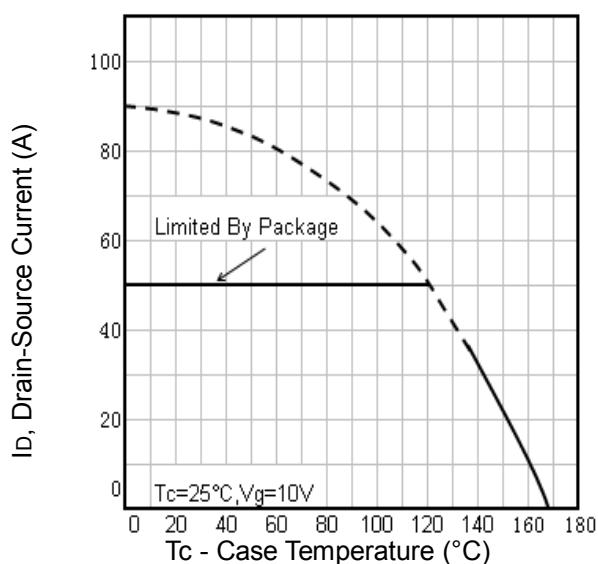


Fig2. Maximum Drain Current Vs. Case Temperature

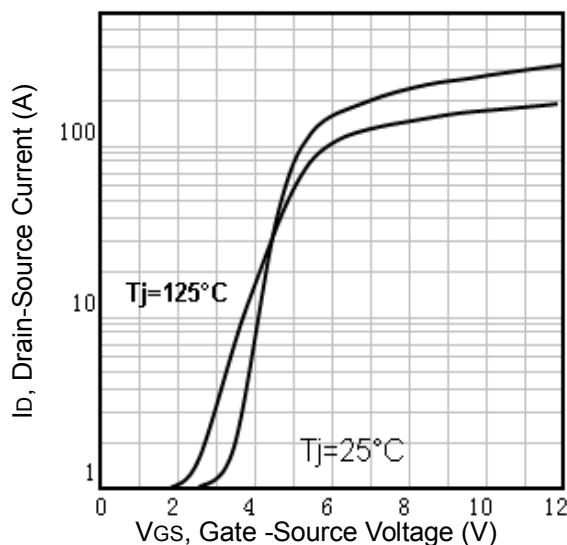


Fig3. Typical Transfer Characteristics

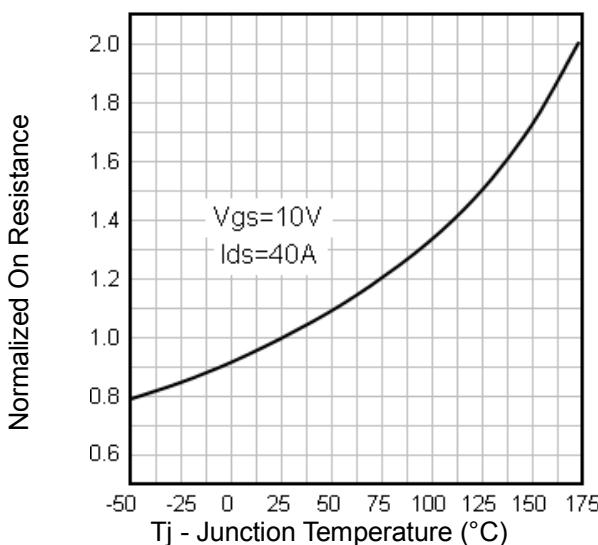


Fig4. Normalized On-Resistance Vs. Temperature

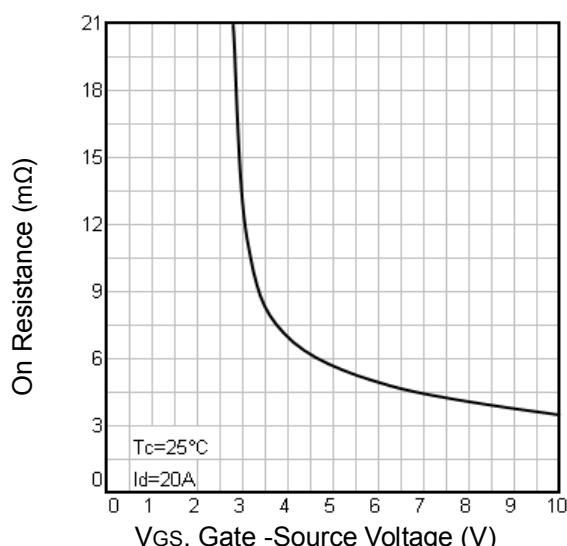


Fig5. On Resistance Vs. Gate-Source Voltage

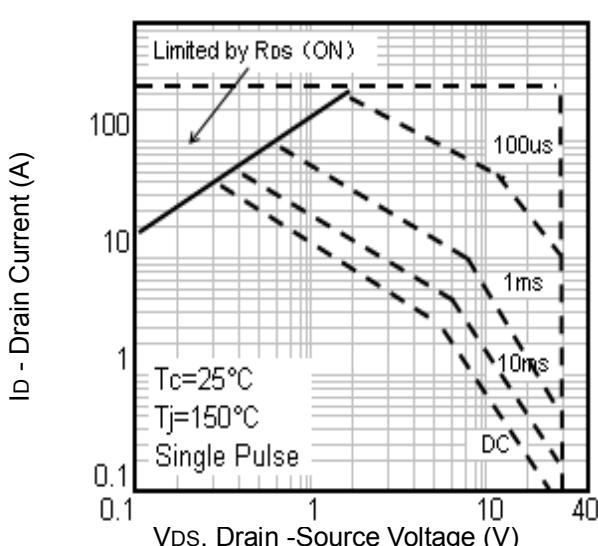


Fig6. Maximum Safe Operating Area

Typical Characteristics

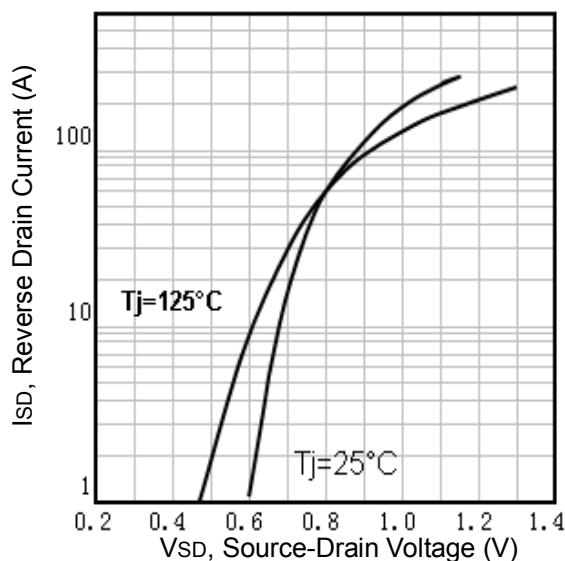


Fig7. Typical Source-Drain Diode Forward Voltage

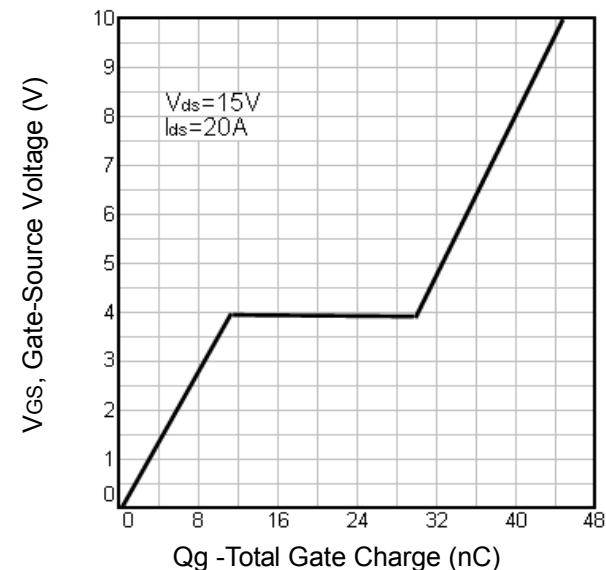


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

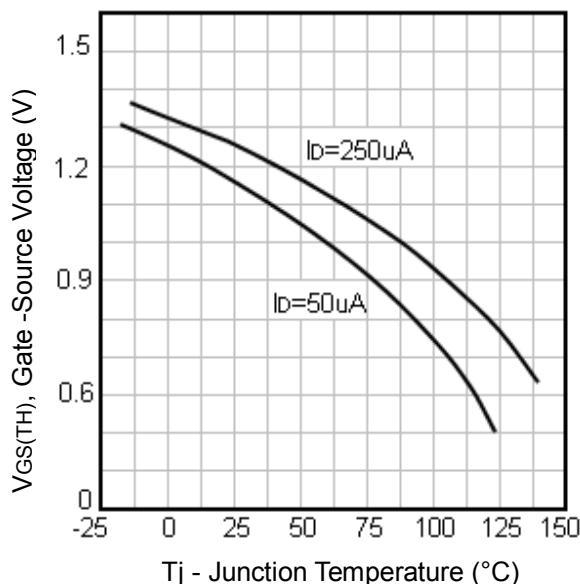


Fig9. Threshold Voltage Vs. Temperature

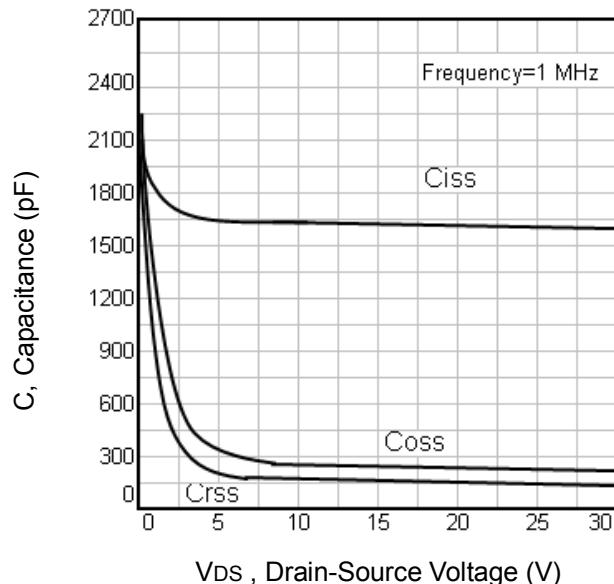


Fig10. Typical Capacitance Vs. Drain-Source Voltage

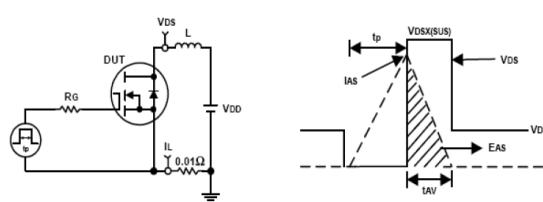


Fig11. Unclamped Inductive Test Circuit and waveforms

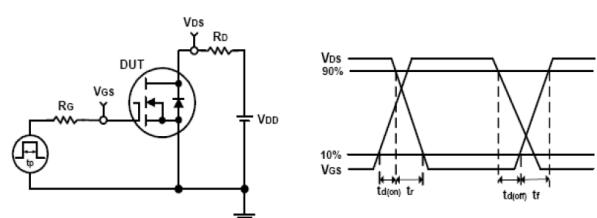


Fig12. Switching Time Test Circuit and waveforms

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