

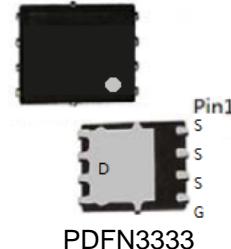
**Features**

- 1.8V Logic Level Control
- PDFN3333 SMD Package

**Applications**

- High Side Load Switch
- Battery Switch
- Optimized for Power Management Applications for Portable Products, such as Aeromodelling, Power bank, Brushless motor, Main board , and Others

BVDSS	-20	V
ID	-45	A
R <sub>DSON</sub> @V <sub>GS</sub> =-4.5V	9	mΩ
R <sub>DSON</sub> @V <sub>GS</sub> =-2.5V	12	mΩ
R <sub>DSON</sub> @V <sub>GS</sub> =-1.8V	16	mΩ

**Absolute Maximum Ratings**

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter	Rating	Unit
<b>Common Ratings (T<sub>c</sub>=25°C Unless Otherwise Noted)</b>			
V <sub>GS</sub>	Gate-Source Voltage	±12	V
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	-20	V
T <sub>J</sub>	Maximum Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
<b>Mounted on Large Heat Sink</b> <b>note B</b>			
I <sub>DM</sub>	Pulse Drain Current Tested (Silicon Limit)	T <sub>c</sub> =25°C -180	A
I <sub>D</sub>	Continuous Drain current @V <sub>GS</sub> =10V	T <sub>c</sub> =25°C -45	A
P <sub>D</sub>	Maximum Power Dissipation	T <sub>A</sub> =25°C 3.5	W
		T <sub>c</sub> =25°C 35	W
R <sub>θJA</sub>	Thermal Resistance Junction-to-Ambient – Steady State (Note 1)	36	°C/W

Note :

1. Surface-mounted on FR4 board using 1 in sq. pad size (Cu area = 1.127 in sq. [2 oz] including traces).

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_J = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ $ID=-250\mu\text{A}$	-20	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain current( $T_c=25^\circ\text{C}$ )	$V_{DS}=-20\text{V}, V_{GS}=0\text{V}$	--	--	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{GS}=\pm 12\text{V}, V_{DS}=0\text{V}$	--	--	$\pm 100$	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, ID=-250\mu\text{A}$	-0.4		-1.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance note A	$V_{GS}=-4.5\text{V}, ID=-20\text{A}$		7.5	9	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance note A	$V_{GS}=-2.5\text{V}, ID=-15\text{A}$		9.2	12	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance note A	$V_{GS}=-1.8\text{V}, ID=-12\text{A}$		12.3	16	$\text{m}\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_J = 25^\circ\text{C}</math> (unless otherwise stated) note B</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{DS}=-10\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	3500	--	pF
$C_{\text{oss}}$	Output Capacitance		--	577	--	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	445	--	pF
$Q_g$	Total Gate Charge	$V_{DS}=-10\text{V}, ID=-20\text{A}, V_{GS}=-4.5\text{V}$	--	55	--	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	10	--	nC
$Q_{\text{qd}}$	Gate-Drain Charge		--	15	--	nC
<b>Switching Characteristics note B</b>						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{DD}=-10\text{V}, R_L=0.5\Omega, RG=3\Omega, V_{GS}=-4.5\text{V}$	--	18	--	nS
$t_r$	Turn-on Rise Time		--	42	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	85	--	nS
$t_f$	Turn-Off Fall Time		--	23	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_J = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$I_{\text{SD}}$	Source-drain current(Body Diode)	$T_c=25^\circ\text{C}$	--	--	-30	A
$V_{\text{SD}}$	Forward on voltage	$IS=-30\text{A}, V_{GS}=0\text{V}$	--	-0.80	-1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, ISD=-10\text{A}, V_{GS}=0\text{V}$ $di/dt=-100\text{A}/\mu\text{s}$	--	47	--	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		--	53	--	nC

Note:

A: Pulse Test: pulse width  $\leq 300$  us, duty cycle  $\leq 2\%$ 

B: Guaranteed by design, not subject to production testing.

## Typical Electrical and Thermal Characteristics (Curves)

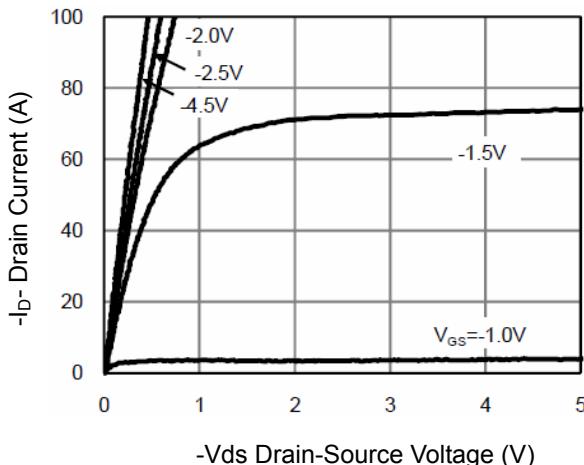


Figure 1 Output Characteristics

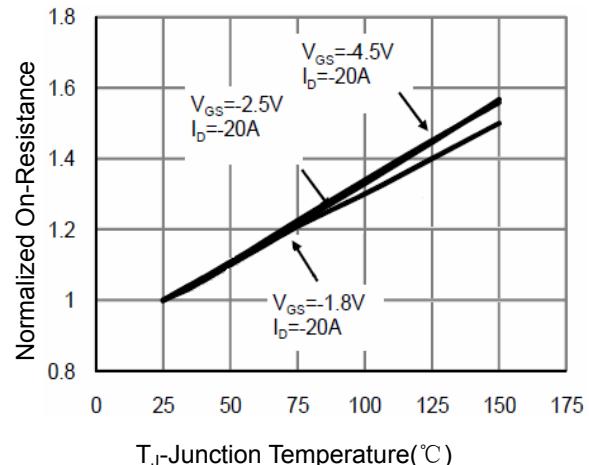


Figure 4 Rdson-Junction Temperature

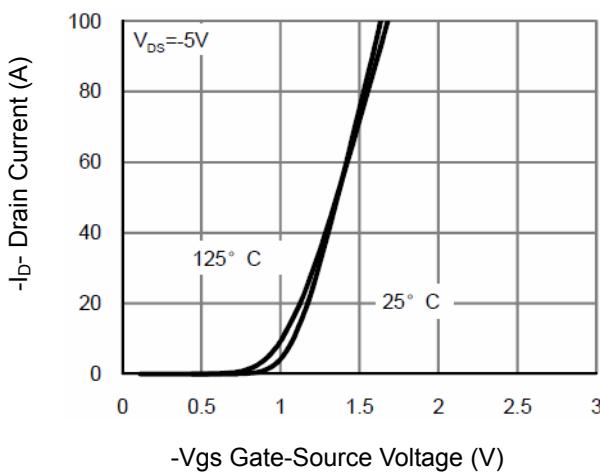


Figure 2 Transfer Characteristics

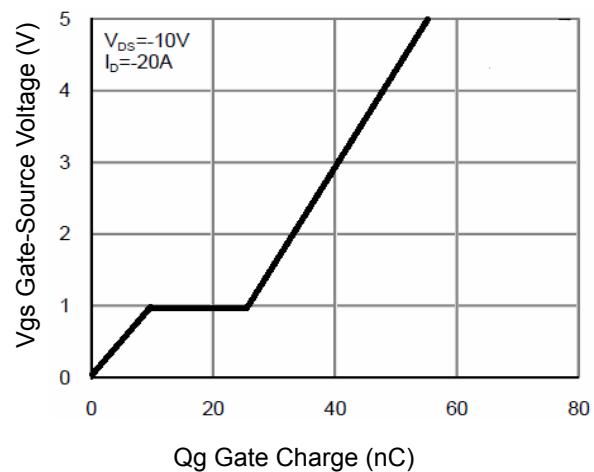


Figure 5 Gate Charge

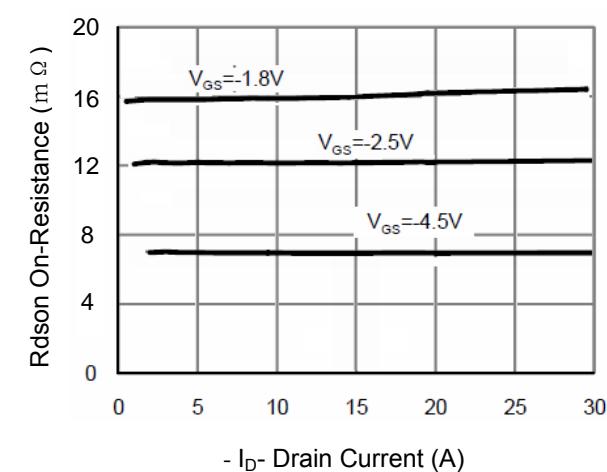


Figure 3 Rdson- Drain Current

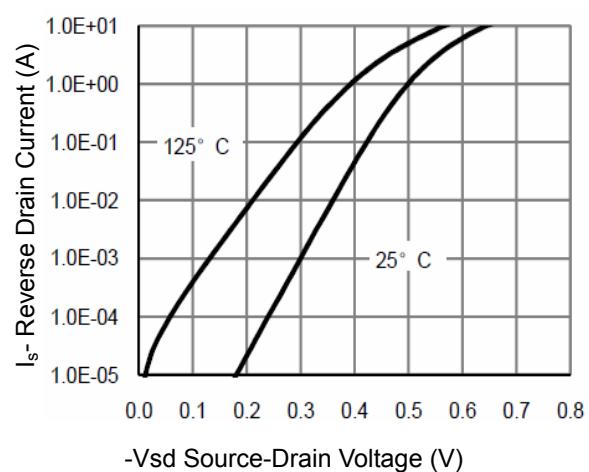
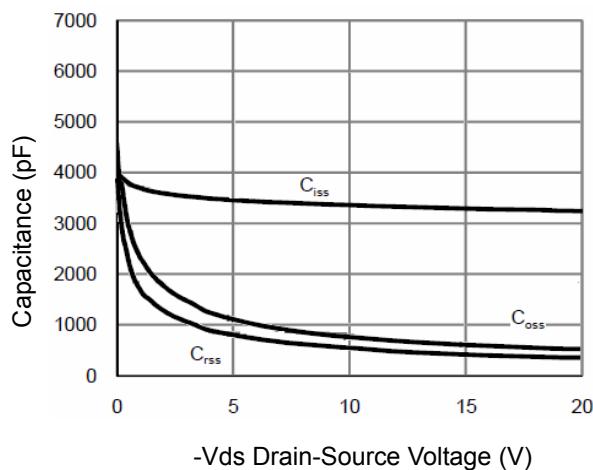
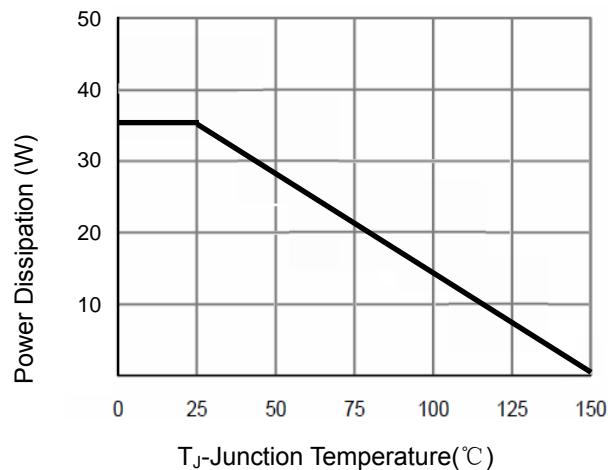
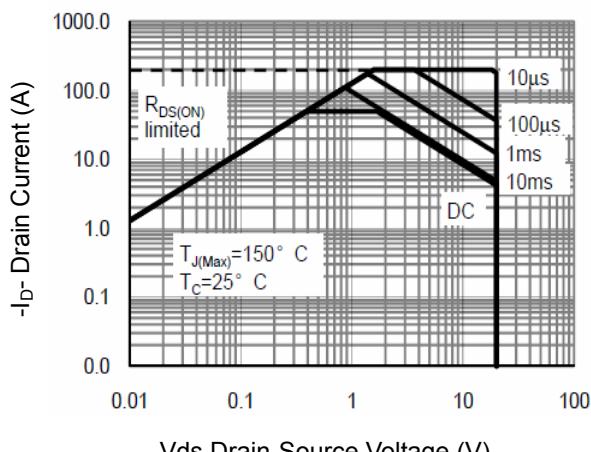
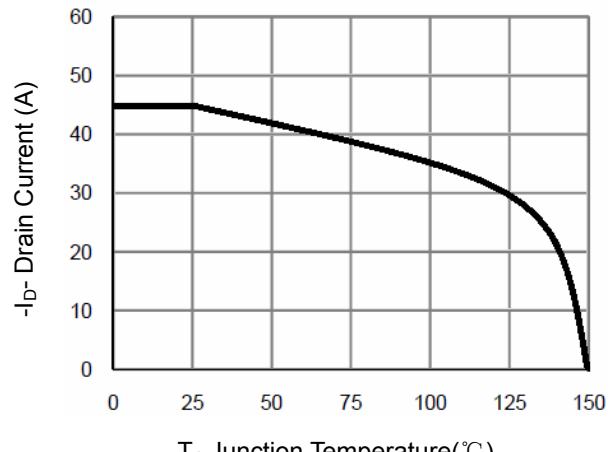


Figure 6 Source- Drain Diode Forward

**Figure 7 Capacitance vs Vds****Figure 9 Power De-rating****Figure 8 Safe Operation Area****Figure 10 -Current De-rating**

# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

***Click to view similar products for MOSFET category:***

***Click to view products by PUOLOP manufacturer:***

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)  
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60\\_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)  
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#)  
[424134F](#) [026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#)  
[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)  
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)