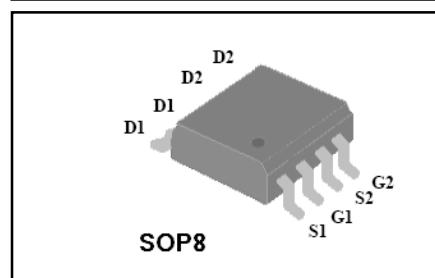
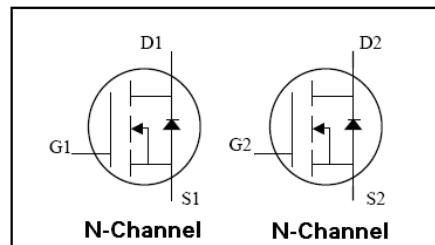


Features

- ◆ $BVDSS > 30V$, $R_{DS(ON)} = 21m\Omega$ (Typ)@ $V_{GS} = 10V$
- ◆ Low On-Resistance
- ◆ Fast Switching
- ◆ Lead-Free, Hg-Free, Green Product

PTS4842 designed by the trench processing techniques to achieve extremely low on-resistance. And fast switching speed and improved transfer effective . These features combine to make this design an extremely efficient and reliable device for variety of DC-DC applications.

Pin Description



Absolute Maximum Ratings ($T_c = 25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{GS}	Gate-Source Voltage	± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	30	V
T_J	Maximum Junction Temperature	150	$^\circ C$
T_{STG}	Storage Temperature Range	-50 to 150	$^\circ C$
I_s	Diode Continuous Forward Current	$T_c = 25^\circ C$	5
			A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested <small>(Note 1)</small>	$T_c = 25^\circ C$	30	A
I_D	Continuous Drain Current($V_{GS} = 10V$)	$T_c = 25^\circ C$	7.7	A
		$T_c = 100^\circ C$	6.5	
P_D	Maximum Power Dissipation	$T_c = 25^\circ C$	2	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		89	$^\circ C/W$

Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=7.7\text{A}$	--	16	21	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=5\text{V}, I_{\text{D}}=5\text{A}$	--	20	30	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	420	--	pF
C_{oss}	Output Capacitance		--	85	--	pF
C_{rss}	Reverse Transfer Capacitance		--	9	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=4\text{A}, V_{\text{GS}}=4.5\text{V}$	--	10.5	--	nC
Q_{gs}	GateSource Charge		--	2.3	--	nC
Q_{gd}	GateDrain Charge		--	3	--	nC

Switching Characteristics

$t_{\text{d}(\text{on})}$	Turnon Delay Time	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=3\text{A}, R_{\text{G}}=3.3\Omega, V_{\text{GS}}=10\text{V}$	--	4.5	--	ns
t_r	Turnon Rise Time		--	3	--	ns
$t_{\text{d}(\text{off})}$	TurnOff Delay Time		-	12	--	ns
t_f	TurnOff Fall Time		--	2	--	ns

Source Drain Diode Characteristics

I_{SD}	Sourcedrain current(Body Diode) ^①	$T_c=25^\circ\text{C}$	--	--	5	A
V_{SD}	Forward on voltage	$T_j=25^\circ\text{C}, I_{\text{SD}}=4\text{A}, V_{\text{GS}}=0\text{V}$	--	0.82	1.2	V

Notes:

① Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

② Pulse width limited by maximum allowable junction temperature

Typical Characteristics

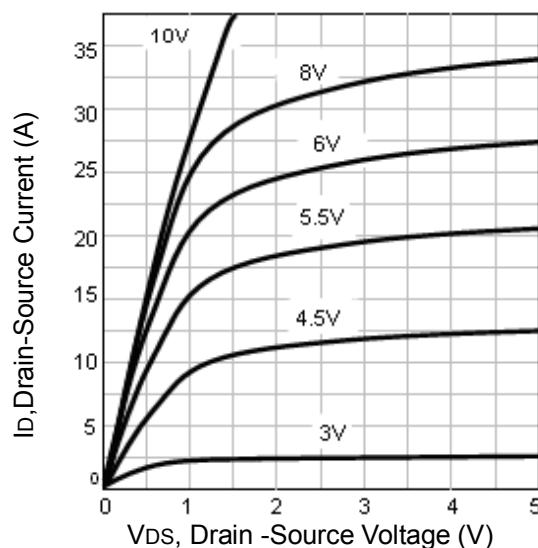


Fig1. Typical Output Characteristics

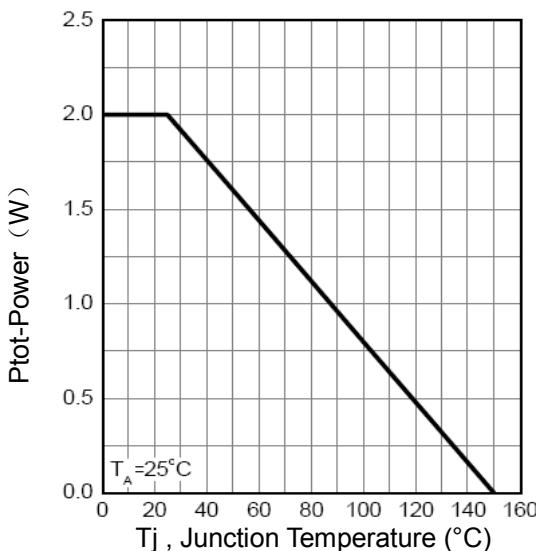


Fig2. Power Dissipation

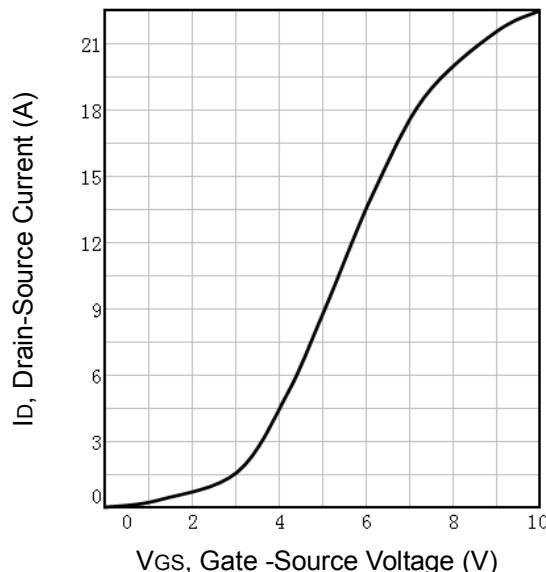


Fig3. Typical Transfer Characteristics

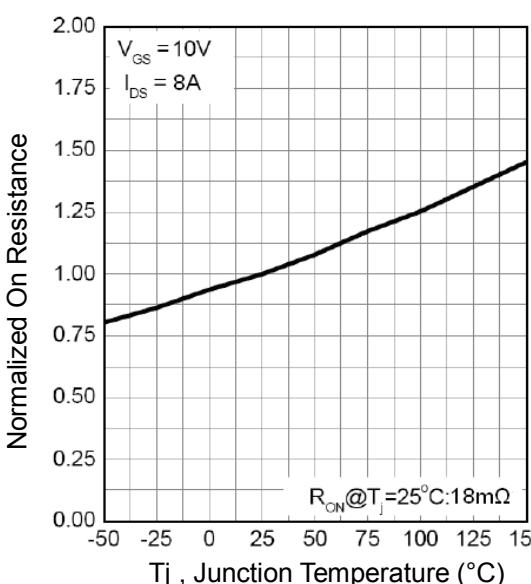


Fig4. Normalized On-Resistance Vs. Temperature

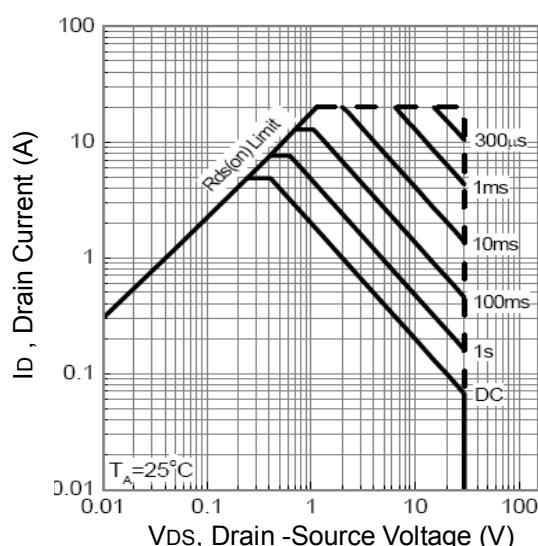


Fig5. Maximum Safe Operating Area

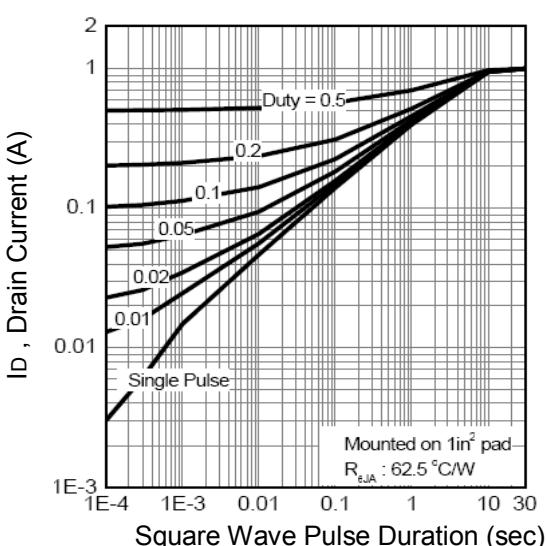


Fig6. Thermal Transient Impedance

Typical Characteristics

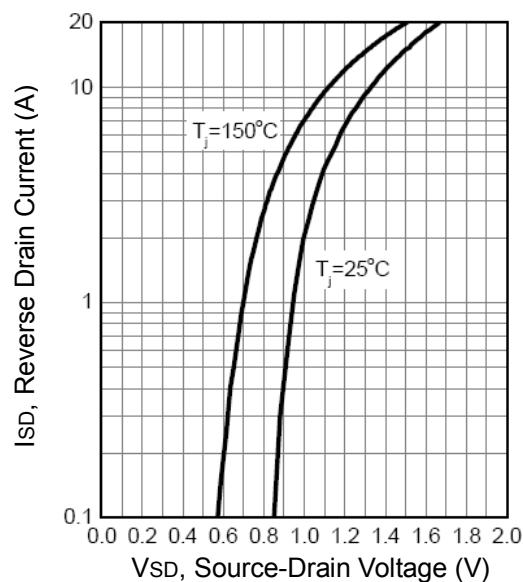


Fig7. Typical Source-Drain Diode Forward Voltage

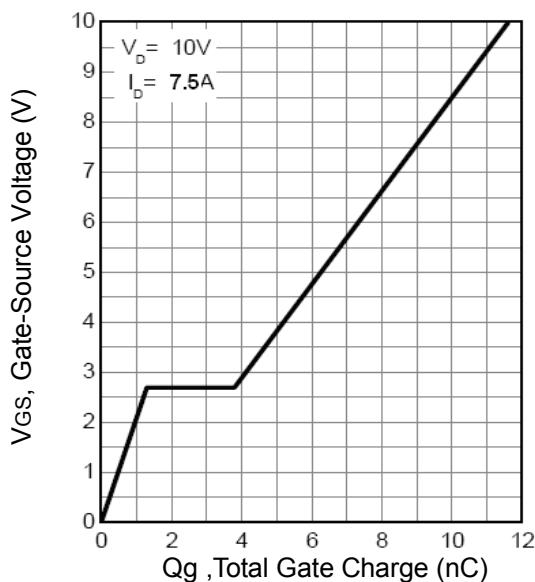


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

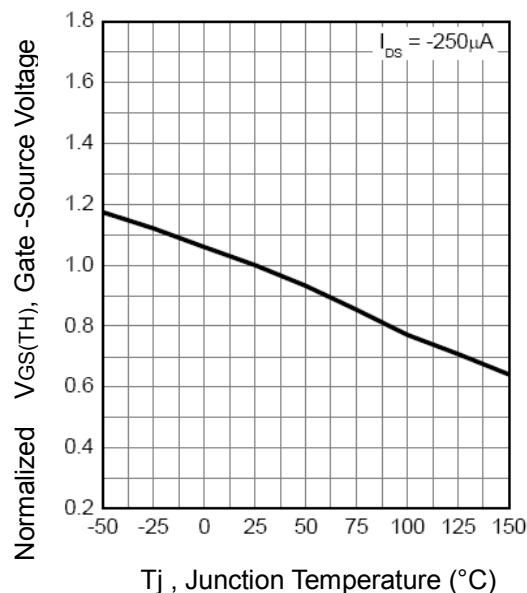


Fig9. Threshold Voltage Vs. Temperature

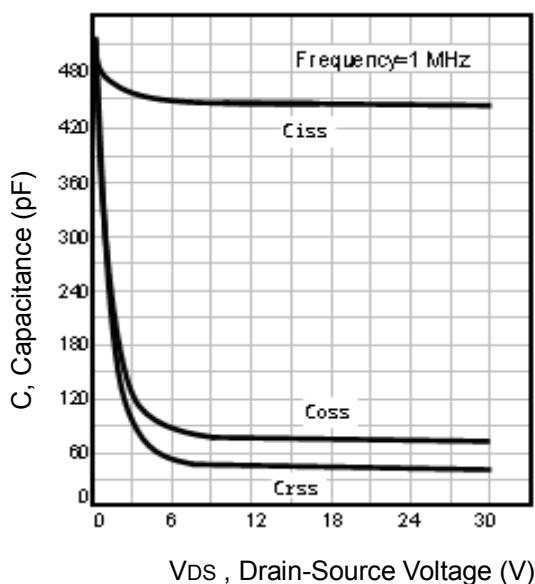


Fig10. Typical Capacitance Vs.Drain-Source Voltage

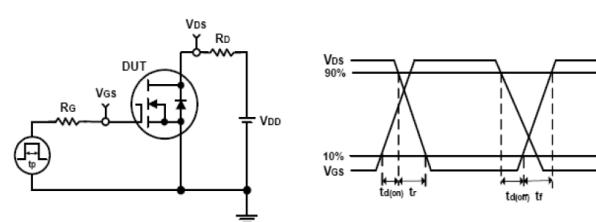


Fig11. Switching Time Test Circuit and waveforms

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