PTP80N60/PTY80N60

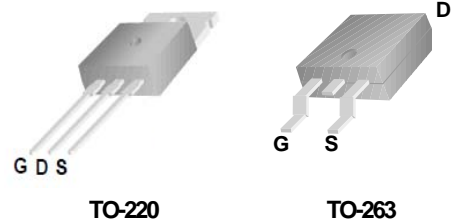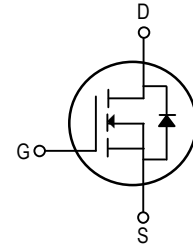## 60V/80A N-Chnnel MOSFET

PUOLOP 迪浦

Features

- 60V/80A
  RDS(ON)=7.3mΩ @ VGS=10V
- Lead free and Green Device Available
- Low Rds-on to Minimize Conductive Loss
- High avalanche Current
- 100% Avalanche Tested

Application

- Power Supply
- DC-DC Converters
- UPS
- Battery Management System

TO-220   **TO-263**

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter		Maximum	Unit
V_{DSS}	Drain-to-Source Voltage		60	V
V_{GSS}	Gate-to-Source Voltage		±25	V
$I_D{}^3$	Continuous Drain Current	T_C=25°C	80	A
		T_C=100°C	66	
$I_{DP}{}^4$	Pulsed Drain Current	T_C=25°C	320	
EAS^5	Avalanche energy		242	mJ
PD	Maximum Power Dissipation	T_C=25°C	125	W
$T_{J,}$ T_{STG}	Junction & Storage Temperature Range		-55~175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
Rθjc	Thermal Resistance-Junction to Case	1.0	°C/W
Rθja	Thermal Resistance-Junction to Ambient	62.5	

Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D=250uA	60	—	—	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=60V,V_{GS}=0V	—	—	1	uA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=250uA	2	3	4	V
I_{GSS}	Gate Leakage Current	V_{GS}=±25V, V_{DS}=0V	—	—	±100	nA
$R_{DS(on)}$[1]	Drain-Source On-Resistance	V_{GS}=10V, I_D=40A	—	7.3	8	mΩ
			—	—	—	
Diode Characteristics						
V_{SD}[1]	Diode Forward Voltage	I_{SD}=40A, $_{GS}$=0V	—	—	1.3	V
Is[3]	Diode Continuous Forward Current		—	—	100	A
t_{rr}	Reverse Recovery Time	I_S=40A,	—	70	—	nS
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/us	—	100	—	nC
Dynamic Characteristics[2]						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=25V Frequency=1MHz	—	3970	—	pF
C_{oss}	Output Capacitance		—	365	—	
C_{rss}	Reverse Transfer Capacitance		—	257	—	
$t_{d(on)}$	Turn-On Delay Time	V_{DD}=34V,I_D=40A, V_{GS}=10V,(Note1,4)	—	57	—	nS
t_r	Rise Time		—	63	—	
$t_{d(off)}$	Turn-Off Delay Time		—	139	—	
t_f	Fall Time		—	50	—	
Gate Charge Characteristics[2]						
Q_g	Total Gate Charge	V_{DD}=48V,I_D=40A, V_{GS}=10V,(Note1,4)	—	91	—	nC
Q_{gs}	Gate-to-Source Charge		—	19	—	
Q_{gd}	Gate-to-Drain Charge		—	30	—	

Note: 1: Pulse test; pulse width $\leqq$ 300us, duty cycle $\leqq$ 2%.

2: Guaranteed by design, not subject to production testing.

3: Package limitation current is 100A.Calculated continuous current based on maximum allowable junction temperature.

4: Repetitive rating, pulse width limited by max junction temperature.

5: Starting TJ = 25°C,L = 1mH,IAS = 22A.

Typical Operating Characteristics

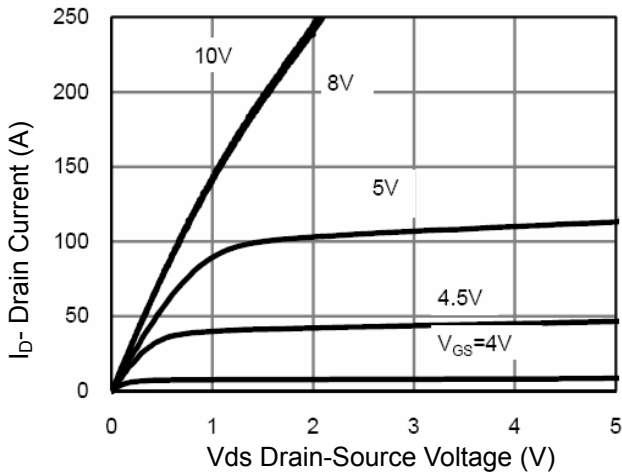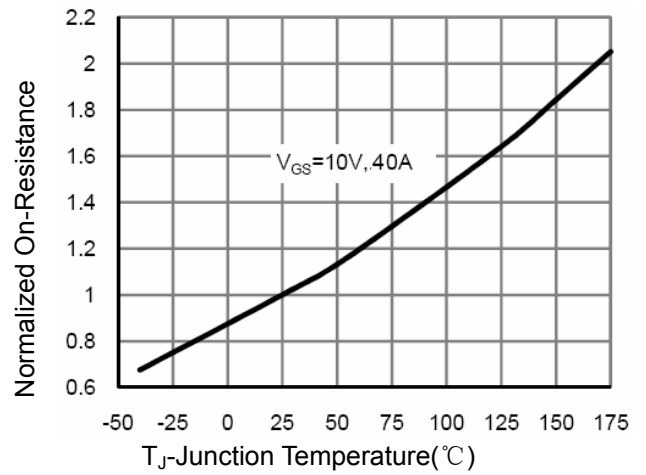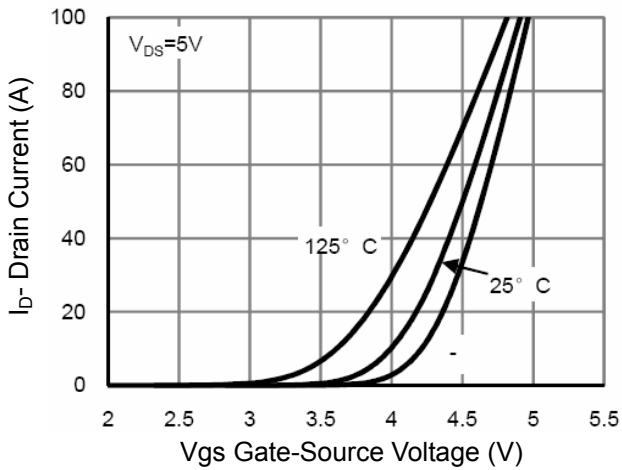**Figure 1 Output Characteristics**

Figure 4 Rdson-JunctionTemperature
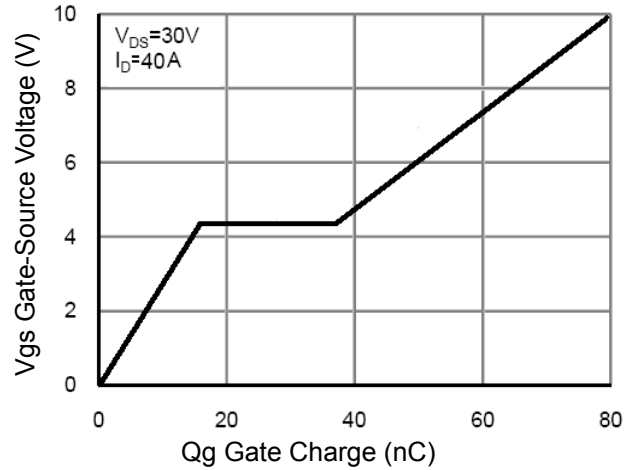

Figure 2 Transfer Characteristics
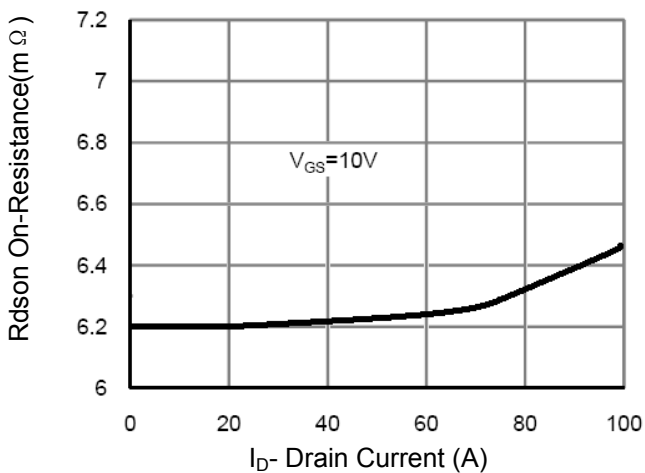

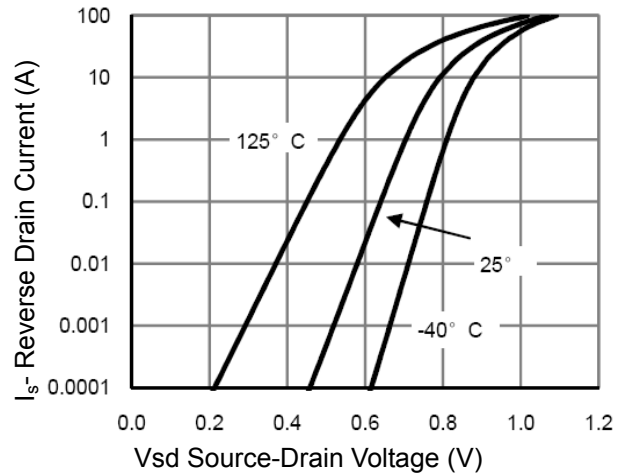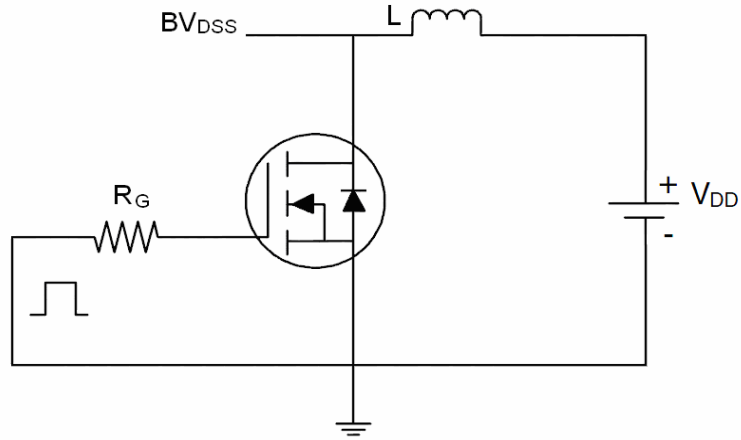**Figure 5 Gate Charge**

Figure 3 Rdson- Drain Current

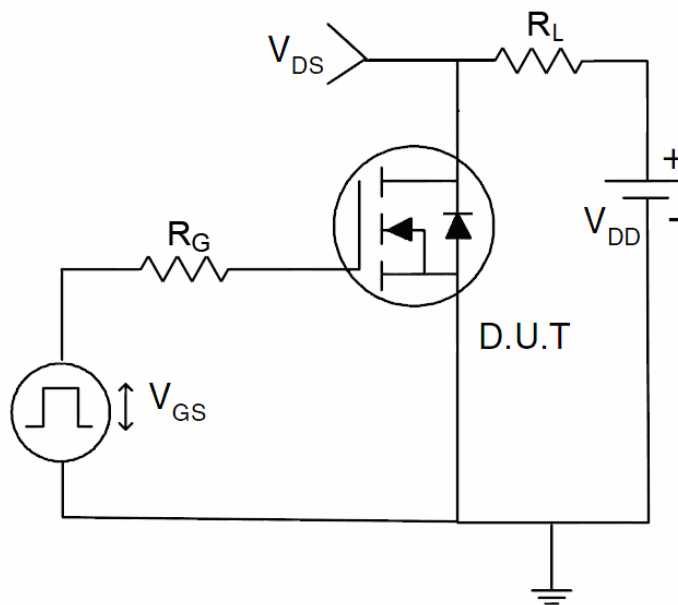

Figure 6 Source- Drain Diode Forward

Test Circuit

1) E$_{AS}$ test Circuit

BV$_{DSS}$ L R$_G$ + V$_{DD}$ -

2) Gate charge test Circuit

0 1K DUT L VCC

3) Switch Time Test Circuit

V$_{DS}$ R$_L$ R$_G$ + V$_{DD}$ - D.U.T V$_{GS}$

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