## $75 \Omega$ SP6T Switch 5 MHz to 2000 MHz

## Product Overview

The QPC6762 is a $75 \Omega$ Silicon on Insulator (SOI) single-pole, six throw (SP6T) switch designed for use in CATV, satellite set top, and other high-performance communications systems. It offers a high isolation symmetric topology with excellent linearity and power handling capability. No blocking caps are necessary on the RF ports. QPC6762 is packaged in a convenient $2.0 \mathrm{~mm} x$ 2.0mm QFN package


14 Pin $2.0 \times 2.0 \mathrm{~mm}$ QFN Package

## Key Features

- 5 MHz to 2000 MHz Operation
- Low Insertion Loss: 0.4 dB at 800 MHz
- No Blocking Caps Required Unless Voltage on RF Line
- High Isolation: 34 dB at 800 MHz
- High Input IP3: 75 dBm at 850 MHz
- 2kV ESD
- +1.8V Logic Compatible
- 3V to 5V Operation


## Applications

- MDU Amplifiers
- Point To Point
- Optical Nodes
- Set Top Box
- PCTV
- Multi-tuner DVR


## Ordering Information

| Part No. | Description |
| :--- | :--- |
| QPC6762SQ | Sample bag with 25 pieces |
| QPC6762SR | 7" Reel with 100 pieces |
| QPC6762TR7 | 7" Reel with 2500 pieces |
| QPC6762PCK | $5-2000 \mathrm{MHz} \mathrm{PCBA} \mathrm{with} 5$ pc. sample bag |

## Absolute Maximum Ratings

| Parameter | Rating |
| :--- | :--- |
| Control Voltage ( $\mathrm{V}_{\mathrm{CTL}}$ ) | 3.0 V |
| Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ) | 6.0 V |
| Maximum CW Input Power at $25^{\circ} \mathrm{C}$ | 32 dBm |
| Max Input Power During Active Switching | 24 dBm |
| Storage Temperature Range | -40 to $+150^{\circ} \mathrm{C}$ |

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

Recommended Operating Conditions

| Parameter | Min |  | Typ |  |
| :--- | :---: | :---: | :---: | :---: |
| Max |  | Units |  |  |
| Supply Voltage, $\mathrm{V}_{\mathrm{DD}}$ | 2.7 | 5 | 5.5 | V |
| Temperature Range | -40 |  | 85 | ${ }^{\circ} \mathrm{C}$ |

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

## Electrical Specifications

| Parameter | Conditions ${ }^{(1)}$ | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Frequency Range |  | 5 |  | 2000 | MHz |
| Insertion Loss <br> (RFC to RFx) | 5 MHz |  | 0.28 |  | dB |
|  | 50 MHz |  | 0.32 |  |  |
|  | 800 MHz |  | 0.40 |  |  |
|  | 1.2 GHz |  | 0.43 |  |  |
|  | 2GHz |  | 0.52 |  |  |
| Isolation (RFC to RFx) | 5 MHz |  | 50 |  | dB |
|  | 50 MHz |  | 60 |  |  |
|  | 800 MHz |  | 34 |  |  |
|  | 1.2 GHz |  | 28 |  |  |
|  | 2GHz |  | 23 |  |  |
| Isolation$(\mathrm{RFx})$ | 5 MHz |  | 55 |  | dB |
|  | 50 MHz |  | 53 |  |  |
|  | 800 MHz |  | 24 |  |  |
|  | 1.2 GHz |  | 25 |  |  |
|  | 2 GHz |  | 17 |  |  |
| Return Loss (RFC) | 5 MHz |  | 48 |  | dB |
|  | 50 MHz |  | 36 |  |  |
|  | 800 MHz |  | 19 |  |  |
|  | 1.2 GHz |  | 23 |  |  |
|  | 2 GHz |  | 15 |  |  |

## Notes:

1. Test Conditions Unless Otherwise Specified: $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=+5 \mathrm{~V}, \mathrm{~V}_{\text {cтL }}=0 /+2.5 \mathrm{~V}, 75 \Omega$ system.

QPC6762
$75 \Omega$ SP6T Switch 5 MHz to 2000 MHz

Electrical Specifications (cont'd.)

| Parameter | Conditions ${ }^{(1)}$ | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input IP3 | $850 \mathrm{MHz}+12 \mathrm{dBm}$ input power per tone, 30 MHz tone spacing |  | 75 |  | dBm |
| Input IP2 | $850 \mathrm{MHz}+12 \mathrm{dBm}$ input power per tone, 30 MHz tone spacing |  | 121 |  |  |
| Input 1dB Compression Point | 850 MHz |  | 37 |  |  |
| Input 0.1dB <br> Compression Point | 850 MHz |  | 33 |  |  |
| MER | 75 dBmV composite at 885 MHz |  | 41.1 |  | dB |
| CCN | 75 dBmV composite at 885 MHz |  | 55.8 |  |  |
| Switching Speed | 10/90\% RF |  | 0.35 |  | $\mu \mathrm{s}$ |
| Switching Speed | 50\% control to 10/90\% RF |  | 1.8 |  |  |
| Turn On Time | Time for VDD $=0 \mathrm{~V}$ to part ON and RF $=90 \%$ |  | 20 |  |  |
| NVG Spurs | $5-30 \mathrm{MHz}$ |  | -117 |  | dBm |
| Harmonics-2nd | 5 MHz |  | -72 |  | dBc |
|  | 50 MHz |  | -76 |  |  |
|  | 850 MHz |  | -130 |  |  |
|  | 1800 MHz |  | -110 |  |  |
| Harmonics-3rd | 5 MHz |  | -93 |  | dBc |
|  | 50 MHz |  | -102 |  |  |
|  | 850 MHz |  | -117 |  |  |
|  | 1800 MHz |  | -106 |  |  |

Notes:

1. Test Conditions Unless Otherwise Specified: $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=+5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 /+2.5 \mathrm{~V}, 75 \Omega$ system. Drive RFC, RFx output.

## Electrical Specifications - Power Supply

| Parameter | Conditions ${ }^{(1)}$ | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Current (IDD) | $V_{D D}=+5.0 \mathrm{~V}$ |  | 65 | 120 | $\mu \mathrm{A}$ |
| Control Current | CTL1, CTL2, CTL3 |  |  | 5 | $\mu \mathrm{A}$ |
| Control Voltage High |  | 1.3 |  | 2.7 | V |
| Control Voltage Low |  | 0 |  | 0.45 | V |

## Power Supply Sequencing Requirements

| Condition | Sequence |
| :--- | :--- |
| Power Up | Turn on VDD, then CTL1, CTL2, and CTL3, then $(20 \mu$ s or greater $)$ apply RF signal |
| Power Down | Turn off RF signal, then CTL1, CTL2, and CTL3, then turn off VDD |

## Truth Table

| Mode | Control Signals <br> CTL2 |  |  |
| :--- | :--- | :--- | :--- |
| RFC to RF1 | High | Low | Low |
| RFC to RF2 | Low | High | Low |
| RFC to RF3 | High | High | Low |
| RFC to RF4 | Low | Low | High |
| RFC to RF5 | High | Low | High |
| RFC to RF6 | Low | High | High |

## 5-2000 MHz Evaluation Board Schematic (QPC6762PCK)



| Ref. Designator | Description | Manufacturer | Part Number |
| :--- | :--- | :--- | :--- |
| PCB | Evaluation Board PCB | Viasystems | QPC6762-4000 |
| U1 | 75ohm SP6T Switch | Qorvo | QPC6762SB |
| J1, J2, J4, J6, J7, J8, J9 | F Connector, Edge Mount, 75 $\Omega, 0.0655^{\prime}$ | Genesis Technology | GT20-300204 |
| R1, R2, R3, R4 | $0 \Omega$ RES, 0402 | Kamaya, Inc | RMC1/16SJPTH |
| R5 | $0 \Omega$ RES, 50mohm max, 0402 | KOA Speer Electronics | RK73Z1ETTP |
| P1 | CONN, HDR, ST, PLRZD, 6-PIN, 0.100" | AMP | 640454-6 |
| C1, C2, C3 | CAP, 100pF, 5\%, 50V, C0G, 0402 | Murata | GRM1555C1H101JA01D |
| C4 | CAP, 10nF, 10\%, 25V, X7R, 0402 | Murata | GRM155R71E103KA01D |
| L1 | IND, 2.2nH, $+/-0.1 n H, ~ M / L, ~ H i-Q, ~ 0201 ~$ | Taiyo Uden | LG HKQ0603W2N2B-T |
| C5 | DNP | N/A | N/A |

## Evaluation Board Assembly (QPC6762PCK)



## Performance Plots

Test conditions unless otherwise noted: $\mathrm{V}_{\mathrm{DD}}=+5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 / 2.5 \mathrm{~V}, \mathrm{Temp}=+25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=75 \Omega$


## Notes:

1. Insertion Loss plots are loss compensated to remove effects of EVB.

## Performance Plots (cont'd.)

Test conditions unless otherwise noted: $\mathrm{V}_{\mathrm{DD}}=+5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 / 2.5 \mathrm{~V}, \mathrm{Temp}=+25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=75 \Omega$







## Performance Plots (cont'd.)

Test conditions unless otherwise noted: $\mathrm{V}_{\mathrm{DD}}=+5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 / 2.5 \mathrm{~V}$, $\mathrm{Temp}=+25^{\circ} \mathrm{C}, \mathrm{Z}_{\mathrm{o}}=75 \Omega$


## MER/CCN Test Conditions:

1. 190 QAM 256 Channels, $57-1215 \mathrm{MHz}$, ITU-T J. 83 , Annex B
2. CCN test procedure according to ANSI/SCTE 17. System BW 5.36 MHz .

## Pin Configuration and Description



Top View

| Pad No. |  | Label |
| :---: | :--- | :--- |
| 1 | CTL3 | Switch Logic Control 3 |
| 2 | CTL2 | Switch Logic Control 2 |
| 3 | CTL1 | Switch Logic Control 1 |
| 4 | VDD | Supply Voltage |
| 5 | RF6 | RF Output Port |
| 6 | RF5 | RF Output Port |
| 7 | RF4 | RF Output Port |
| 8 | GND | Ground |
| 9 | RFC | RF Input Port |
| 10 | GND | Ground |
| 11 | RF1 | RF Output Port |
| 12 | RF2 | RF Output Port |
| 13 | RF3 | RF Output Port |
| 14 | GND | Ground |

## Applications Schematic; 5-2000MHz



Notes:

1. R1-R4, C1-C4 optional for spurious or RFI suppression.
2. Isolation can be optimized by maximizing ground between RF Ports.
3. L1, C5 optional for improving high frequency return loss.

## Package Dimensions



Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. The terminal \#1 identifier and terminal numbering conform to JESD 95-1 SPP-012.
3. Contact plating: NiPdAu

## Package Marking



Pin 1 Indicator
Trace Code to be assigned by SubCon

## Handling Precautions

| Parameter |  | Rating | Standard |
| :--- | :--- | :--- | :--- |
| ESD-Human Body Model (HBM) | Class 2 (2000V) | ANSI/ESD/JEDEC JS-001-2010 |  |
| ESD-Charged Device Model (CDM) | Class C3 (1000V) | JESD22-C101 |  |
| MSL-Moisture Sensitivity Level | MSL2 | JEDEC J-STD-020 |  |

## Solderability

Compatible with both lead-free ( $260^{\circ} \mathrm{C}$ max. reflow temp.) and tin/lead ( $245^{\circ} \mathrm{C}$ max. reflow temp.) soldering processes.
Solder profiles available upon request.
Contact plating: NiPdAu

## RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment). This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A $\left(\mathrm{C}_{15} \mathrm{H}_{12} \mathrm{Br}_{4} \mathrm{O}_{2}\right)$ Free
- PFOS Free
- SVHC Free
- Qorvo Green



## Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:
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