

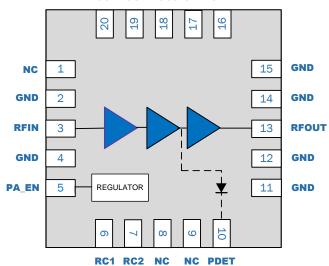
RFMD + TriQuint = Qorvo

RFPA5522

WiFi Integrated PA Module 4.9GHz to 5.925GHz

The RFPA5522 is a three-stage power amplifier (PA) designed for 802.11a/n/ac applications. The integrated input and output 50Ω match greatly reduces the layout area, bill of materials and manufacturability cost in the customer application. The RFPA5522 is manufactured on an advanced InGaP heterojunction bipolar transistor (HBT) process and is capable of achieving linear powers up to 23dBm with an EVM <1.8% while maintaining excellent power added efficiency. The device is provided in a 4.0mm x 4.0mm x 0.9mm laminate package that meets or exceeds the power requirements of IEEE802.11a/n/ac WiFi RF systems.

VCC1 VCC2 VCC3 GND GND



Functional Block Diagram

Ordering Information

RFPA5522SB	Standard 5-piece Sample Bag
RFPA5522SQ	Standard 25-piece Sample Bag
RFPA5522SR	Standard 100-piece Reel
RFPA5522TR13	Standard 2500-piece Reel
RFPA5522PCK-410	Fully Assembled Evaluation Board w/5-piece bag



Package: QFN, 20-pin, 4.0mm x 4.0mm x 0.9mm

Features

- Pout = 23dBm, 5V, 11ac, 80MHz MCS9 @ 1.8% EVM
- P_{OUT} = 25dBm, 5V, 11n, 20/40MHz, MCS7 @ 3% EVM
- Typical Gain = 33dB
- 3.3v Functionality
- High PAE
- Integrated Regulator
- Input and Output Matched to 50Ω
 Integrated Power Detector,
 Harmonic filter, and notch filter

Applications

- Customer Premise Equipment (CPE)
- Wireless Access Points, Gateways
- Routers
- Set-Top Box Applications
- Picocell/Femtocell



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Absolute Maximum Ratings

Parameter	Rating	Unit
DC Supply Voltage	-0.5 to +6	V_{DC}
DC Supply Current	1000	mA
Operating Temperature Range	-40 to +85	۰C
Storage Temperature	-40 to +150	۰C
Maximum TX Input Power into 50Ω for 11a/n/ac (No Damage). $^*R1=0Ω$	+10	dBm
Maximum TX Input Power 10:1 VSWR for 11a/n/ac (No Damage). $^*R1=15\Omega$	+15	dBm
Junction Temperature	+160	С
Moisture Sensitivity Level (260°C JEDEC J-STD-020)	MSL2	

^{*}Note: For R1 placement please refer to the applications schematic



Caution! ESD sensitive device.



RFMD Green: RoHS status based on EU Directive 2011/65/EU (at time of this document revision), halogen free per IEC 61249-2-21, < 1000ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony in solder.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied. This is an InGaP PA designed for high duty cycle applications with Tj>100°C

Nominal Operating Parameters

Dayamatay	Specification			11.5	0.00
Parameter	Min	Тур	Max	Unit	Condition
Compliance					802.11a/n/ac
Operating Frequency	5.180		5.925	GHz	
Extended Operating Frequency	4.900		5.180	GHz	
Power Supply V _{cc}	3.0	5.00	5.25	V	
PA Enable - High	1.7	3.0	3.3	V	
PA Enable - Low	0		0.5	V	
5V Transmit Performance					T= +25°C, V _{CC} =5.0V, V _{PAEN} = 3.0V, Unless otherwise noted
11ac 80MHz Output Power	22	23		dBm	
11ac 80MHz EVM		1.5	1.8	%	MCS9 256QAM
TTAC BUMHZ EVIVI		-36.5	-35	dB	
11ac 160MHz Output Power		22		dBm	
11ac 160MHz EVM			1.8	%	MCS9 256QAM
1100 100101112 E 1101			-35	dB	
11n 20/40MHz Output Power	23.5	25		dBm	
11n 40MHz EVM			3	%	MCS7 64QAM
1 111 40WH 12 E VW			-30.5	dB	
11a Output Power	23.5	25		dBm	
11a DEVM		3	4	%	54Mbps 64QAM
TIA DE VIVI		-30.5	-28	dB	
Gain	31	33		dB	
Gain Variation	-2		2.5	dB	Over operating temp; over 100MHz BW
		5		dBm	P _{OUT} =23dBm; MCS0 160MHz
Margin to Spectral Mask		5		dBm	P _{OUT} =25dBm; MCS0 80MHz
Margin to Spectral Mask		5		dBm	P _{OUT} =26dBm; MCS0 40MHz;
		5		dBm	P _{OUT} =27dBm; MCS0 20MHz;
Operating Current		285	330	mA	P _{OUT} =23dBm
Operating Current		385	430		P _{OUT} =27dBm



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		Specification			Parameter 1	
	Condition	Unit	Max	Тур	Min	Parameter
ss otherwise	T= +25°C, V _{CC} =5.0V, V _{PAEN} = 3.0V, Unless otherw noted					5V Transmit Performance (continued)
		mA		150		Quiescent Current
	PA_EN High	uA	20	4		PA Enable Current
	RF OFF; V _{PAEN} = 0V	uA	5	1		Leakage Current
	Р _{ОИТ} = 27dBm; OFDM 6Mbps	dBm/MHz	-40	-45		Second Harmonic
	'	dBm/MHz	-45	-50		Third Harmonic
	3.3 – 3.8GHz	dB		-5		OOB Gain
	7.0 GHz	dB		7		OOB Gaiii
		dB		12		Input Return Loss
		dB		12		Output Return Loss
	$P_{OUT} = 0dBm$	V		0.25		
	$P_{OUT} = 23dBm$			0.65		Power Detector Range
	$P_{OUT} = 27dBm$	V		0.88		
ss otherwise	T= +25°C, V _{CC} =3.3V, V _{PAEN} = 3.0V, Unless otherw noted					3.3V Transmit Performance
		dBm		19	18	11ac 80MHz Output Power
	MCS9 256QAM	%	1.8	1.5		44 000411 57/84
]	dB	-35	-36.5		11ac 80MHz EVM
		dBm		20	19	11n 20/40MHz Output Power
	MCS7 64QAM	%	3			44 401411 57/04
	1	dB	-30			11n 40MHz EVM
		dBm		18		11ac 160MHz Output Power
	MCS9 256QAM	%	1.8	1.5		
		dB	-35	-36.5		11ac 160MHz EVM
		dB		33	31	Gain
	Over temp; over 100MHz BW	dB	+3.0		-2.5	Gain Variation
	P _{OUT} =21dBm; MCS0 80MHz	dBm		5		
	P _{OUT} =19dBm; MCS0 160MHz	dBm		5		
	P _{OUT} =22dBm; MCS0 40MHz	dBm		5		Margin to Spectral Mask
				5		
		mA	235	210		
	P _{OUT} =23dBm		300	275		Operating Current
		mA		150		Quiescent Current
	PA FN High	uA	500	375		
	- 1 FIGURE -					
	P _{OUT} = 23dBm; OFDM 6Mbps					
			70			
	<u> </u>					
	Pour = 0dBm					at
						Power Detector Range
	P _{OUT} =23dBm; MCS0 20MHz P _{OUT} =19dBm P _{OUT} =23dBm PA_EN High RF OFF; V _{PAEN} = 0V	dBm mA mA		5 210 275 150		Operating Current Quiescent Current PA Enable Current Leakage Current Second Harmonic Third Harmonic Input Return Loss Output Return Loss Power Detector Range

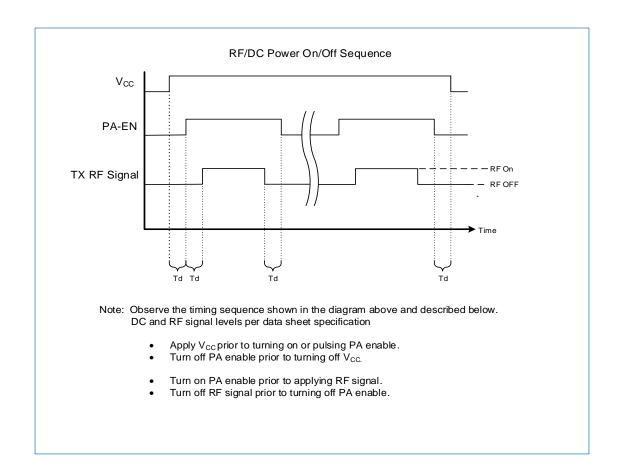


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Parameter	Specification			Unit	Condition
	Min	Тур	Max	Offic	Condition
General Specifications					
Stability	VSWR 4:1; Output Power 0 to 27dBm				CW signal. No spurious above -41.25dBm/MHz for non-harmonic related signals.
Output P1dB		33		dBm	CW signal; 5V
		29		dBm	CW signal; 3.3V
Ramp ON/OFF time		200		nS	10-90% / 90-10% of gain
General Specifications					
Thermal Resistance*		32		°C/W	At Pout=27dBm
ESD HBM	500			V	EIA/JESD22-114A; All pins
ESD CDM	500			V	JESD22-C101C; All pins

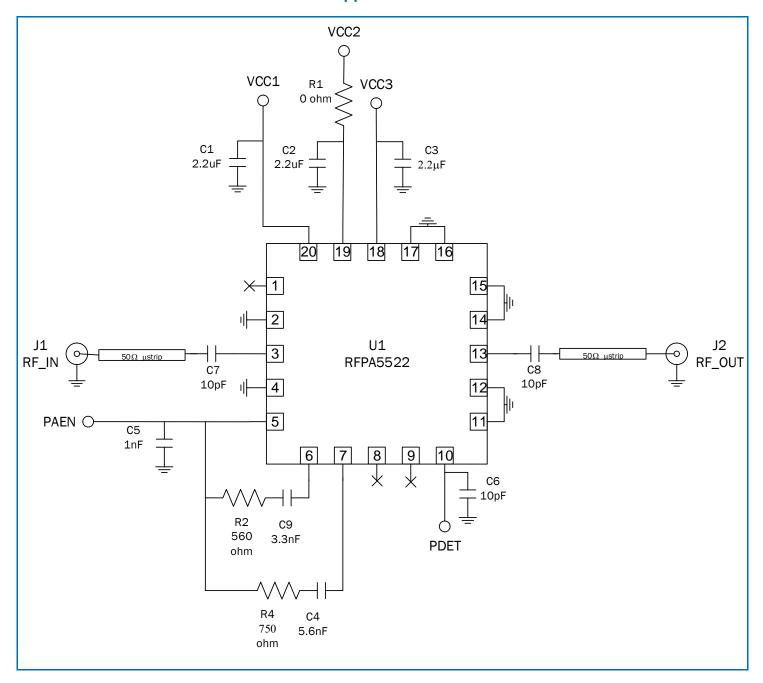


Timing Diagram



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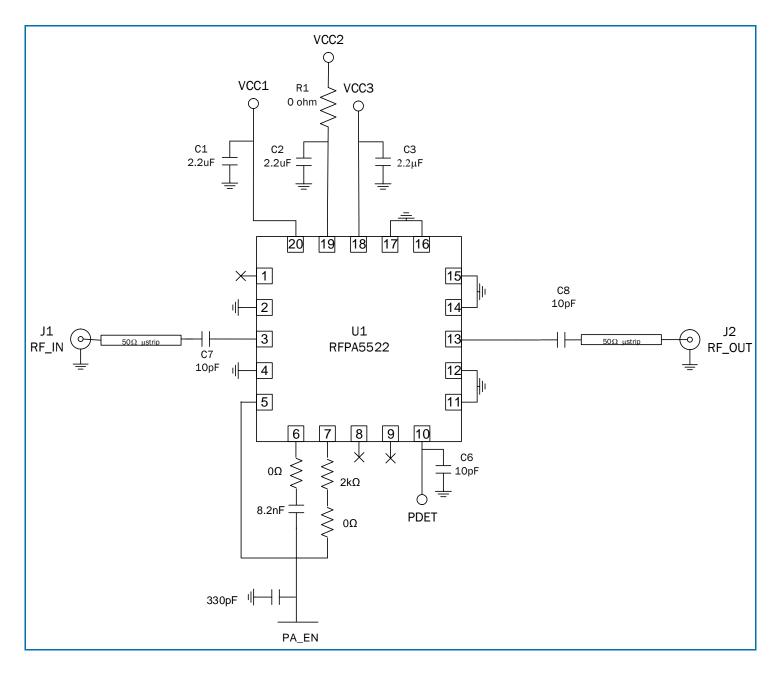
RFPA5522 5V Applications Schematic



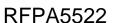
^{*0402} component size required



RFPA5522 3.3V Applications Schematic

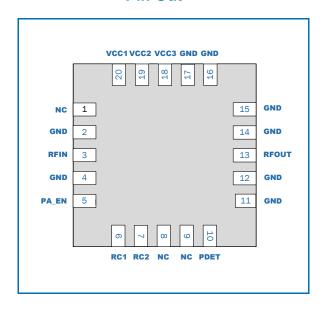


^{*0402} component size required

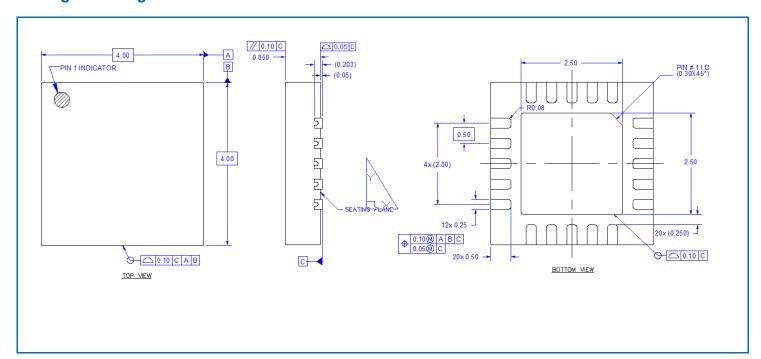




Pin Out

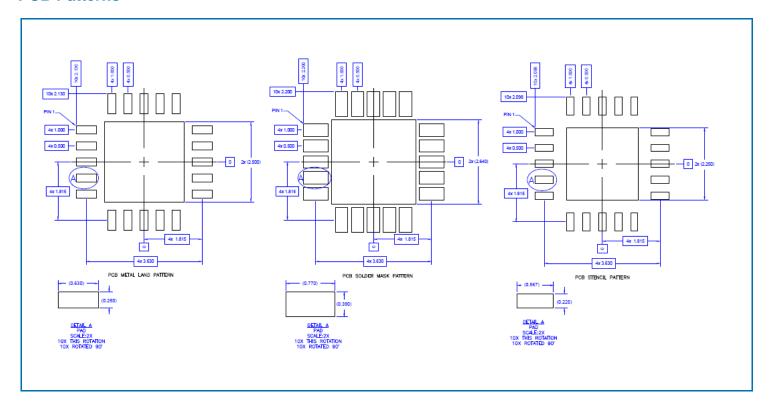


Package Drawing





PCB Patterns





Pin Names and Descriptions

Pin	Name	Description		
1	NC	Not connected internally. It may be left floating or connected to ground.		
2	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
3	RFIN	RF input, internally matched to 50Ω and DC shorted. External DC blocking capacitor required.		
4	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
5	PA_EN	PA Enable pin. Apply $<0.4V_{DC}$. Apply $1.5V_{DC}$ to V_{CC} to enable PA.		
6	RC1	Tuning RC pin 1. See EVB schematic for details.		
7	RC2	Tuning RC pin 2. See EVB schematic for details.		
8	NC	Not connected internally. It may be left floating or connected to ground.		
9	NC	Not connected internally. It may be left floating or connected to ground.		
10	PDET	Power detector. Provides an output voltage proportional to the RF output power level.		
11	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
12	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
13	RFOUT	RF output, internally matched to 50Ω and DC shorted. External DC blocking capacitor required.		
14	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
15	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
16	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
17	GND	Ground connection. This pin is not connected internally and can be left floating or connected to ground.		
18	VCC3	Third stage supply voltage		
19	VCC2	Second stage supply voltage.		
20	VCC1	First stage supply voltage.		
Pkg Base	GND	Ground connection. The back side of the package should be connected to the ground plan though as short of a connection as possible. PCB vias under the device are recommended.		

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