## rfmd

## RFSW6232

## General Purpose Differential 3T Switch 50 MHz to 6000 MHz

The RFSW6232 is a Differential Three Throw Switch that offers very low insertion loss along with excellent linearity performance. It is ideal for filter switching to 6000 MHz . The RFSW6232 is compatible with +1.8 V control logic and is packaged in a compact $2 \mathrm{~mm} \times 2 \mathrm{~mm}$, 18-pin, leadless QFN package which allows for a very small solution size and no requirements for external DC blocking capacitors, unless external DC is applied to the device ports.


Ordering Information

| RFSW6232SQ | Sample bag with 25 pieces |
| :--- | :--- |
| RFSW6232SR | 7" Reel with 100 pieces |
| RFSW6232TR7 | 7" Reel with 2500 pieces |
| RFSW6232PCK-410 | 50MHz to 6000 MHz PCBA with 5-piece sample <br> bag |



Package: QFN, 18-pin, $2.0 \mathrm{~mm} \times 2.0 \mathrm{~mm}$

## Features

- Very Low Insertion Loss and High Isolation
- $<0.5 \mathrm{~dB}$ Insertion Loss 1 GHz
- $>20 \mathrm{~dB}$ Isolation at 2.5 GHz Typical
- Power Handling to 36 dBm
- Low Phase Error $+/ 3^{\circ}$ at 2 GHz
- Multi-Band Operation 50 MHz to 6000MHz
- HBM ESD Rating >2kV on All Ports
- QFN Package, $2 \mathrm{~mm} \times 2 \mathrm{~mm} \times$ $0.55 \mathrm{~mm}, 18$-Pin
- No DC Blocking Capacitors Required in Most Applications


## Applications

- Filter Switching
- Antenna Switching
- Differential Switching


## Absolute Maximum Ratings

| Parameter | Rating | Unit |
| :---: | :---: | :---: |
| $V_{D D}$ | 3.9 | V |
| $\mathrm{C}_{\text {TL1 }}, \mathrm{C}_{\text {TL2 }}$ | 3.0 | dBm |
| Maximum Input Power - Momentary Infrequent Occurrence | $\begin{gathered} +37.5 \text { in } 50 \Omega, 25^{\circ} \mathrm{C} \\ +37 \text { in } 50 \Omega, 90^{\circ} \mathrm{C} \\ +34.5 \text { in } 50 \Omega, 105^{\circ} \mathrm{C} \\ +32.5 \text { in } 6: 1,90^{\circ} \mathrm{C} \\ +30 \text { in } 6: 1,105^{\circ} \mathrm{C} \end{gathered}$ | dBm |
| Maximum Input Power - Continuous Operation | $\begin{gathered} +36 \text { in } 50 \Omega, 25^{\circ} \mathrm{C} \\ +35.5 \text { in } 50 \Omega, 90^{\circ} \mathrm{C} \\ +33 \text { in } 50 \Omega, 105^{\circ} \mathrm{C} \\ +31 \text { in } 6: 1,90^{\circ} \mathrm{C} \\ +28.5 \text { in } 6: 1,105^{\circ} \mathrm{C} \end{gathered}$ | dBm |
| Storage Temperature | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| ESD Rating - Human Body Model (HBM) | 2000 | V |
| Moisture Sensitivity Level | MSL2 |  |



Caution! ESD sensitive device.

Maximum Rating conditions may cause permanent Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

## Recommended Operating Condition

| Parameter | Specification |  |  | Unit |
| :--- | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max |  |
| Operating Temperature Range | -40 | 25 | +105 | ${ }^{\circ} \mathrm{C}$ |
| Operating Junction Temperature |  |  | 125 | ${ }^{\circ} \mathrm{C}$ |
| Switch Supply Voltage | 2.4 | 3.1 | 3.5 | V |

## Nominal Operating Parameters

| Parameter | Specification |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max |  |  |
| 900MHz Specifications |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {Low }}=1.8 \mathrm{~V} / 0 \mathrm{~V}$, Temp $=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| Frequency |  | 900 |  | MHz |  |
| Insertion Loss (P1, P2 or P3 to PC) |  | 0.36 | 0.5 | dB | 900 MHz |
| Isolation |  |  |  |  |  |
| P1 to P2 or P3 | 25 | 28 |  | dB | 900 MHz ; S1, S2, or S3 active |
| P1, P2 or P3 to PC | 25 | 38 |  | dB | 900 MHz ; HI mode active |
| Harmonics |  |  |  |  |  |
| $\mathrm{P} 1, \mathrm{P} 2$ or P3 to PC, 2Fo | -85 | -100 |  | dBc | PIN $=26 \mathrm{dBm}, \mathrm{CW}, 900 \mathrm{MHz}$ |
| $\mathrm{P} 1, \mathrm{P} 2$ or P3 to PC, 3F0 | -85 | -98 |  | dBc |  |
| P1, P2 or P3 to PC, up to 2.75 GHz | -85 | -103 |  | dBc |  |


| Parameter | Specification |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max |  |  |
| 900MHz Specifications Continued |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {Low }}=1.8 \mathrm{~V} / 0 \mathrm{~V}$, Temp $=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| Input IP2 | 110 | 120 |  | dBm | $\mathrm{P}_{\text {IN }}=20 \mathrm{dBm}$ |
| Input IP3 | 65 | 70 |  | dBm |  |
| Return Loss (P1, P2, P3) |  | 24.7 |  | dB | 900 MHz ; Active to PC |
| 2000MHz Specifications |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=\mathbf{2 . 7 V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {Low }}=1.8 \mathrm{~V} / 0 \mathrm{~V}$, $\mathrm{Temp}=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| Frequency |  | 2000 |  | MHz |  |
| Insertion Loss (P1, P2 or P3 to PC) |  | 0.42 | 0.5 | dB | 2GHz; S1, S2 or S3 Active |
| Isolation |  |  |  |  |  |
| P1 to P2 or P3 | 20 | 22.5 |  | dB | 2GHz; S1, S2 or S3 Active |
| P1, P2 or P3 to PC | 20 | 22.5 |  | dB | 2GHz; HI Mode Active |
| Harmonics |  |  |  |  |  |
| $\mathrm{P} 1, \mathrm{P} 2$ or P3 to PC, 2F0 | -85 | -104 |  | dBc | 2 GHz |
| $\mathrm{P} 1, \mathrm{P} 2$ or P3 to PC, 3F0 | -85 | -98 |  | dBc |  |
| P1, P2 or P3 to PC | -85 | -103 |  | dBc |  |
| Input IP2 | 110 | 120 |  | dBm | $\mathrm{PIN}=20 \mathrm{dBm}, 2 \mathrm{GHz}$ |
| Input IP3 | 65 | 70 |  | dBm |  |
| Return Loss |  |  |  |  |  |
| P1A, P2A, P3A |  | 20.9 |  | dB | 2 GHz |
| P1B, P2B, P3B |  | 20.9 |  | dB |  |
| Phase Balance (P1, P2, or P3PC) | -3 | 1.4 | +3 | Deg | Test at 1980MHz |
| 2500MHz Specifications |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=\mathbf{2 . 7} \mathrm{V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {LOW }}=1.8 \mathrm{~V} / 0 \mathrm{~V}$, Temp $=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| Frequency |  | 2500 |  | MHz |  |
| Insertion Loss (P1, P2 or P3 to PC) |  | 0.45 | 0.55 | dB | 2500MHz; S1, S2 or S3 Active |
| Isolation |  |  |  |  |  |
| P1 to P2 or P3 | 16 | 20.7 |  | dB | 2500 MHz ; S1, S2 or S3 Active |
| P1, P2 or P3 to PCA | 16 | 21 |  | dB | 2500 MHz ; HI Mode Active |
| Harmonics |  |  |  |  |  |
| $\mathrm{P} 1, \mathrm{P} 2$ or P3 to PC, $2 \mathrm{~F}_{0}$ | -85 | -98 |  | dBc | 2500MHz |
| $\mathrm{P} 1, \mathrm{P} 2$ or P 3 to $\mathrm{PC}, 3 \mathrm{~F}_{0}$ | -85 | -102 |  | dBc |  |
| P1, P2 or P3 to PC, up to 12.75 GHz | -85 | -103 |  | dBc |  |
| Input IP2 - UMTS Mode | 110 | 115 |  | dBm | $\mathrm{PIN}=20 \mathrm{dBm}, 2500 \mathrm{MHz}$ |

## rimd

| Parameter | Specification |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max |  |  |
| 2500MHz Specifications Continued |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {Low }}=1.8 \mathrm{~V} / 0 \mathrm{~V}$, Temp $=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| Input IP3 - UMTS Mode | 65 | 70 |  | dBm | $\mathrm{PIN}=20 \mathrm{dBm}, 2500 \mathrm{MHz}$ |
| Return Loss (P1, P2, P3) |  | 24.7 |  | dB | 2500 MHz |
| DC Control and Electrical Specifications |  |  |  |  | Nominal Conditions Unless Otherwise Stated, $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$, $\mathrm{V}_{\text {HIGH }} / \mathrm{V}_{\text {Low }}=1.8 \mathrm{~V} / \mathrm{OV}$, Temp $=25^{\circ} \mathrm{C}, 50 \Omega$, Single-ended |
| VDD Supply Current | 50 | 75 | 100 | $\mu \mathrm{A}$ | S1, S2 or S3 Mode active |
| $\mathrm{C}_{\text {TL1 }}$, CTL2 - Control Voltage High | 1.3 |  | 2.7 | V |  |
| $\mathrm{C}_{\text {TL1 }}, \mathrm{C}_{\text {TL2 }}$ - Control Voltage Low | 0 |  | 0.45 | V |  |
| Control Current |  |  | 1 | $\mu \mathrm{A}$ |  |
| Switching Speed |  | 3 | 5 | $\mu \mathrm{s}$ | 10\% to 90\% RF |

Notes:

1. The DP3T switch is controlled by CTL1, CTL2. The poles are synchronized such that each is connected to the same port number.
2. Operation of switch may be delayed up to 100 microseconds after initial application of VDD supply, under extreme cold conditions.

Mode Control Table

| Mode | CTL1 | CTL2 | Test Condition |
| :---: | :---: | :---: | :---: |
| P1A - PCA | High | Low | S1 |
| P1B - PCB |  |  |  |
| P2A - PCA | Low | Low | S2 |
| P2B - PCB |  |  |  |
| P3A - PCA | Low | High | S3 |
| P3B - PCB |  |  |  |
| PCA - PCB (High Isolation) | High | High | HI |

Note: HI mode connects a termination across PCA-PCB and disconnects P1, P2 and P3 paths.

Low Frequency Linearity Table

| Frequency | $\mathbf{2 . 5 M H z}$ | 5 MHz | 25 MHz | 50 MHz |
| :---: | :---: | :---: | :---: | :---: |
| IP3 | 55 dBm | 57 dBm | 64 dBm | 67 dBm |
| IP2 | 74 dBm | 80 dBM | 86 dBm | 90 dBm |

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Typical Performance: $\mathrm{T}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ unless otherwise noted



Evaluation Board Schematic 50 MHz to 6000 MHz Application Circuit


Evaluation Board Bill of Materials (BOM) 50MHz to 6000MHz Application Circuit

| Description | Reference Designator | Manufacturer | Manufacturer's P/N |
| :--- | :---: | :---: | :---: |
| RFSW6232-410(A) Evaluation Board |  | Viasystems | RFSW6232-410(A) |
| General Purpose Differential 3T <br> Switch | U1 | RFMD | RFSW6232 |
| CAP, 10000pF, 10\%, 25V, X7R, 0402 | C1 | Murata Electronics | GRM155R71E103KA01D |
| RES, 02, 0402 | R1 | Kamaya, Inc. | RMC1/16SJPTH |
| RES, 1K, 5\%, 1/16W, 0402 | R2-R3 | Kamaya, Inc. | RMC1/16S-102JTH |
| CAP, 100pF, 5\%, 50V, C0G, 0402, <br> DNI | C2-C3 | Taiyo Yuden (USA), Inc. | RM UMK105CG101JV-F |
| CONN, SMA, END LNCH, MINI, FLT, <br> $0.068 "$ | PCA, PCB, P1A, P1B, <br> P2A, P2B, P3A, P3B | Emerson Network Power | 142-0741-851 |
| CONN, HDR, ST, PLRZD, 5-PIN, <br> $0.100 " ~$ | P1 | ITW Pancon | MPSS100-5-C |

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Evaluation Board Assembly Drawing


Pin Names and Descriptions

| Pin | Name |  |
| :--- | :--- | :--- |
| 1 | GND | RF and DC Ground |
| 2 | GND | RF and DC Ground |
| 3 | P2A | RF Path 2 Pole A |
| 4 | P2B | RF Path 2 Pole B |
| 5 | GND | RF and DC Ground |
| 6 | P1B | RF Path 1 Pole B |
| 7 | P1A | RF Path 1 Pole A |
| 8 | GND | RF and DC Ground |
| 9 | GND | RF and DC Ground |
| 10 | GND | RF and DC Ground |
| 11 | PCB | Antenna Common Pole B |
| 12 | VDD | Antenna Common Pole A |
| 13 | GND | RF Power Supply Input |
| 14 | CTL2 | Control Logic Line 2 |
| 15 | CTL1 | Control Logic Line 1 |
| 16 | P3B | RF Path 3 Pole B |
| 17 | P3A | RF Path 3 Pole A |
| 18 | GND | RF and DC Ground |
| PKG BASE |  |  |

## Pin Out Top View



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Package Outline (Dimensions in millimeters)


## Branding Diagram



Pin 1 Indicator
Trace Code to be assigned by SubCon

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BGS1414MN20E6327XTSA1 BGS1515MN20E6327XTSA1 BGSA11GN10E6327XTSA1 BGSX28MA18E6327XTSA1 HMC199AMS8
HMC986A SKY13374-397LF SKY13453-385LF CG2430X1-C2 CG2415M6-C2 HMC986A-SX SW-314-PIN UPG2162T5N-E2-A
SKY13416-485LF MASWSS0204TR-3000 MASWSS0201TR MASWSS0181TR-3000 MASW-007588-TR3000 MASW-004103-13655P MASW-003102-13590G MASWSS0202TR-3000 MA4SW310B-1 MA4SW110 SW-313-PIN CG2430X1 SKY13321-360LF SKY13405490LF BGSF 18DM20 E6327 SKY13415-485LF MMS008PP3 BGS13PN10E6327XTSA1 SKY13319-374LF BGS14PN10E6327XTSA1 SKY12213-478LF SKY13404-466LF MASW-011060-TR0500 SKYA21024

